

Continuous-Control-Set Model Predictive Control-Based Energy Routing With Over/Under Voltage Protection

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Abstract—Multiactive-bridge (MAB) converters are highly promising as energy routers in renewable energy applications. Their multiple-input multiple-output control challenges can be effectively addressed by a model predictive control (MPC) approach, which minimizes the interference between different ports and simplifies the controller design. However, during the optimization process, the input and output constraints are usually ignored to simplify the implementation, which may degrade the control performance and lead to large voltage overshoot/undershoot when the constraints are violated. Meanwhile, solving the constrained MPC problem within a short sampling time poses a huge computational challenge. To address these issues, in this article a continuous-control-set model predictive control (CCS-MPC) scheme is developed for the MAB converters, which incorporates input and output constraints to achieve over/under voltage protection. An MPC problem with multiple constraints is formulated and converted into a quadratic programming (QP) problem. Its real-time implementation on a low-cost microcontroller unit is further discussed including QP solver deployment, real-time certification, and delay compensation. Finally, the developed CCS-MPC scheme is experimentally verified by a four-port 500 W energy router prototype and well demonstrates the over/under voltage protection capability.

Index Terms—Continuous-control-set model predictive control (CCS-MPC), multiactive-bridge (MAB) converter, over/under voltage protection (OVP/UVP), quadratic programming (QP).

I. INTRODUCTION

MULTIACTIVE bridge (MAB) converters have attracted much attention in recent decades due to their advantages, such as galvanic isolation, bidirectional power flow, reduced number of magnetic components, and high integration and power density. They have a potential to become energy routers in

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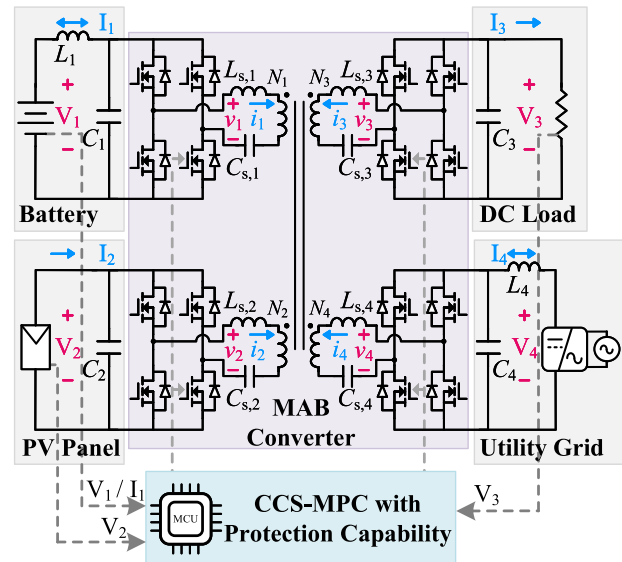


Fig. 1. Example configuration of an MAB converter under CCS-MPC controller with protection capability.

dc microgrids, battery storage systems, photovoltaic (PV) power plants, and many other multiport power conversion applications [1]. Fig. 1 shows the configuration of an example four-port MAB converter that enables energy routing between battery, PV panel, dc load, and utility grid. A multiwinding transformer interconnects various energy sources and loads through multiple H-bridges and their auxiliary networks. This configuration improves system efficiency, integration, and power density. However, the single transformer also introduces cross-coupling of power flows and complicates the control of the MAB converters, which is essentially a multiple-input multiple-output (MIMO) control problem.

The most popular approach currently is to decompose the original MIMO control system into several independent single-input single-output (SISO) control loops and combine a decoupling solution to mitigate the cross-coupling effect. Existing decoupling solutions can be roughly divided into two categories: hardware-based and algorithm-based ones. A straightforward hardware-based solution is to remove the series inductance (e.g., $L_{s,i}$ in Fig. 1) at a specific port such that the voltage and flux variations are clamped and the cross-coupling can be inherently eliminated [2], [3], [4]. A winding arrangement method has been

TABLE I
COMPARISON OF RECENT QP SOLVER-BASED CCS-MPC SCHEMES IN POWER ELECTRONICS APPLICATIONS

Ref.	Application	QP solver	Control Platform	Sampling Time	Horizon Length	Output Constraints
[18]	Load-commutated inverter-fed synchronous machines	qpOASES	AC 800PEC (ABB's industrial controller)	1 ms	10	No
[19]	Induction motor drives	qpOASES	dSPACE MicroLabBox	100 μ s	2	No
[20]	Synchronous motor drives	qpOASES	dSPACE MicroLabBox	100 μ s	3	No
[21]	Reluctance synchronous machines	qpOASES	dSPACE MicroLabBox	250 μ s	2	No
[22]	Medium-voltage drives	Sphere decoding	dSPACE DS1106	125 μ s	1-4	No
[23, 24]	Voltage forming grid inverters	–	dSPACE SCALEXIO	100 μ s	2	Yes
[25]	Permanent magnet Synchronous machines	Primal-dual interior-point	dSPACE DS1007	125 & 100 μ s	2	Yes
[26]	Neutral point clamped converters	qpOASES	Zynq UltraScale+MPSoc (2 ARM + 1 FPGA)	666 μ s	4	Yes
This work	MAB converters	OSQP	STM32H723VGT6 (Cortex-M7 ARM)	200 μ s	3	Yes

proposed to minimize the leakage inductance of a multiwinding transformer [5]. However, these hardware-based decoupling solutions increase the component stress and design complexity of the converter and transformer. In contrast, multiple algorithm-based decoupling solutions have also been proposed without modifying the circuit hardware topology. A widely adopted one is to introduce the inverse matrix of the MIMO model to suppress the cross-coupling between the single SISO control loops. Based on this natural idea, various advanced SISO control methods have been developed [6], [7], [8]. As the number of converter ports increases, these algorithm-based solutions may face increasing design complexity since the number of SISO controllers and the dimension of the inverse matrix will expand accordingly.

Besides, several MIMO control solutions for the MAB converters have also been reported. An artificial neural network was applied to calculate the optimal duty cycle of a triple-active-bridge converter [9]. A feedforward neural network was developed to predict the control variables required to achieve the target power flow [10]. These data-driven solutions determine the control actions in an open-loop manner. Therefore, feedback controllers in the outer loops are still required to ensure stability. A model predictive control (MPC) has also been proposed as a MIMO controller, in which an explicit control law is derived based on solving an optimization problem offline [11]. However, it lacks the ability to handle input and output constraints. In particular, incorporating output constraints is critical to achieve a safe and reliable operation. For instance, it prevents large voltage peaks during transients, which could trigger under voltage/over voltage lockout of connected devices or even cause component damage or failure [12]. It is worth noting that this aspect has not been discussed in the above-mentioned literature. At the same time, MPC has been applied to dual-active-bridge (DAB) converters (i.e., converters with two ports) to minimize current stress and transient dc offset, where its constrained optimization problem can be solved offline using the Karush–Kuhn–Tucker conditions [13], [14], [15]. However, as the number of variables and constraints increases substantially in the MAB converters, it becomes increasingly difficult to obtain an analytical solution offline.

In the field of power electronics, MPC has become increasingly popular in the past decade. It can be classified into two categories, namely finite-control-set MPC (FCS-MPC) and

continuous-control-set MPC (CCS-MPC) [16]. FCS-MPC directly determines the optimal switching signals by enumerating the predicted behavior of all possible states, thereby eliminating the need for an external modulator. Due to its intuitive concept and straightforward implementation, it is widely used in the control of motor drives and grid-tied inverters [17]. In contrast, CCS-MPC solves a quadratic programming (QP) problem to obtain optimal control variables, which are then converted into switching signals through a modulator, such as carrier-based PWM or space vector PWM [16]. Input and output constraints can be included when formulating the QP problem. However, solving the QP problem in real time is particularly challenging due to its large computational burden and short sampling time of power converters. Compared to FCS-MPC, CCS-MPC remains unexplored in many power electronics applications.

As summarized in Table I, in recent years, a variety of CCS-MPC schemes based on optimization solvers, mostly the open-source qpOASES, have been reported for motor drives and neutral-point clamped converters [18], [19], [20], [21], [22], [23], [24], [25], [26]. However, most of the reported CCS-MPC implementations rely on high-performance but bulky platforms, such as dSPACE MicroLabBox or industrial computers, with processor speeds up to several GHz. Note that all the references cited in Table I included experimental validation. For the on-site applications of the MAB converters, such as multiport energy routers, it is important to reflect practical constraints and achieve a short sampling time with a low-cost embedded control platform, such as a microcontroller unit (MCU). A systematic effort on the constrained MPC problem formulation and fast real-time implementation is highly expected, particularly for the MAB converters. Therefore, this article aims to develop a new CCS-MPC scheme with over/under voltage protection (OVP/UVP) capability for MIMO control of the MAB converters and implement it in real-time on a low-cost MCU as fast as possible. The main contributions of this article include the following:

- 1) A systematic CCS-MPC MIMO control scheme for the MAB converters that include necessary input and output constraints required for OVP/UVP purposes.
- 2) A low-cost and fast implementation of the developed CCS-MPC control, including a new QP solver OSQP deployment and real-time certification.

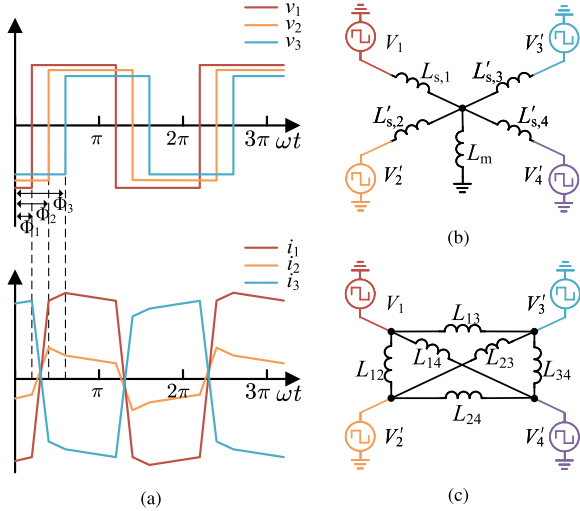


Fig. 2. Waveforms and equivalent circuits of an MAB converter. (a) Voltage and current waveforms. (b) “Y-type” equivalent circuit. (c) “ Δ -type” equivalent circuit.

- 3) A four-port prototype energy router with an embedded MPC controller running on a mainstream commercial MCU and demonstrating fast real-time implementation and OVP/UVF capability.

The rest of this article is organized as follows. Section II describes the formulation of CCS-MPC-based MIMO control problem, especially the reflection of the input and output constraints in the MAB converters; Section III focuses on the fast real-time implementation of the developed control scheme over a low-cost control platform, such as an MCU; Section IV gives simulation results as preliminary theoretical verification; Section V experimentally validates the control scheme and its real-time implementation using a prototype four-port energy router. Finally, Section VI concludes this article.

II. PROBLEM FORMULATION

For the reliable operation of MAB converters, it is essential to satisfy practical constraints on both input and output variables. The theoretical aspects of the CCS-MPC problem formulation are first discussed, with particular emphasis on the handling of these practical constraints, which will guide the following low-cost real-time implementation.

A. Predictive Model

The CCS-MPC control of the MAB converters first requires a predictive model. Fig. 1 shows the topology of an example four-port MAB converter for energy routing purposes. The four H-bridge modules are driven by phase-shifted gate signals with a 50% duty cycle, generating square voltage waves with phase differences between each port. The piecewise-linear currents are then excited in the transformer windings, as shown in Fig. 2(a). Note that under the single-phase-shift modulation that dominates in the MAB converters, there are no other operation modes [1]. The power of each port of the MAB converter P_i can be expressed as follows:

$$P_i = \sum_{j \neq i}^4 \frac{V_i V_j}{2\pi f_{sw} L_{ij} N_i N_j} (\Phi_i - \Phi_j) \left(1 - \frac{|\Phi_i - \Phi_j|}{\pi}\right) \quad (1)$$

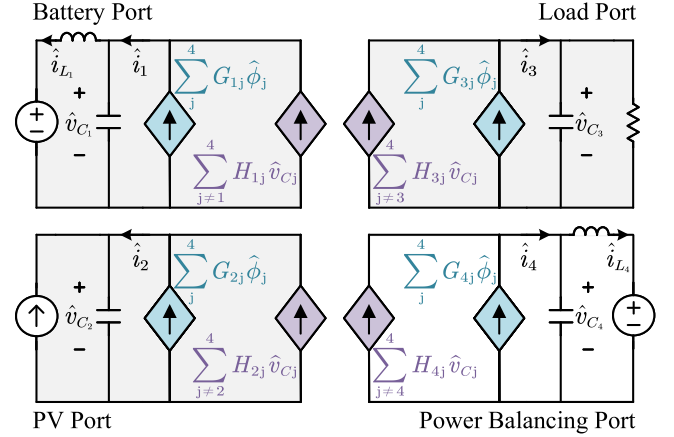


Fig. 3. Small signal model of the example four-port MAB converter.

where f_{sw} is the switching frequency, V_i , N_i , and Φ_i are the dc voltage, number of transformer turns, and phase shift angle of the i th port, respectively. L_{ij} represents the link inductor between port i and j in the Δ -type equivalent circuit, which is calculated from the Y-type equivalent circuit, as shown in Fig. 2(b) and (c). The expressions of $L_{s,i}$ and L_{ij} are defined in Appendix A.

Since the series inductor currents and series capacitor voltage consist of pure ac components with zero average value, these series components ($L_{s,1-4}$ and $C_{s,1-4}$) are ignored in the small signal model based on the reduced-order method. This simplification has demonstrated high accuracy in similar applications, such as the DAB converters [27]. Fig. 3 shows the small signal model of the above-mentioned four-port MAB converter, in which G_{ij} and H_{ij} are the small signal gains from port j to port i , referring to Appendix B [2], [11]. The connected sources/loads, i.e., the battery, PV panel, and dc load, are modeled as voltage source, current source, and resistive load, respectively. Here, the fourth port is designated as a power balancing port, whose phase shift angle is constantly zero to provide a reference. Based on the small signal model in Fig. 3, a MIMO predictive model can then be derived in the form of state-space equations

$$\begin{aligned} \dot{\mathbf{x}} &= \mathbf{A}\mathbf{x} + \mathbf{B}\mathbf{u} \\ \mathbf{y} &= \mathbf{C}\mathbf{x} \end{aligned} \quad (2)$$

where state variables \mathbf{x} are the states of storage components (i.e., dc inductor currents \hat{i}_{L_1} and dc capacitor voltages $\hat{v}_{C_1}, \hat{v}_{C_2}, \hat{v}_{C_3}$); input variables (or control variables) \mathbf{u} are the phase shift angles of the first three ports ($\hat{\phi}_1, \hat{\phi}_2, \hat{\phi}_3$); and battery current (\hat{i}_{L_1}), PV panel voltage (\hat{v}_{C_2}), and load voltage (\hat{v}_{C_3}) are chosen as three output variables, namely control targets

$$\begin{aligned} \mathbf{x} &= [x_1, x_2, x_3, x_4]^T = [\hat{i}_{L_1}, \hat{v}_{C_1}, \hat{v}_{C_2}, \hat{v}_{C_3}]^T \\ \mathbf{u} &= [u_1, u_2, u_3]^T = [\hat{\phi}_1, \hat{\phi}_2, \hat{\phi}_3]^T \\ \mathbf{y} &= [y_1, y_2, y_3]^T = [\hat{i}_{L_1}, \hat{v}_{C_2}, \hat{v}_{C_3}]^T. \end{aligned} \quad (3)$$

Note that since batteries operate in constant current (CC) mode most of the time, here the battery current is specifically chosen as an output variable [28]. System matrices $\{\mathbf{A}, \mathbf{B}, \mathbf{C}\}$

in (2) are derived based on the Kirchoff's law

$$\begin{aligned}
 A &= \begin{bmatrix} 0 & 1/L_1 & 0 & 0 \\ -1/C_1 & 0 & H_{12}/C_1 & H_{13}/C_1 \\ 0 & H_{21}/C_2 & 0 & H_{23}/C_2 \\ 0 & H_{31}/C_3 & H_{32}/C_3 & -1/(C_3 R_3) \end{bmatrix} \\
 B &= \begin{bmatrix} 0 & 0 & 0 \\ G_{11}/C_1 & G_{12}/C_1 & G_{13}/C_1 \\ G_{21}/C_2 & G_{22}/C_2 & G_{23}/C_2 \\ G_{31}/C_3 & G_{32}/C_3 & G_{33}/C_3 \end{bmatrix} \\
 C &= \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}. \quad (4)
 \end{aligned}$$

For a digital implementation, the continuous-time predictive model (2) must be discretized with sampling time T_s , such as through exact discretization method

$$\begin{aligned}
 \Delta \mathbf{x}(k+1) &= G \Delta \mathbf{x}(k) + H \Delta \mathbf{u}(k) \\
 \Delta \mathbf{y}(k) &= C \Delta \mathbf{x}(k) \quad (5)
 \end{aligned}$$

where $G = e^{AT_s}$, $H = (\int_0^{T_s} e^{A\tau} d\tau)B$, and $\Delta = 1 - z^{-1}$ is a difference operator. Thus, the output variables at the next j step, $\vec{y}(k+j|k)$ can be predicted in an iterative manner. Here, the vector form of the predicted output variables within a predictive horizon N_p , denoted as $\vec{Y}(k)$, is expressed as follows:

$$\begin{aligned}
 \vec{Y}(k) &= [\vec{y}(k+1|k)^T \vec{y}(k+2|k)^T \cdots \vec{y}(k+N_p|k)^T]^T \\
 &= L \Delta \mathbf{U}(k) + F \Delta \mathbf{x}(k) + \mathbf{Y}_0(k) \quad (6)
 \end{aligned}$$

where matrices L and F are defined in Appendix C, and $\mathbf{Y}_0(k)$ and $\Delta \mathbf{U}(k)$ are vectors of present output variables and assumed control increments along the predictive horizon, respectively

$$\begin{aligned}
 \mathbf{Y}_0(k) &= [\mathbf{y}(k)^T \mathbf{y}(k)^T \cdots \mathbf{y}(k)^T]^T \\
 \Delta \mathbf{U}(k) &= [\Delta \mathbf{u}(k)^T \Delta \mathbf{u}(k+1)^T \cdots \Delta \mathbf{u}(k+N_p-1)^T]^T. \quad (7)
 \end{aligned}$$

B. Formulation of Constrained CCS-MPC Problem

The MPC controller needs to solve an optimization problem online within each sampling interval to obtain a control action $\Delta \mathbf{u}^*(k)$. Here, the cost function J of the optimization problem is defined as the weighted sum of squares of predicted errors and control increments along the horizon N_p , namely

$$J = \sum_{i=1}^{N_p} \|\vec{y}(k+i) - \mathbf{w}(k+i)\|_Q^2 + \|\Delta \mathbf{u}(k+i-1)\|_R^2 \quad (8)$$

where $\vec{y}(k+i)$, $\mathbf{w}(k+i)$, and $\Delta \mathbf{u}(k+i)$ are the predicted outputs, output references, and control increments at the next i step, respectively. Q and R are weighting factor matrices of the predicted errors and control increments (refer to Appendix D).

In the deadbeat predictive control, the analytical solution of the optimization problem is derived offline by directly solving $dJ/d\Delta \mathbf{U}(k) = 0$, neglecting the constraints on input and output variables [11]. However, the developed CCS-MPC scheme here

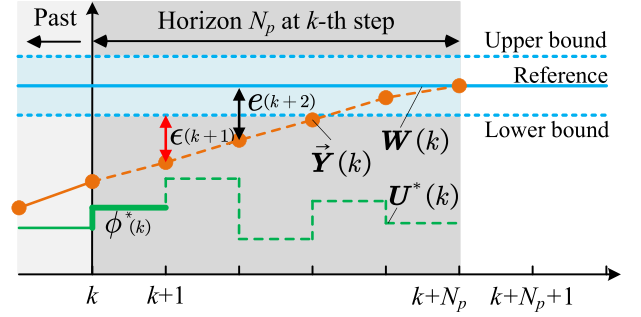


Fig. 4. Illustration of soft constraint on output variables at k th instant.

for the MAB converters further incorporates constraints on both input and output variables. First, the phase shift angles of MAB converters must be strictly bounded within a range of $[-\pi/2, \pi/2]$ to guarantee a monotonic relationship between the output power and phase shift angle at each port [refer to P_i in (1)]. This requires a hard constraint imposed on the control variables \mathbf{u} over the predictive horizon N_p

$$-\pi/2 \leq \mathbf{u}(k+i) \leq \pi/2, \quad i \in (0, N_p - 1). \quad (9)$$

At the same time, to ensure safety operation and protection, constraints on the output variables should also be reflected over the horizon N_p . For instance, constraints on the dc load voltage \vec{y}_3 are imposed as

$$V_{\text{load,min}} \leq \vec{y}_3(k+i) \leq V_{\text{load,max}}, \quad i \in (1, N_p). \quad (10)$$

But if these output constraints are imposed as strict boundaries that can never be violated, the optimization problem itself may become infeasible [29]. To address this challenge, such constraints have to be softened in practical applications, permitting occasional violations only when really necessary. A possible solution is to introduce a nonnegative slack variable ϵ . This variable particularly represents the degree of constraint violation. For instance, $\epsilon(k+1)$ in Fig. 4 is the quantities of constraint violation of $\vec{y}(k+1)$. This slack variable ϵ should be penalized in the cost function J , forcing its value to be as close to zero as possible. As an example, the constraint on the dc load voltage in (10) can be softened as

$$\begin{aligned}
 V_{\text{load,min}} - \epsilon_3(k+i) &\leq \vec{y}_3(k+i) \leq V_{\text{load,max}} + \epsilon_3(k+i) \\
 \epsilon_3(k+i) &\geq 0. \quad (11)
 \end{aligned}$$

Without loss of generality, constraints on the other output variables, i.e., battery current and PV panel voltage, are similarly introduced and softened. Although they are relatively less restrictive in the present application, as discussed further in Section IV. These soft constraints on the output variables at time instant $k+i$ can be rearranged in a vector form

$$K \epsilon(k+i) \geq M \vec{y}(k+i) + \mathbf{c} \quad (12)$$

where $\epsilon(k+i) = [\epsilon_1(k+i), \epsilon_2(k+i), \epsilon_3(k+i)]$ is the vector of slack variables, and matrix K , M , and vector \mathbf{c} are defined in Appendix E.

As explained previously, to avoid the violation of these output constraints in (12) as much as possible, a quadratic penalty for slack variables $\|\epsilon(k+i)\|_S^2$ can be added into the cost function J , where S is the weighting matrix of slack variables:

$S = \text{diag}\{s_1, s_2, s_3\}$. Then, a final optimization problem is formulated as follows:

$$\begin{aligned} \min_{\Delta \mathbf{u}, \epsilon} J &= \sum_{i=1}^{N_p} \|\bar{\mathbf{y}}(k+i) - \mathbf{w}(k+i)\|_Q^2 \\ &\quad + \|\Delta \mathbf{u}(k+i-1)\|_R^2 + \|\epsilon(k+i)\|_S^2 \\ \text{s.t. } \Delta \mathbf{x}(k+i+1) &= G\Delta \mathbf{x}(k+i) + H\Delta \mathbf{u}(k+i) \\ \Delta \mathbf{y}(k+i) &= C\Delta \mathbf{x}(k+i) \\ \Delta \mathbf{u}(k+i) &= \mathbf{u}(k+i) - \mathbf{u}(k+i-1) \\ -\pi/2 &\leq \mathbf{u}(k+i-1) \leq \pi/2 \\ K\epsilon(k+i) &\geq M\bar{\mathbf{y}}(k+i) + \mathbf{c} \\ i &\in (1, N_p). \end{aligned} \quad (13)$$

The above-mentioned optimization problem in (13) is solved at each time instant to obtain an optimal control sequence $\Delta \mathbf{U}^*(k)$. In this control sequence, only the optimal control variable at present time instant $\Delta \mathbf{u}^*(k)$ is actually implemented [29]

$$\Delta \mathbf{u}^*(k) = [\mathbf{I}_3, \mathbf{0}_3, \dots, \mathbf{0}_3] \Delta \mathbf{U}^*(k) \quad (14)$$

where \mathbf{I}_3 and $\mathbf{0}_3$ are 3×3 identity matrix and zero matrix, respectively. At the next time instant, the horizon N_p recedes by one step, and the above-mentioned optimization is repeated based on new measurements. Note that the above-mentioned MPC problem is formulated in an incremental structure, which implicitly integrates tracking errors and thus ensures the steady-state accuracy.

III. LOW-COST IMPLEMENTATION

As mentioned in the introduction section, solving the optimization problem (13) online within a short sampling interval is quite computationally challenging. To realize real-time implementation of the above-mentioned developed CCS-MPC scheme on a low-cost control platform, such as MCUs, the following three aspects are especially discussed, including 1) problem reformulation, 2) solver selection, and 3) modification for real-time certification.

A. Conversion to a QP Problem

Due to the quadratic form of the cost function J , the optimization problem in (13) can be converted to a condensed QP form, which is more efficient for the embedded implementation. Similar to the representation of $\bar{\mathbf{Y}}(k)$ and $\Delta \mathbf{U}(k)$ in (7), the control variables, slack variables, and output references along the predictive horizon N_p can all be represented by a vector form

$$\begin{aligned} \mathbf{U}(k) &= [\mathbf{u}(k)^T \mathbf{u}(k+1)^T \dots \mathbf{u}(k+N_p-1)^T]^T \\ \mathbf{E}(k) &= [\epsilon(k+1)^T \epsilon(k+2)^T \dots \epsilon(k+N_p)^T]^T \\ \mathbf{W}(k) &= [\mathbf{w}(k+1)^T \mathbf{w}(k+2)^T \dots \mathbf{w}(k+N_p)^T]^T. \end{aligned}$$

Then, the cost function J in (13) can also be converted to a compact vector form through a few algebraic manipulations

$$\begin{aligned} J &= [\bar{\mathbf{Y}}(k) - \mathbf{W}(k)]^T \mathcal{Q} [\bar{\mathbf{Y}}(k) - \mathbf{W}(k)] \\ &\quad + \Delta \mathbf{U}(k)^T \mathcal{R} \Delta \mathbf{U}(k) + \mathbf{E}(k)^T \mathcal{S} \mathbf{E}(k). \end{aligned} \quad (15)$$

Accordingly, based on the definition of $\mathbf{E}(k)$ and $\bar{\mathbf{Y}}(k)$, the soft constraints (12) along the predictive horizon N_p is modified as

$$\mathcal{K}\mathbf{E}(k) \geq \mathcal{M}\bar{\mathbf{Y}}(k) + \bar{\mathbf{c}} \quad (16)$$

where \mathcal{Q} , \mathcal{R} , and \mathcal{S} are block diagonal matrices of weighting matrices Q , R , and S , while \mathcal{K} and \mathcal{M} are block diagonal matrices of K and M . The detailed expressions of these matrices and vector $\bar{\mathbf{c}}$ are defined in Appendix F.

Similarly, since the optimization variables in (13) are control increments $\Delta \mathbf{U}(k)$, the hard constraints (9) on the phase shift angles $\mathbf{U}(k)$ should also be modified with respect to $\Delta \mathbf{U}(k)$

$$-\pi/2 - \mathbf{U}_0(k-1) \leq \mathcal{N}\Delta \mathbf{U}(k) \leq \pi/2 - \mathbf{U}_0(k-1) \quad (17)$$

where \mathcal{N} are defined in Appendix F, and $\mathbf{U}_0(k-1)$ is the vector of control variables employed at previous time instance $k-1$

$$\mathbf{U}_0(k-1) = [\mathbf{u}(k-1)^T \mathbf{u}(k-1)^T \dots \mathbf{u}(k-1)^T]^T.$$

As mentioned previously, the slack variables $\mathbf{E}(k)$ is augmented into the optimization variables, i.e., $\Delta \tilde{\mathbf{U}}(k) = [\Delta \mathbf{U}(k)^T, \mathbf{E}(k)^T]^T$. Then, by substituting $\bar{\mathbf{Y}}(k)$ in the modified cost function (15) and soft constraints (16) with the expression in (6), the original optimization problem can be converted to a QP problem with respect to the augmented optimization variables $\Delta \tilde{\mathbf{U}}(k)$

$$\begin{aligned} \min_{\Delta \tilde{\mathbf{U}}(k)} J &= \Delta \tilde{\mathbf{U}}(k)^T P \Delta \tilde{\mathbf{U}}(k) + 2q \Delta \tilde{\mathbf{U}}(k) \\ \text{s.t. } lb &\leq \mathcal{A} \Delta \tilde{\mathbf{U}}(k) \leq ub \end{aligned} \quad (18)$$

where the quadratic cost matrix P , linear cost vector q , constraint matrix \mathcal{A} , lower bound lb , and upper bound ub are defined as follows:

$$\begin{aligned} P &= \begin{bmatrix} L^T \mathcal{Q} L + \mathcal{R} & \mathbf{0}_{3N_p} \\ \mathbf{0}_{3N_p} & \mathcal{S} \end{bmatrix} \quad \mathcal{A} = \begin{bmatrix} \mathcal{N} & \mathbf{0}_{3N_p} \\ \mathcal{M} L & -\mathcal{K} \end{bmatrix} \\ q &= [F \Delta \mathbf{x}(k) + \mathbf{Y}_0(k) - \mathbf{W}(k)]^T \mathcal{Q} L, \mathbf{0}_{1 \times 3N_p} \\ lb &= \left[\left[-\frac{\pi}{2} - \mathbf{U}_0(k-1) \right]^T, [-\infty_{1 \times 9N_p}]^T \right]^T \\ ub &= \left[\left[\frac{\pi}{2} - \mathbf{U}_0(k-1) \right]^T, [-\mathcal{M} F \Delta \mathbf{x}(k) - \mathcal{M} \mathbf{Y}_0(k) - \bar{\mathbf{c}}]^T \right]^T. \end{aligned} \quad (19)$$

Note that $\mathbf{0}_{1 \times 3N_p}$ represents a zero row vector with a dimension of $3N_p$.

B. Embedded Platform and Solver

The above-mentioned converted QP problem should be solved at each sampling instant. Benefiting from the development of numerical solvers in recent years, a variety of open source and commercial QP solvers have been developed and successfully deployed in the MPC applications. The most popular QP solver in the field of power electronics is qpOASES, which can solve QP problems in a range of hundreds of microseconds and has a user-friendly interface for Simulink and dSPACE MicroLab-Box [30]. For instance, most reported motor drive CCS-MPC schemes deployed the QP solver qpOASES on dSPACE MicroLab-Box for real-time implementation [18], [19], [20], [21].

However, in actual applications, it is usually desirable to realize a low-cost embedded implementation of the developed

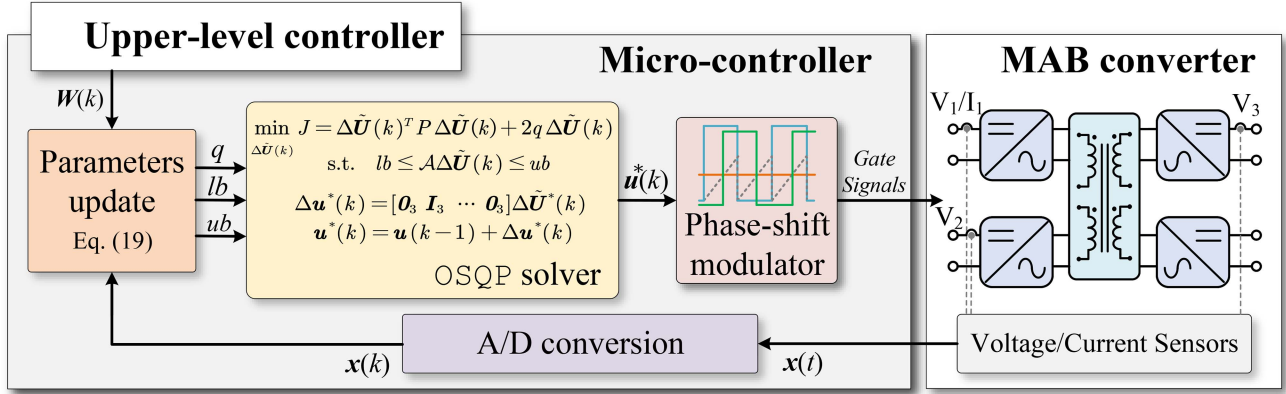


Fig. 5. Control diagram of the developed CCS-MPC scheme for MAB converters.

CCS-MPC schemes, such as on a commercial MCU. Here, a new QP solver OSQP is applied to solve the problem (18) in real-time, which is based on the efficient alternating direction method of multipliers (ADMMs) [31]. The ADMM is particularly effective for large-scale QP problems with multiple constraints, such as (18). It decomposes a complex problem into simpler subproblems that can be solved more efficiently. In contrast, the classical active set (AS) method in qpOASES requires frequent re-evaluation of the active constraints, which may become inefficient when dealing with a large number of constraints. The code generation tool of OSQP also makes it particularly suitable for the present low-cost embedded implementation problem, as it can generate compact C codes for a specific QP problem [32].

Fig. 5 shows the control diagram of the developed CCS-MPC scheme for the MAB converters. As a core of the control scheme, OSQP is responsible for solving the QP problem with varying input parameters at each time instant, namely q , lb , and ub . From (19), it can be seen that the quadratic cost matrix P and constraint matrix A remain unchanged at different time instants. The varying parameters q , lb , and ub depend on the real-time measured voltages and currents [i.e., $\Delta x(k)$ and $Y_0(k)$], thereby need to be constantly updated before the QP solving. This makes the problem in (18) a parametric program, as required by the OSQP solver. The matrices P and A can be precomputed offline to accelerate the implementation of the solver, and the resulting algorithm is division-free [32]. As shown in Fig. 5, with the updated input parameters, OSQP solves the optimal control variables $\Delta u^*(k)$ and sends them to the phase-shift modulator to generate gate signals.

C. Real-Time Certification and Delay Compensation

In the OSQP solver, determining the optimal control variables $\Delta \tilde{u}(k)$ requires multiple ADMM iterations. The number of iterations depends on multiple factors, including the size of the QP problem, initial iteration point, and convergence tolerance level. By default, the iteration process terminates once the computed residual falls below a specified tolerance level, i.e., the convergence. However, this termination criterion leads to a varying iteration number and execution duration at different time instants, which may cause the MPC execution duration T_{mpc} to exceed an expected sampling interval T_s , thereby violating the real-time requirement, as shown in Fig. 6(a). The worst-case

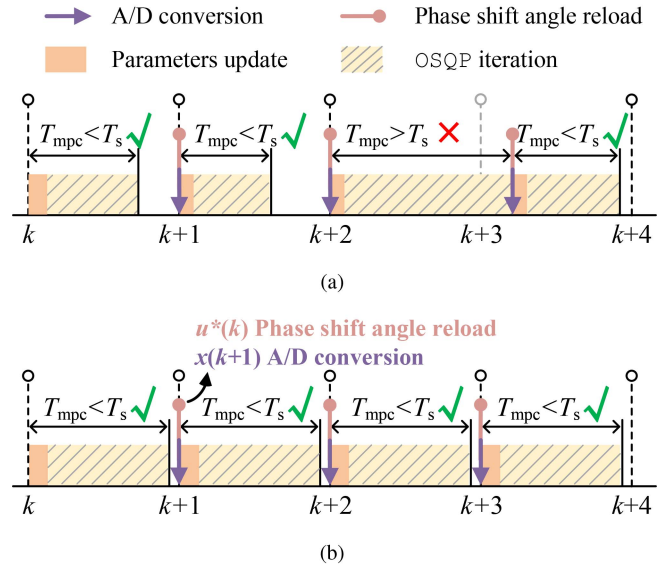


Fig. 6. Graphical description of the MPC implementation. (a) Without real-time certification. (b) With real-time certification.

execution duration has been checked to ensure that the selected sampling interval is always longer than this worst-case duration, namely in a case-by-case manner [21], [25], [26].

For a strict real-time certification, a new termination criterion is proposed here. Instead of iterating continuously until the residual falls below its tolerance level, the solver fully utilizes the available sampling interval T_s for the iterative computations, and terminates the iteration when T_{mpc} approaches T_s , as shown in Fig. 6(b). Although this new criterion may slightly sacrifice the accuracy and convergence of results, it guarantees the feasibility of real-time implementation, which is more important in the present application of MPC control. Once the QP problem is formulated, T_{mpc} can be largely determined by the number of iterations, as parameter updates typically have a fixed execution duration. Therefore, the proposed termination criterion can be straightforwardly implemented by adjusting the maximum number of iterations in OSQP solver, ensuring that the resulting T_{mpc} fits within T_s . In addition, because OSQP is a numerical solver,

TABLE II
SOFT CONSTRAINTS ON OUTPUT VARIABLES

Parameter and symbol	Rated value	Min.	Max.
DC loads voltage V_{load}	48 V	44 V	52 V
PV panel voltage V_{pv}	48 V	0 V	60 V
Battery current I_{bat}	3 A	-6 A	6 A

TABLE III
SPECIFICATIONS OF PROTOTYPING ENERGY ROUTER

Module	Parameter and symbol	Specification
Power modules	MOSFET	HSBA15810C
	Port voltage V_i	48 V
	Port inductor L_i	15 μ H
	Port capacitor C_i	680 μ F
	Series inductor $L_{s,i}$	2.2 μ H
	Series capacitor $C_{s,i}$	200 μ F
	Switching frequency f_{sw}	100 kHz
	Deadtime t_d	66 ns
Planar transformer	Turns ratio N_i	4:4:4 Nt
	PCB winding width	20 mm
	PCB winding thickness	70 μ m (2 oz)
	Magnetic core type	DMEGC EIW58.4A
	Magnetic core material	DMEGC DMR95
Sampling modules	Optical isolator	ACPL-C87H
	Hall sensor	TMCS1100
	Operational amplifier	MAX44245
MPC controller	MCU	STM32H723VGT6
	Sampling time T_s	200 μ s
	Horizon length N_p	3
	Weighting matrix Q	$\text{diag}\left\{\frac{1}{(4A)^2}, \frac{1}{(48V)^2}, \frac{1}{(48V)^2}\right\}$
	Weighting matrix S	$\text{diag}\left\{\frac{1}{A^2}, \frac{1}{\sqrt{2}}, \frac{1}{\sqrt{2}}\right\} \cdot 0.005$
	Weighting matrix R	$\text{diag}\{1, 1, 1\} \cdot 500/\text{rad}^2$

control stability itself is determined by the MPC controller parameters including the horizon length N_p and weighting matrices Q , R , and S [29].

Meanwhile, the computed control action will be executed at the beginning of next sampling interval under this criterion, as shown in Fig. 6(b), which will introduce an execution delay of T_s . To compensate for this one-step delay, the discrete predictive model (5) is modified accordingly as

$$\Delta \mathbf{x}(k+1) = G\Delta \mathbf{x}(k) + H\Delta \mathbf{u}(k-1). \quad (20)$$

The executed optimal control variables in (14) are then adjusted as

$$\Delta \mathbf{u}^*(k) = [\mathbf{0}_3, \mathbf{I}_3, \dots, \mathbf{0}_3] \Delta \tilde{\mathbf{U}}^*(k). \quad (21)$$

IV. PARAMETER DESIGN AND SIMULATION

The effectiveness of the developed CCS-MPC scheme for the MAB converters, featuring OVP/UVP, is first verified through simulations on PLECS software. The four-port circuit topology and parameters are identical to those of the following experimental prototype, as detailed in Fig. 1 and Table III. The choice of N_p and T_s is particularly crucial for real-time implementation and will be further discussed in the following section on experimental validation. The weighting matrix Q for predicted errors is designed to normalize the different terms in the cost function, as defined in Appendix D. The weighting matrices R and S for control increments and slack variables are tuned through the

branch-and-bound (BnB) method to prevent aggressive actions and ensure stability [33].

The constraint on dc load voltage is set within ± 4 V of its rated value (i.e., [44 V, 52 V]), which is a strict constraint because of the narrow range of allowed load voltage variation and possible sudden load change. The upper bound of PV panel voltage is specified as its open-circuit voltage (i.e., 60 V), which is approximately 1.3 times the operation voltage, indicating a wide permitting voltage range [34]. And the maximum permissible battery current is ± 6 A. Due to the presence of a filter inductor in the battery port, which is a common practice for current-fed ports, this port is unlikely to experience current overshoot and undershoot [35]. Therefore, the two constraints on the PV panel voltage and battery current are less restrictive than the constraint on the dc load voltage. The above-mentioned three soft constraints [i.e., c in (12) and Appendix E] on the output variables are further summarized in Table II.

The weighting factors on constraint violations ϵ are uniformly defined as $S = \text{diag}\{s, s, s\}$, and their impact is discussed as follows. A dynamic link library (DLL) file is developed using the OSQP solver through the visual studio integrated development environment. This DLL file is then called via the PLECS DLL block and used as a digital controller in the simulation. Fig. 7 shows the simulated waveforms of the MAB converter under the MPC control with/without OVP/UVP and with different uniform weighting factor s . Here, the load at port #3 undergoes a step change, dropping from 48 to 19.2 Ω at 0.4 s, and returning to 48 Ω at 0.6 s. Under the MPC control without OVP/UVP (i.e., $s = 0$), the voltage overshoot/undershoot during the transient responses significantly exceeds the constraint, which is indicated by the gray area in Fig. 7. However, once the constraint violations are penalized (i.e., $s > 0$), the overshoot and undershoot are effectively suppressed. Increasing the weighting factor s results in reduced constraint violations. However, a tradeoff in the value of s exists between the constraint violation penalty and control stability. In practice, s is tuned via the BnB method to minimize constraint violations while avoiding oscillatory responses (i.e., poor stability).

V. EXPERIMENTAL RESULTS

A. Prototype and Experimental Setup

A four-port MAB converter, working as an energy router, is fabricated in house for experimental validation, as shown in Fig. 8. The above-mentioned constrained CCS-MPC scheme is implemented on a low-cost control platform, an STM32H723 MCU (Arm Cortex-M7 with 550 MHz clock and floating-point unit). This MCU, which costs about ten USDs, performs A/D conversion (12-bit resolution), QP problem solving through the OSQP solver, and phase-shifted gate signal modulation (refer to Fig. 5). The state variables \mathbf{x} , namely the dc voltages and currents, are measured through optical isolators and Hall sensors. High-precision components, calibration, and A/D oversampling are especially adopted to minimize measurement and quantization noise. The detailed parameters and specifications of the above-mentioned prototyping energy router are listed in Table III. Here, the switching frequency and series inductance are determined according to (1) to meet the required power level. The series capacitance is chosen to be large enough to block the dc bias, and the dead time is tuned to achieve full zero voltage switching (ZVS). The transformer turns ratio is designed

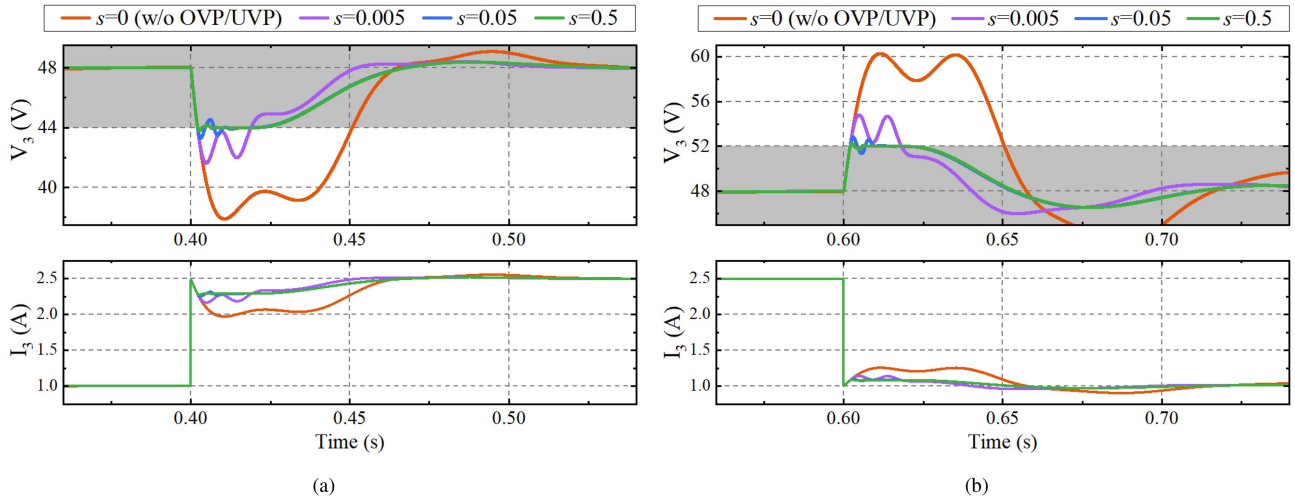


Fig. 7. Simulated waveforms with different weighting factors (i.e., s) on constraint violations. (a) Load change in port #3 from 48 to 19.2 Ω at 0.4 s. (b) Load change in port #3 from 19.2 to 48 Ω at 0.6 s.

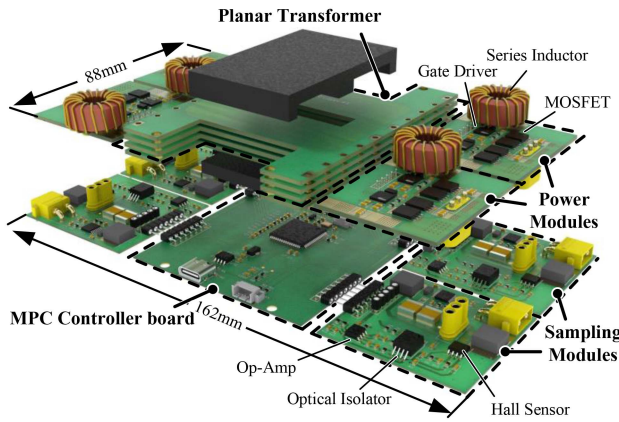


Fig. 8. 3-D assembly drawing of the prototyping four-port energy router.

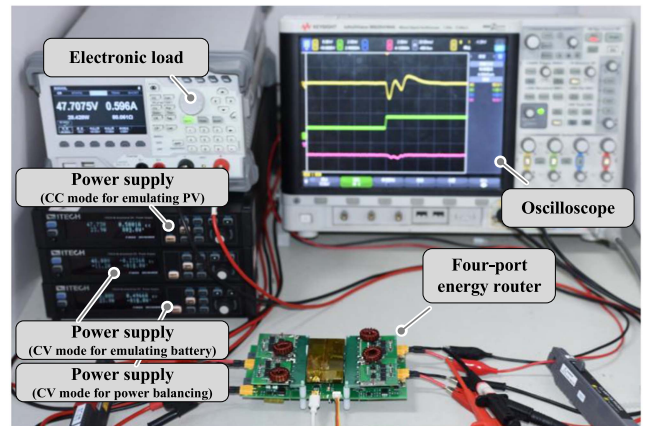


Fig. 9. Laboratory experimental setup.

to match the port voltage ratio, ensuring full-range soft switching for all the H-bridges [36].

Fig. 9 shows the entire laboratory experimental setup. Here, ports #1 and #4 of the four-port prototyping energy router are connected to their respective bidirectional power supplies both configured in constant voltage mode for emulating the storage battery and power balancing, while port #2 is connected to another power supply configured in CC mode emulating the PV panels, and port #3 is connected to a programmable electronic load as a dc load. Note that in this article (including in the following experiments), the dc–ac function (i.e., the dc–ac converter in Fig. 1) is not considered. The host PC transmits the reference voltage/current signal of each port, i.e., $W(k)$, to the MCU via the UART protocol.

Fig. 10(a) illustrates the steady-state waveforms of the energy router. The turn-ON transient is especially enlarged to confirm the ZVS operation. Fig. 10(b) presents the power loss breakdown under full load condition. Fig. 11(a) shows the measured efficiency contours in a two-input two-output scenario, in which the PV port and battery port are configured as input ports, while the load port and power balancing port are configured as output ports

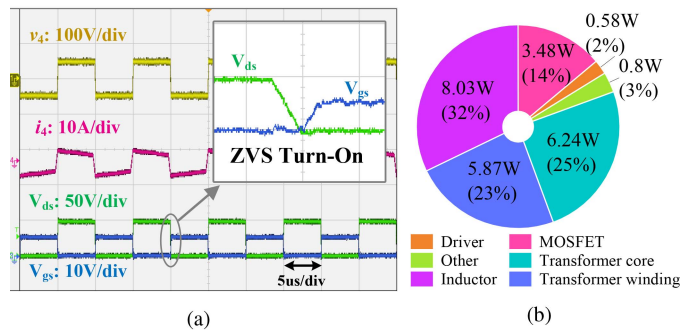


Fig. 10. Steady-state characteristics. (a) Voltage/current waveforms and ZVS characteristics. (b) Loss breakdown under a full load operating condition.

with the same output power. Fig. 11(b) summarizes the measured MIMO efficiency curves with different port configurations. The developed prototype can deliver a maximum power of 500 W. The peak efficiency is over 97% and the MIMO efficiency maintains above 95% across a wide operation range.

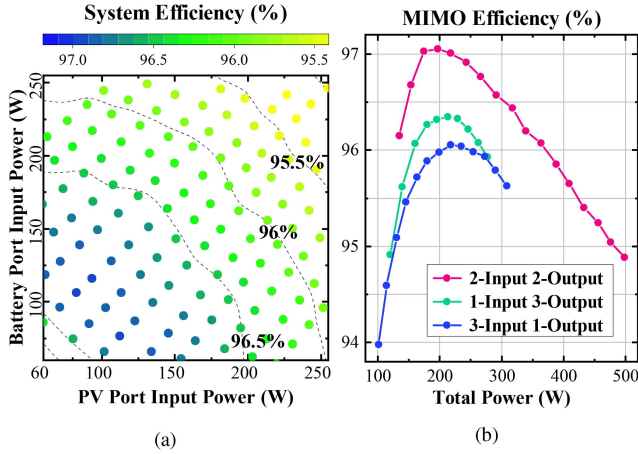


Fig. 11. Measured system efficiency. (a) Efficiency contours in a two-input two-output scenario. (b) MIMO efficiency curves with different port configurations.

B. Over/Under Voltage Protection

The dynamic performance of the developed MPC control with OVP/UVP is compared with that under the MPC control without OVP/UVP and conventional PI control. Note that under the MPC control without OVP/UVP, the constraints on output variables are invalid by setting the weighting factor $s = 0$, and the input constraints (9) are still effective. And under the PI control, input saturators and integrator clamping are applied in the following experiments, as the input/output constraints are difficult to explicitly incorporate. As examples, the dynamic responses to step load changes at port #3 under the three controls are shown in Figs. 12 and 13; and the dynamic responses under current reference change at port #1 are shown in Fig. 14.

The UVP capability of the constrained MPC control is first demonstrated by comparing the three waveforms in Fig. 12, where the dc load at port #3 is suddenly changed from 1 to 2.5 A. Fig. 12(b) and (c) shows that under MPC control without OVP/UVP and PI control, the voltage undershoot is as high as 7 and 7.5 V, respectively, which obviously exceeds the lower bound (represented by the straight yellow line). In contrast, the developed MPC control with OVP/UVP effectively suppresses this voltage undershoot, resulting in only minor constraint violation. As shown in Fig. 12(a), when the lower bound is at risk of being violated, the predicted slack variables ϵ become positive, forcing the MPC control to adjust the phase shift angles that minimize the constraint violation [refer to (13)]. Fig. 13(a)–(c) shows the dynamic responses to a load change at port #3 from 2.5 A back to 1 A. Again, the OVP capability is confirmed as the voltage overshoot under the MPC control with OVP/UVP only slightly exceeds the upper bound, while the peak overshoot under the MPC control without OVP/UVP and PI control is more than one-third larger. Fig. 14(a)–(c) shows the dynamic responses to the current reference change of port #1 from 1 to -1.5 A under the three controllers. The waveforms of the two MPC controls in Fig. 14(a) and (b) are nearly identical, as no output constraints are violated during the transient process. Consequently, the MPC with OVP/UVP behaves equivalently to the MPC without OVP/UVP in this scenario, since the slack variables ϵ are both zero. Meanwhile, under the PI control, V_3 in Fig. 14(c) exceeds the constraint due to the cross-coupling effect.

In addition, since the cross-coupling effect of the MAB converter can be explicitly reflected in the predictive model (refer to Section II-A), the interferences between the ports can be better suppressed through the MPC controls than through the PI control. For instance, in Fig. 14, the current reference (I_1) change at port #1 causes the output voltage (V_3) at port #3 to fluctuate, and the two MPC controls reduce the fluctuation in V_3 compared to the PI control. The similar results can also be observed in Figs. 12 and 13. Meanwhile, the fluctuations in I_1 under the MPC with OPV/UVP in Figs. 12(a) and 13(a) are not as same as the MPC without OPV/UVP in Figs. 12(b) and 13(b). This is because the MPC with OPV/UVP enforces stricter penalties on the constraint violations, which in turn reduces the effort allocated to current reference tracking in port #1. At the same time, the suppression of the output constraint violations also shortens the settling time of transient responses, as shown by comparing Fig. 12(a) and (b) as well as Fig. 13(a) and (b).

The robustness of the developed MPC is further checked by introducing parameter variations. Specifically, the series inductance at port #3 ($L_{s,3}$) varies by approximately $\pm 30\%$ (i.e., 1.5 and $3 \mu\text{H}$) compared to its nominal value $2.2 \mu\text{H}$. As shown in Fig. 15, the experimental results demonstrate that the MPC control can suppress the load disturbance and maintain stable performance despite the inductance variation. Fig. 16 illustrates the impact of the weighting factor s . It can be seen that increasing s reduces the constraint violation, but an excessively large s may degrade the control performance or even cause oscillatory responses (i.e., poor stability). As mentioned in Section IV, $s (= 0.005$ here) is typically tuned via the BnB method to minimize constraint violations while avoiding oscillation [33].

C. Timing Analysis

To investigate the actual real-time performance, the execution duration of the developed CCS-MPC control in each time instant is measured by toggling a signal at the beginning and end of the control algorithm execution, as shown in Fig. 17. The total execution duration T_{mpc} (i.e., ③) is consistent at different time instants, which proves the above-mentioned theoretical analysis in Fig. 6(b) and thus the real-time certification.

It is worth noting that the consistent execution duration is the most notable advantage of the OSQP solver, as the problem dimension of the ADMM algorithm remains constant across iterations [31]. Consequently, once the number of iterations is specified, the execution duration becomes deterministic and consistent, providing a strict real-time certification. In contrast, the AS algorithm used in qpOASES exhibits a varying problem dimension across iterations, since a different number of constraints may be activated at each iteration. As a result, the total execution duration fluctuates even when the number of iterations is fixed. In practical applications, the sampling time T_s is typically chosen to be significantly longer than the worst-case execution duration to maintain a safety margin [18], [19], [20], [21].

The execution duration with different horizon lengths (N_p) and numbers of iterations (N_{iter}) under the proposed termination criterion in Section III-C is also examined to provide guidance for parameter selection, as shown in Fig. 18. Increasing N_p and N_{iter} results in a longer execution duration, thus requires a longer sampling time T_s for the present embedded implementation. Meanwhile, it is known that a larger N_p also enhances the control stability, and a larger N_{iter} increases the accuracy of optimization results [29]. Based on the experimental results in

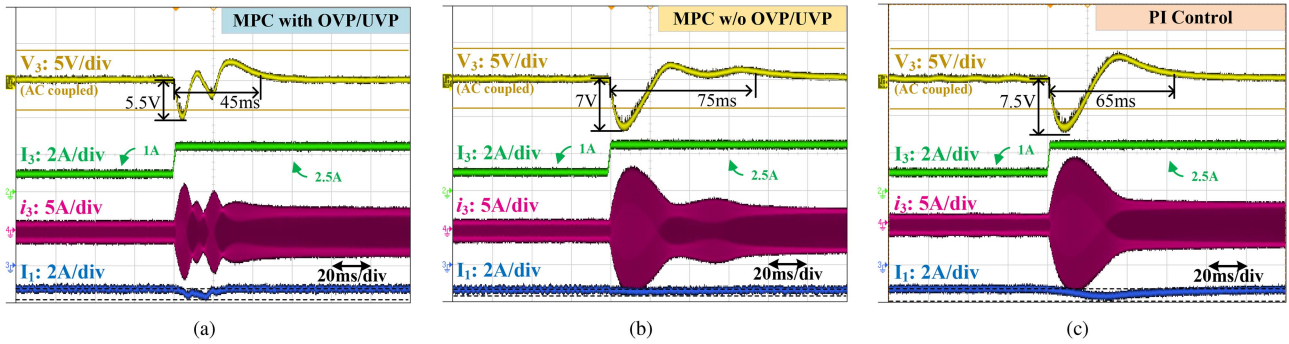


Fig. 12. Dynamic responses under a step load change at port #3 from 1 to 2.5 A. (a) MPC with OVP/UVP. (b) MPC without OVP/UVP. (c) PI control.

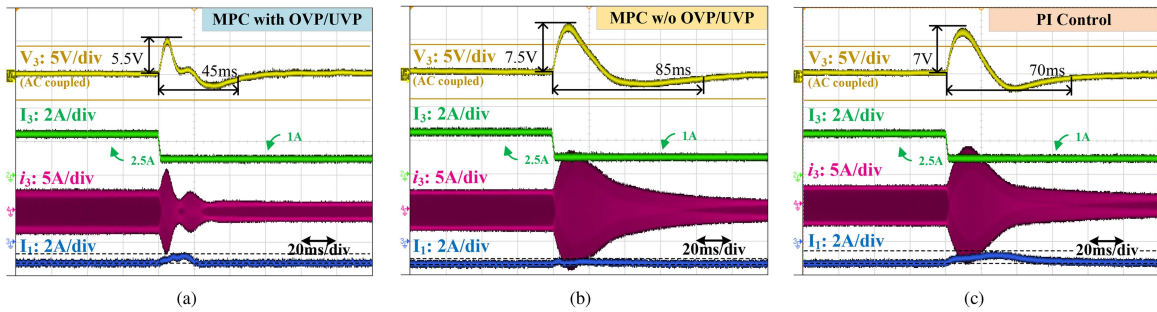


Fig. 13. Dynamic responses under a step load change at port #3 from 2.5 to 1 A. (a) MPC with OVP/UVP. (b) MPC without OVP/UVP. (c) PI control.

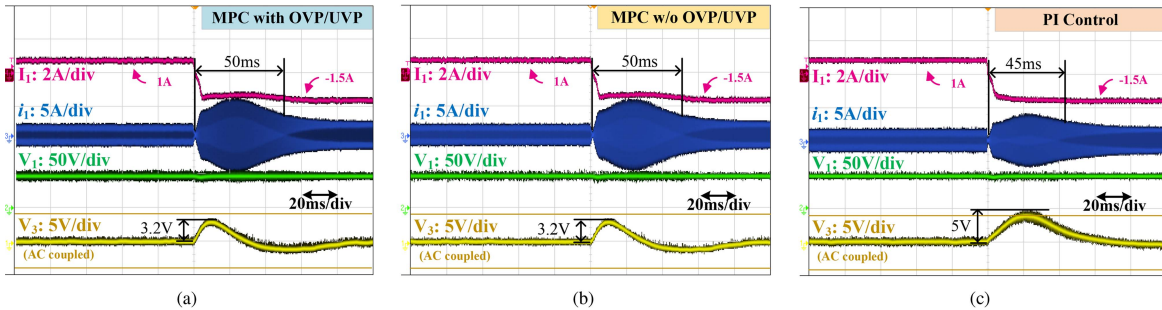


Fig. 14. Dynamic responses under a current reference change at port #1 from 1 to -1.5 A. (a) MPC with OVP/UVP. (b) MPC without OVP/UVP. (c) PI control.

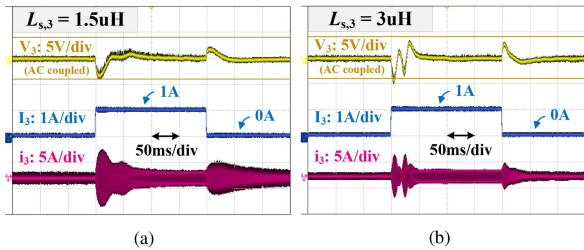


Fig. 15. Dynamic responses with parameter variations. (a) $L_{s,3} = 1.5 \mu\text{H}$. (b) $L_{s,3} = 3 \mu\text{H}$.

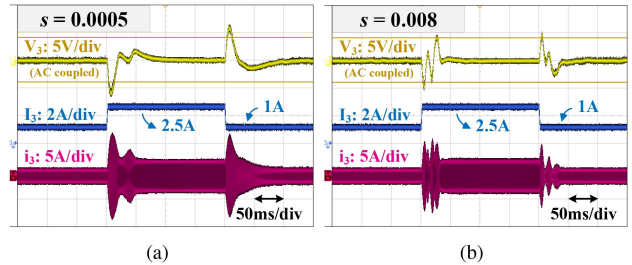


Fig. 16. Dynamic responses under different weighting factor s . (a) $s = 0.0005$. (b) $s = 0.008$.

Fig. 18, the parameters $T_s = 200 \mu\text{s}$, $N_p = 3$ and $N_{iter} = 10$ are chosen accordingly to make sure that the total execution

duration T_{mpc} can almost fill the sampling interval (i.e., T_s), while having relatively large N_p and N_{iter} . Note that due to the

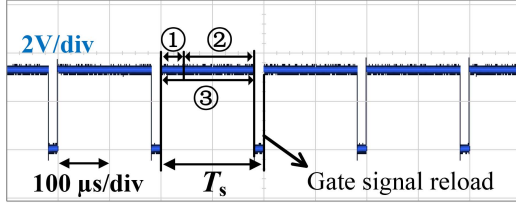


Fig. 17. Execution duration of the proposed CCS-MPC control within a single sampling interval [(1): A/D conversion and parameters update (1 μ s). (2): OSQP iteration (181 μ s). (3): Total execution T_{mpc} (182 μ s)].

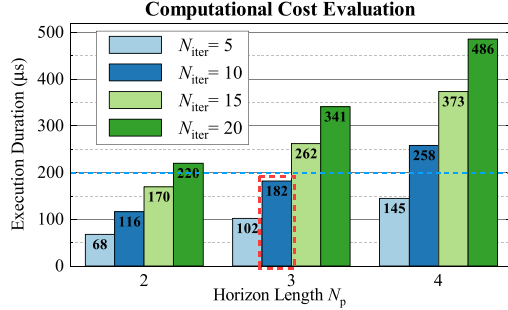


Fig. 18. Execution duration versus horizon length N_p and maximum number of iterations N_{iter} .

TABLE IV
KEY PARAMETERS AND SETTINGS OF OSQP SOLVER

Parameter	Description	Value/Setting
max_iter	Maximum number of iterations	$N_{iter} = 10$
check_termination	Check termination interval	$N_{iter} = 10$
warm_start	Perform warm starting	1

high computational burden, the control frequency is much lower than the switching frequency in this application. Therefore, another higher frequency timer (e.g., 100 kHz) can be enabled in the MCU for hard fault detection. Finally, thanks to future advances in QP solvers and MCU's computing performance, a possible faster control frequency can further accelerate the transient responses.

For reference purposes, the relevant key parameters and solver settings of OSQP are listed in Table IV below. Since the initial iteration point may affect the final solution, the so-called warm start strategy is adopted to improve the accuracy of the final solution, in which the solution of the previous time instant $k - 1$ is set as the initial iteration point of the present time instant k .

VI. CONCLUSION

This article proposes and implements a constrained CCS-MPC-based energy routing control scheme for the MAB converters with OVP/UVP capability. The MPC control problem is first formulated as a QP problem by introducing soft constraints on the output voltage/current and hard constraints on the phase shift angle. Then, a new efficient QP solver OSQP with improved termination criteria is applied over a low-cost MCU, which is able to solve the QP problem online within a short sampling interval (i.e., 200 μ s here) and thus achieve real-time implementation. Finally, the proposed control scheme is verified by simulation and experiments. The four-port 500 W prototyping

energy router demonstrates effective OVP/UVP and guaranteed real-time performance.

APPENDIX

A. Parameters in “ Δ -type” and “Y-type” equivalent circuit shown in Fig. 2(b) and (c)

$$L'_{s,i} = L_{s,i}(N_1/N_i)^2, V'_i = V_i(N_1/N_i)^2$$

$$L_{ij} = L'_{s,i}L'_{s,j} \sum_{k=1}^n 1/L'_{s,k}$$

B. Small signal gains in predictive model (4)

$$G_{ij} = \begin{cases} \sum_{j \neq i}^n \frac{V_j}{2\pi f_{sw} L_{ij} N_j N_i} \left(1 - \frac{2|\Phi_i - \Phi_j|}{\pi}\right), & j = i \\ -\frac{V_j}{2\pi f_{sw} L_{ij} N_j N_i} \left(1 - \frac{2|\Phi_i - \Phi_j|}{\pi}\right), & j \neq i \end{cases}$$

$$H_{ij} = \frac{1}{2\pi f_{sw} L_{ij} N_j N_i} (\Phi_i - \Phi_j) \left(1 - \frac{|\Phi_i - \Phi_j|}{\pi}\right).$$

C. Matrix F and L in predictive law (6)

$$F = \begin{bmatrix} (\sum_{i=1}^1 CG^i)^T & (\sum_{i=1}^2 CG^i)^T & \dots & (\sum_{i=1}^{N_p} CG^i)^T \end{bmatrix}^T$$

$$L = \begin{bmatrix} \sum_{i=0}^0 CG^i H & 0 & \dots & 0 \\ \sum_{i=0}^1 CG^i H & \sum_{i=0}^0 CG^i H & \dots & 0 \\ \vdots & \vdots & \ddots & \vdots \\ \sum_{i=0}^{N_p-1} CG^i H & \sum_{i=0}^{N_p-2} CG^i H & \dots & \sum_{i=0}^0 CG^i H \end{bmatrix}$$

D. Weighting Factor Matrix Q , R in cost function (8)

$$Q = \text{diag} \{q_1, q_2, q_3\} = \text{diag} \left\{1/\tilde{I}_{L_1}^2, 1/\tilde{V}_{C_2}^2, 1/\tilde{V}_{C_3}^2\right\}$$

$$R = \text{diag} \{r_1, r_2, r_3\} = \text{diag} \{\lambda, \lambda, \lambda\}$$

where \tilde{I}_{L_1} , \tilde{V}_{C_2} , \tilde{V}_{C_3} represent the rated value of I_{L_1} , V_{C_2} , V_{C_3} , respectively.

E. Matrix K , M , and c in soft constraints (12)

$$K = \begin{bmatrix} 1 & 0 & 0 \\ 1 & 0 & 0 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 1 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \\ 0 & 0 & 1 \\ 0 & 0 & 1 \end{bmatrix} \quad M = \begin{bmatrix} 1 & 0 & 0 \\ -1 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & -1 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 0 & -1 \\ 0 & 0 & 0 \end{bmatrix} \quad c = \begin{bmatrix} -I_{bat,max} \\ I_{bat,min} \\ 0 \\ -V_{PV,max} \\ V_{PV,min} \\ 0 \\ -V_{load,max} \\ V_{load,min} \\ 0 \end{bmatrix}$$

F. Matrix Q , R , S , K , M , N , and vector \bar{c} in QP problem

$$Q = \text{blk diag} \{Q, Q, \dots, Q\} \quad R = \text{blk diag} \{R, R, \dots, R\}$$

$$S = \text{blk diag} \{S, S, \dots, S\} \quad K = \text{blk diag} \{K, K, \dots, K\}$$

$$M = \text{blk diag} \{M, M, \dots, M\} \quad \bar{c} = [c, c, \dots, c]$$

$$N = \begin{bmatrix} I_3 & 0_3 & \dots & 0_3 \\ I_3 & I_3 & \dots & 0_3 \\ \vdots & \vdots & \ddots & 0_3 \\ I_3 & I_3 & \dots & I_3 \end{bmatrix}$$

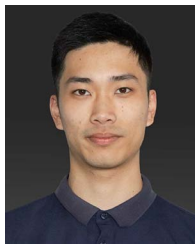
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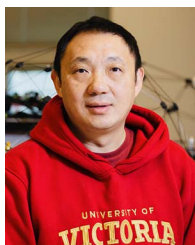
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