

Letters

A Noninvasive Grid Impedance Estimation Method With Phase-Lock Loops

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Abstract—For grid-following inverters, an accurate grid impedance estimation (GIE) enables many beneficial applications. Conventional active GIE methods, such as the PQ variation method, intentionally introduce power disturbances around the system's steady-state operating point. However, it might bring undesired reactive power into the grid and its accuracy can be influenced by phase-lock loop (PLL) phase angle variations. To address these challenges, this letter proposes a noninvasive GIE method, which utilizes PLL's inherent operating characteristics. It only requires a single steady-state operating point transition that naturally occurs during the system's normal operation, eliminating the need to inject deliberate PQ disturbances. In addition, by considering PLL phase angle variations, this method also ensures accurate estimation results across diverse conditions. Experimental verifications are provided, which demonstrate the effectiveness and accuracy of the proposed method.

Index Terms—Grid-following inverter (GFI), grid impedance, noninvasive estimation, phase-lock loop (PLL), PQ variations.

I. INTRODUCTION

WITH the ongoing emphasis on reducing carbon footprints, renewable energy generation systems and energy storage systems are receiving heightened attention and are being more widely integrated into power systems [1]. These power-electronics-based systems commonly connect to the grid via grid-following inverters (GFIs) at the point of common coupling (PCC), and their performance is inevitably influenced by grid impedance, affecting power transfer, passive component resonance, and synchronization stability [2], [3], [4]. Consequently, an accurate grid impedance estimation (GIE) has garnered extensive attention and finds applications in areas such as islanding detection, adaptive control implementations, and real-time impedance-based stability assessment [4], [5], [6].

GIE methods can be generally categorized into two groups: passive and active approaches [7]. Passive approaches enable impedance estimation by exploring existing information in the system. For example, grid inductance can be measured using

current switching ripples or current distortions caused by dead-time effects [8], [9]. Alternatively, Duan et al. [10] proposed a method for identifying grid inductance by calculating the oscillation frequencies of grid currents in the transient state. However, these methods typically depend on high-bandwidth sampling and noise filtering techniques. In addition, they often overlook the grid resistance.

Active methods intentionally induce a disturbance in the grid after which grid impedance can be estimated by analyzing the consequent current responses. For example, a pulsed signal injection approach and a recursive least square algorithm were used to measure the grid impedance [11]. Alternatively, a pseudorandom binary sequence signal injection scheme was proposed to improve GIE accuracies in nonideal grid conditions [12]. Recently, a zero-sequence voltage injection GIE method was adopted in a three-phase four-wire GFI [13]. While these active methods may offer higher estimation accuracy than their passive counterparts, they undeniably pose potential power quality concerns.

For GFIs, the PQ variation method offers another active approach for GIE that brings fewer power quality concerns. This technique estimates grid impedance by intentionally changing the system's active and reactive power, thereby avoiding the need for harmonic injections [14], [15], [16], [17], [18]. To facilitate calculations, a frame transformation is typically employed to convert measured ac signals into dc signals within the dq -frame [7]. Building on this, a decoupled power injection strategy was proposed in [15], where only active power is adjusted when estimating the grid resistance, and only reactive power is varied for estimating the grid inductance. Moreover, an online, event-based PQ variation GIE method was proposed in [16], which involves the continuous monitoring of the positive-sequence amplitude of PCC voltages. Recently, this method was adopted in a multi-inverter system to save real-time communications [17]. However, despite their simplicity, most PQ variation techniques still require at least two PQ perturbation injections and may introduce undesired reactive power to the grid.

Moreover, it is crucial to note that the phase angle variations of phase-lock loops (PLLs) will influence the accuracy of the PQ variation estimation method. Specifically, for GFIs, the measured voltages and currents align with the PCC-voltage dq -frame. This frame is typically provided by a PLL, which regulates the q -axis PCC-voltage to zero, giving a reference synchronous phase for signal measurements and

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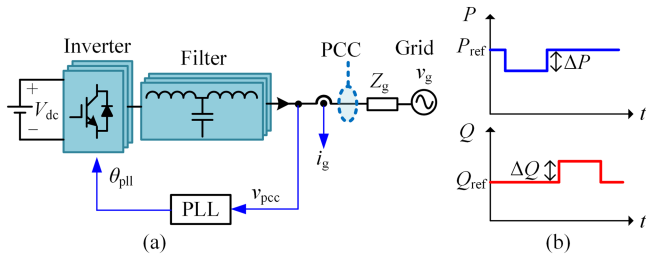


Fig. 1. Diagrams of (a) a GFI that connects to the grid at PCC with a PLL and (b) a typical PQ variation scheme for GIE.

injections. However, in the presence of grid impedance, the PQ variations inevitably cause steady-state changes to the PCC voltages, leading to a PLL frequency drift during the transient state. Consequently, after the PQ variations, the measured signals align with a different dq -frame. It can compromise the estimation accuracy if this effect is overlooked. Meng-Chun et al. [18] proposed a PQ variation method based on a virtual reference axis, which intentionally sets the PLL angular frequency as a constant, ensuring all measured signals align the same dq -frame during estimation. While this improves accuracy, it changes PLL's internal configurations and disables its closed-loop regulation.

To overcome the limitations of conventional methods, this letter proposes a noninvasive GIE method for GFIs based on PLL's inherent characteristics. Its major benefits are as follows.

- 1) The proposed method utilizes the system's natural steady-state transitions to identify both grid resistance and inductance, without injecting deliberate PQ disturbances.
- 2) The proposed method utilizes PLL's inherent operating characteristics, eliminating the need to change existing hardware or software configurations. Furthermore, it takes into account PLL phase angle variations, guaranteeing precise estimation results across diverse conditions.

The rest of the letter is organized as follows. Section II briefly reviews PQ variation-based GIE methods, highlighting how PLL phase angle variations influence the accuracy of the estimations. Section III introduces the proposed non-invasive GIE method, along with a practical implementation guideline. The experimental results are presented in Section IV. Finally, Section V concludes this letter.

II. PQ VARIATION-BASED GIE METHODS AND PLL PHASE ANGLE VARIATIONS

This section briefly reviews the principles of PQ variation-based GIE methods, highlighting how PLL phase angle variations impact the accuracy of the estimations. The system structure diagram is shown in Fig. 1(a), illustrating a GFI that is connected to the grid at PCC. Within this structure, V_{dc} is the dc-link voltage; i_g represents the grid currents; v_{pcc} and v_g denote the PCC voltages and grid voltages, respectively. The grid impedance, represented by Z_g , comprises the grid equivalent resistance R_g and equivalent inductance L_g , i.e.,

$$Z_g = R_g + j\omega_g L_g \quad (1)$$

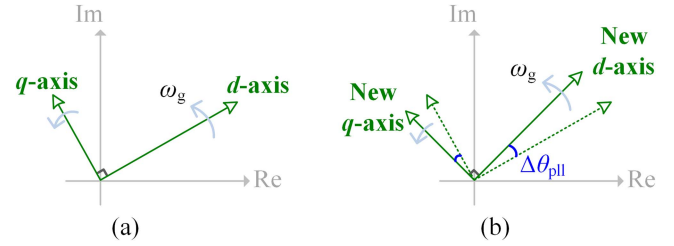


Fig. 2. Phasor diagrams of the PLL dq -frame established by PCC-voltages (a) before PQ variation and (b) after PQ variation, where the $\Delta\theta_{pll}$ is the PLL phase angle variation.

where ω_g represents the grid angular frequency. Fig. 1(b) shows a typical PQ variation scheme for GIE, which intentionally disturbs the system output power (specifically, the grid currents) around the system's steady-state operating points. By employing Kirchhoff's voltage law, the pre-disturbance v_{pcc} can be expressed as

$$v_{pcc} = v_g + i_g Z_g. \quad (2)$$

When i_g changes to $i_g + \Delta i_g$, (2) is rewritten as

$$v_{pcc} + \Delta v_{pcc} = v_g + (i_g + \Delta i_g) Z_g \quad (3)$$

where Δv_{pcc} denotes the PCC voltage change. Based on (2) and (3), Z_g can be determined as

$$Z_g = \Delta v_{pcc} / \Delta i_g. \quad (4)$$

Transformation techniques are commonly employed to convert time-varying ac signals in the abc -frame into time-invariant dc signals in the dq -frame to facilitate calculations [7]. By doing so, the estimation of Z_g changes to

$$Z_g = \Delta v_{pcc-dq} / \Delta i_{g-dq} \quad (5)$$

where Δv_{pcc-dq} and Δi_{g-dq} represent the PCC voltage and current changes in dq -frame, respectively. However, this result overlooks the impacts of PLL phase angle variations, which can affect the estimation accuracy. Specifically, the measured signals align with the PCC-voltage dq -frame. As shown in Fig. 1, this frame is provided by a PLL, which naturally regulates $v_{pcc-q} = 0$ in the steady state, thereby establishing a reference phase θ_{pll} for frame transformations, signal measurements and disturbance injections. However, as indicated in (2) and (3), the PQ variations inevitably cause steady-state changes to v_{pcc} , bringing a PLL frequency drift during the transient state. As a result, after the PQ variations, the measured signals align with a different dq -frame, exhibiting a phase angle difference (represented as $\Delta\theta_{pll}$ hereinafter) compared to the original dq -frame.

The effect of this PLL phase angle variation is shown in Fig. 2, indicating that the relationship among the variables, pre- and post-PQ variations, is geometric rather than simply algebraic. Therefore, estimation results derived from a direct subtraction of the variables before and after the disturbance, as outlined in (5), are prone to inaccuracies.

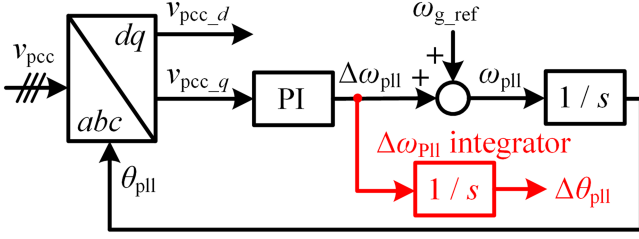


Fig. 3. Block diagram of an SRF-PLL with the $\Delta\theta_{pll}$ measurement by the $\Delta\omega_{pll}$ integrator.

III. PROPOSED NONINVASIVE GIE METHOD

A. $\Delta\theta_{pll}$ Measurement Scheme by PLLs

The $\Delta\theta_{pll}$ caused by PQ variations and PLL regulations leads to estimation inaccuracies. Thus, having prior information of $\Delta\theta_{pll}$ is important for an accurate GIE.

First of all, the structure of a synchronous reference frame PLL (SRF-PLL) is shown in Fig. 3, where the PLL loop filter, i.e., the PI controller, and a voltage-controlled oscillator, viz., the integrator, collectively adjust θ_{pll} to attenuate v_{pcc-q} . Note that in the steady state, $\omega_{pll} = \omega_g$, $v_{pcc-q} = 0$, and $v_{pcc-d} = V_{pcc}$ are satisfied, where V_{pcc} denotes the magnitude of v_{pcc} . Based on Fig. 3, θ_{pll} can be determined in the s -domain as

$$\theta_{pll}(s) = \omega_{g_ref}/s + \Delta\omega_{pll}(s)/s. \quad (6)$$

As seen, θ_{pll} comprises two components. The first component is the integration of ω_{g_ref} , which provides a constant angular frequency to the reference dq -frame. As ω_{g_ref} is unchanged, this component remains unaffected by PQ variations and, consequently, does not contribute to $\Delta\theta_{pll}$. The second component is the integration of $\Delta\omega_{pll}$, which determines $\Delta\theta_{pll}$. Specifically, when PQ variations cause changes to v_{pcc} , v_{pcc-q} will deviate from zero in the transient period. In response to this deviation, the PI controller's output, i.e., $\Delta\omega_{pll}$, causes a small angular frequency drift to adjust θ_{pll} , aiming to reduce v_{pcc-q} . Consequently, in the system's subsequent steady state with $v_{pcc-q} = 0$, the phase shift accumulated by $\Delta\omega_{pll}$ establishes $\Delta\theta_{pll}$, that is

$$\Delta\theta_{pll}(s) = \Delta\omega_{pll}(s)/s. \quad (7)$$

Based on (7), in order to solely obtain $\Delta\theta_{pll}$, an external integrator, hereafter referred to as the $\Delta\omega_{pll}$ integrator, is added to $\Delta\omega_{pll}$. As depicted in the red block in Fig. 3, this addition does not alter the existing operations of the PLL. By doing so, one can easily obtain $\Delta\theta_{pll}$ from this integrator's output after the PQ variations.

B. Working Principles of the Proposed GIE Method

In this section, the working principles of the proposed GIE method are presented. For clarity, v_g is chosen as a reference of zero degree, denoted as

$$v_g = V_g \angle 0 \quad (8)$$

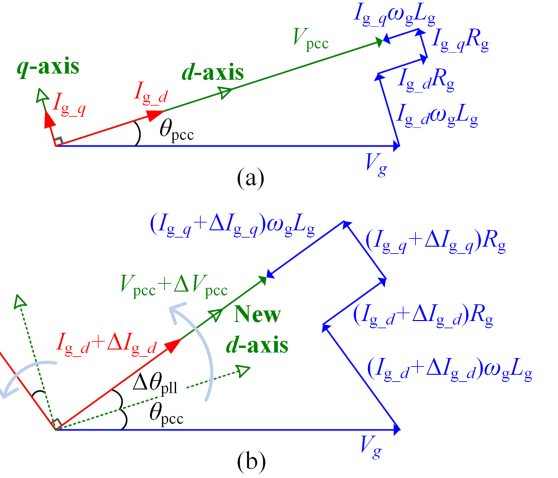


Fig. 4. Phasor diagrams of PCC voltages, grid currents, and grid voltages (a) before variation and (b) after variation.

where V_g represents the magnitude of the grid voltage. Using this reference, v_{pcc} can be represented as

$$v_{pcc} = V_{pcc} \angle \theta_{pcc} \quad (9)$$

where θ_{pcc} is the initial phase angle of v_{pcc} . It is important to highlight that θ_{pcc} determines the steady-state phase angle of the reference dq -frame. Based on that, i_g can be expressed as

$$i_g = I_{g-d} \angle \theta_{pcc} + I_{g-q} \angle (\theta_{pcc} + \pi/2) \quad (10)$$

where I_{g-d} and I_{g-q} are the magnitudes of currents in the d - and q -axes, respectively. Based on (1) and (2), the interrelationships among the above variables are illustrated in a phasor diagram, as shown in Fig. 4(a), where the magnitude of each phasor is labeled adjacent to its representation. Based on Fig. 4(a), one can obtain that

$$V_g \cos(\theta_{pcc}) + I_{g-d} R_g - I_{g-q} \omega_g L_g = V_{pcc} \quad (11)$$

$$V_g \sin(\theta_{pcc}) = I_{g-d} \omega_g L_g + I_{g-q} R_g. \quad (12)$$

When there is a change in the steady-state working point, characterized by ΔI_{g-d} and ΔI_{g-q} that represent the changes in the d - and q -axes current magnitudes, respectively, the PCC voltage shifts to

$$v'_{pcc} = (V_{pcc} + \Delta V_{pcc}) \angle (\theta_{pcc} + \Delta\theta_{pll}) \quad (13)$$

where ΔV_{pcc} is the PCC voltage magnitude change, and $\Delta\theta_{pll}$ is the PLL phase angle variation. Aligning with a new dq -frame established by v'_{pcc} , thus, the steady-state grid current changes to

$$i'_g = (I_{g-d} + \Delta I_{g-d}) \angle (\theta_{pcc} + \Delta\theta_{pll}) + (I_{g-q} + \Delta I_{g-q}) \angle (\theta_{pcc} + \Delta\theta_{pll} + \pi/2). \quad (14)$$

Subsequently, this results in modifications to the phasor diagram in the steady state, as depicted in Fig. 4(b), from which one can obtain that

$$V_g \cos(\theta_{pcc} + \Delta\theta_{pll}) + (I_{g-d} + \Delta I_{g-d}) R_g - (I_{g-q} + \Delta I_{g-q}) \omega_g L_g$$

$$= V_{\text{pcc}} + \Delta V_{\text{pcc}} \quad (15)$$

$$V_g \sin(\theta_{\text{pcc}} + \Delta\theta_{\text{pll}}) = (I_{g-d} + \Delta I_{g-d})\omega_g L_g + (I_{g-q} + \Delta I_{g-q})R_g \quad (16)$$

Note that both V_{pcc} and $V_{\text{pcc}} + \Delta V_{\text{pcc}}$ can be obtained from $v_{\text{pcc}-d}$ before and after the variation. Furthermore, $\Delta\theta_{\text{pll}}$ can be obtained using the $\Delta\omega_{\text{pll}}$ integrator, as shown in Fig. 3. Therefore, based on the four sets of equations, denoted as (11), (12), (15), and (16), and the four corresponding unknowns, namely, R_g , L_g , V_g , and θ_{pcc} , it is feasible to derive exact analytical solutions for the grid impedance, written as (17) and (18) at the bottom of this page. Particularly, for the GFIs working with a unity power factor, one can insert $I_{g-q} = \Delta I_{g-q} = 0$ into (17) and (18), then we obtain the following:

$$R_{g,|\text{PF}|=1} = \frac{(\Delta V_{\text{pcc}} I_{g-d} + V_{\text{pcc}} \Delta I_{g-d} + 2V_{\text{pcc}} I_{g-d})[1 - \cos(\Delta\theta_{\text{pll}})] + \Delta V_{\text{pcc}} \Delta I_{g-d}}{2(I_{g-d}^2 + I_{g-d} \Delta I_{g-d})[1 - \cos(\Delta\theta_{\text{pll}})] + \Delta I_{g-d}^2} \quad (19)$$

$$\omega_g L_{g,|\text{PF}|=1} = -\frac{\Delta V_{\text{pcc}} I_{g-d} - V_{\text{pcc}} \Delta I_{g-d}}{2(I_{g-d}^2 + I_{g-d} \Delta I_{g-d})[1 - \cos(\Delta\theta_{\text{pll}})] + \Delta I_{g-d}^2} \sin(\Delta\theta_{\text{pll}}). \quad (20)$$

C. Practical Implementation Guideline

This section gives a practical implementation guideline for the proposed method.

Note that (17) and (18) offer a generalized estimation result that incorporates PLL phase angle variations. This enables the system to accurately identify both R_g and L_g with only one transition between any two steady states, specifically from (I_{g-d}, I_{g-q}) to $(I_{g-d} + \Delta I_{g-d}, I_{g-q} + \Delta I_{g-q})$. Such a transition commonly occurs in practical applications, such as when distributed generation systems adjust current references based on higher level

controllers, or when energy storage systems change their steady states in response to commands from energy management systems. Therefore, the proposed method can leverage these existing and natural steady-state transitions to estimate grid impedance, without causing any perturbations to the system's normal operation. From this perspective, the proposed method is noninvasive.

For conventional methods, utilizing these natural steady-state transitions is restricted, primarily because they overlook $\Delta\theta_{\text{pll}}$. Specifically, without the knowledge of $\Delta\theta_{\text{pll}}$, a single transition is not sufficient to obtain both R_g and L_g , necessitating additional information. Moreover, as $\Delta\theta_{\text{pll}}$ is not considered, their accuracy is limited to scenarios with minor steady-state transitions, where $\Delta\theta_{\text{pll}}$ is negligible. Typically, conventional

TABLE I
KEY SYSTEM PARAMETERS

Symbol	Value	Symbol	Value
V_g	110 V (rms)	ω_g	314 rad/s
V_{dc}	400 V	f_{sw}	20 kHz
R_g	1 Ω	L_g	4.4 mH
$V_{\text{th}-q}$	0.5 V	$T_{\text{th}-q}$	200 ms

methods inject two PQ distributions around the steady-state working point intentionally [14], [15], [16], [17], which is done in an invasive manner.

A step-by-step implementation guideline for the proposed GIE method is given as follows, where the compensation for the grid frequency variation is included.

Step I: Check whether $\omega_{g-\text{ref}} = \omega_g$. If yes, then proceed to Step III. If not, proceed to Step II.

Step II: When $\omega_{g-\text{ref}} \neq \omega_g$, there is a dc-bias in $\Delta\omega_{\text{pll}}$. In this case, one can manually set a new $\omega_{g-\text{ref}}$ that equals to ω_g . When the dc-bias of $\Delta\omega_{\text{pll}}$ reduces to zero, then proceed to Step III.

Step III: Record I_{g-d} , I_{g-q} , and V_{pcc} from $v_{\text{pcc}-d}$. Then, set the initial value of the $\Delta\omega_{\text{pll}}$ integrator to zero. After that, proceed to Step IV.

Step IV: Based on the commands issued by higher level controllers, the current references in d - and q -axes change to $I_{g-d} + \Delta I_{g-d}$ and $I_{g-q} + \Delta I_{g-q}$, respectively. When the system reaches a new steady state and $|v_{\text{pcc}-q}|$ is smaller than a given threshold voltage $V_{\text{th}-q}$ for a period $T_{\text{th}-q}$, then proceed to Step V.

Step V: Record $V_{\text{pcc}} + \Delta V_{\text{pcc}}$ from $v_{\text{pcc}-d}$, and $\Delta\theta_{\text{pll}}$ from the $\Delta\omega_{\text{pll}}$ integrator. Then, calculate R_g and L_g based on (17) and (18), or (19) and (20). It is worth noting that one can adopt the first-order Taylor approximation into (17)–(20) for small $\Delta\theta_{\text{pll}}$ values, i.e., $\sin(\Delta\theta_{\text{pll}}) \approx \Delta\theta_{\text{pll}}$ and $\cos(\Delta\theta_{\text{pll}}) \approx 1$, to simplify the calculations [19].

IV. EXPERIMENTAL RESULTS

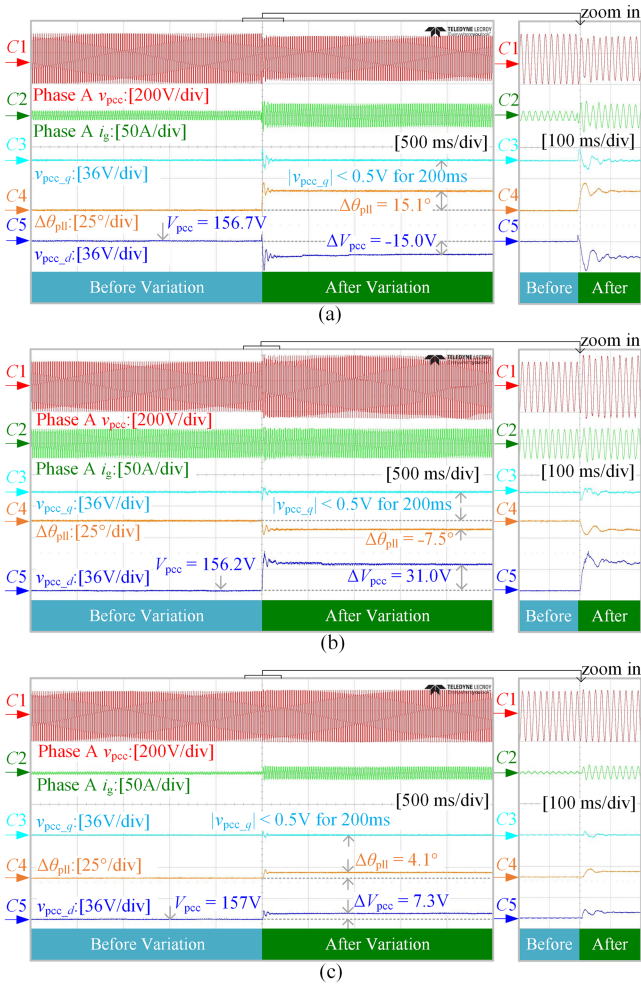
The effectiveness and accuracy of the proposed GIE method were tested and evaluated in this section. In the performed experiments, the grid voltage is provided by a Chroma grid emulator 61830. The three-phase GFI is fed by a bidirectional dc power supply ITECH IT600B, and the digital controller is implemented with TI TMS320F28377. The key system parameters are listed in Table I, where the total grid equivalent resistance and inductance are 1 Ω and 4.4 mH, respectively.

$$R_g = \frac{(\Delta V_{\text{pcc}} I_{g-d} + V_{\text{pcc}} \Delta I_{g-d} + 2V_{\text{pcc}} I_{g-d})[1 - \cos(\Delta\theta_{\text{pll}})] + (V_{\text{pcc}} \Delta I_{g-q} - \Delta V_{\text{pcc}} I_{g-q}) \sin(\Delta\theta_{\text{pll}}) + \Delta V_{\text{pcc}} \Delta I_{g-d}}{2(I_{g-d}^2 + I_{g-q}^2 + I_{g-d} \Delta I_{g-d} + I_{g-q} \Delta I_{g-q})[1 - \cos(\Delta\theta_{\text{pll}})] + 2(I_{g-d} \Delta I_{g-q} - \Delta I_{g-d} I_{g-q}) \sin(\Delta\theta_{\text{pll}}) + \Delta I_{g-d}^2 + \Delta I_{g-q}^2} \quad (17)$$

$$\omega_g L_g = -\frac{(\Delta V_{\text{pcc}} I_{g-q} + V_{\text{pcc}} \Delta I_{g-q} + 2V_{\text{pcc}} I_{g-q})[1 - \cos(\Delta\theta_{\text{pll}})] + (\Delta V_{\text{pcc}} I_{g-d} - V_{\text{pcc}} \Delta I_{g-d}) \sin(\Delta\theta_{\text{pll}}) + \Delta V_{\text{pcc}} \Delta I_{g-q}}{2(I_{g-d}^2 + I_{g-q}^2 + I_{g-d} \Delta I_{g-d} + I_{g-q} \Delta I_{g-q})[1 - \cos(\Delta\theta_{\text{pll}})] + 2(I_{g-d} \Delta I_{g-q} - \Delta I_{g-d} I_{g-q}) \sin(\Delta\theta_{\text{pll}}) + \Delta I_{g-d}^2 + \Delta I_{g-q}^2} \quad (18)$$

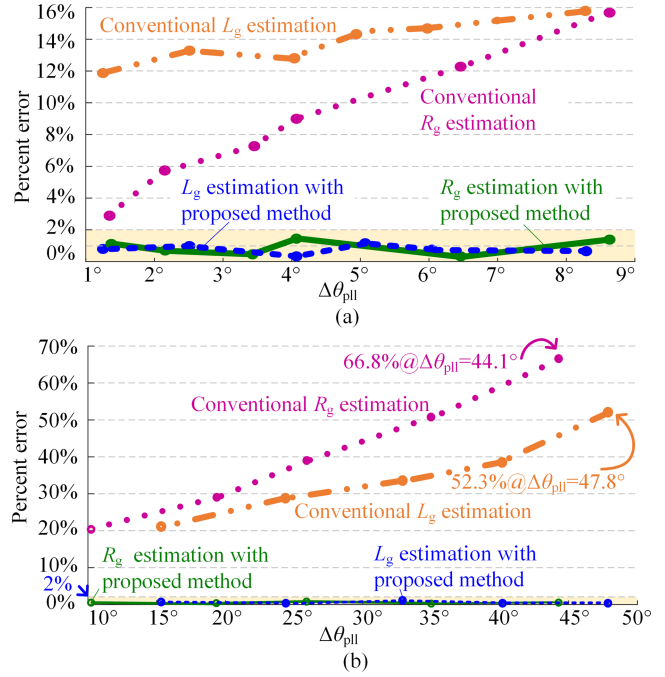
TABLE II
 THREE CASES OF SYSTEM OUTPUT CURRENT VARIATIONS

	$I_{g,d}$	$I_{g,q}$	$\Delta I_{g,d}$	$\Delta I_{g,q}$
Case I	-5 A	-5 A	15 A	20 A
Case II	20 A	10 A	0 A	-20 A
Case III	2 A	0 A	8 A	0 A


 Fig. 5. Experimental waveforms of v_{pcc} , i_g , v_{pcc-q} , v_{pcc-d} , and $\Delta\theta_{pll}$ in (a) Case I, (b) Case II, and (c) Case III.

In Table II, three cases of the system's output states and their respective variations are given. These variations aim to emulate different natural steady-state transitions that occur during the system's normal operation.

Fig. 5(a) shows the experimental waveforms of v_{pcc} , i_g , v_{pcc-q} , v_{pcc-d} , and $\Delta\theta_{pll}$ in Case I, where v_{pcc-q} , v_{pcc-d} , and $\Delta\theta_{pll}$ are measured at the outputs of digital-to-analog chips at the controller board. As can be seen, v_{pcc} varies with i_g in the presence of R_g and L_g , bringing transient in v_{pcc-q} . After PLL's regulation, the system will achieve a new steady state when v_{pcc-q} returns to zero. Based on (17) and (18), R_g and L_g are estimated as 0.997Ω and 4.411 mH in this case, with percent error only being 0.3% and 0.25% , respectively. It should be highlighted that conventional PQ variation-based GIE methods cannot be applied in this case, as they require


 Fig. 6. Percent error of L_g and R_g estimations across various $\Delta\theta_{pll}$ with and without the proposed method (the $\Delta\theta_{pll}$ is introduced by various ΔI_{g-q} and ΔI_{g-d} in Case II and Case III, respectively), when (a) $V_g = 110 \text{ V rms}$ and (b) $V_g = 40 \text{ V rms}$.

separate perturbations in ΔI_{g-d} and ΔI_{g-q} . In this case, the percent error of R_g and L_g estimation using the first-order Taylor approximation, i.e., $\sin(\Delta\theta_{pll}) \approx \Delta\theta_{pll}$ and $\cos(\Delta\theta_{pll}) \approx 1$, amounts to 5.0% and 6.3% , respectively. Furthermore, the total calculation time for deriving (17) and (18) is approximately 412 ns , while for the approximation results, it is significantly reduced to about 78 ns . It should be noted that the decision to utilize the approximations also depends on the specific requirements for estimation accuracies.

Fig. 5(b) shows the experimental waveforms in Case II, where only reactive power is varied. Based on (17) and (18), R_g and L_g are estimated as 0.995Ω and 4.362 mH , with percent error only being 0.5% and 0.86% , respectively. In contrast, the conventional PQ variation-based GIE methods can only estimate L_g in this case. Moreover, their accuracies are limited as $\Delta\theta_{pll}$ is not considered. Fig. 6(a) illustrates the comparative percent error of L_g estimation between the proposed and conventional GIE methods in this case. As seen, the proposed method exhibits a consistently low percent error, lower than 2% , under all testing conditions. In contrast, the traditional method demonstrates a significantly higher percent error, varying from 11.9% to 15.8% .

Fig. 5(c) shows the experimental waveforms in Case III, where only active power is generated and varied. Based on (19) and (20), R_g and L_g are estimated as 0.987Ω and 4.407 mH in this case, with percent error only being 1.3% and 0.2% , respectively. This result verifies that the proposed method can accurately identify both R_g and L_g solely through a single transition in active power, without requiring any injections of reactive power. This characteristic significantly enhances the practicability of the

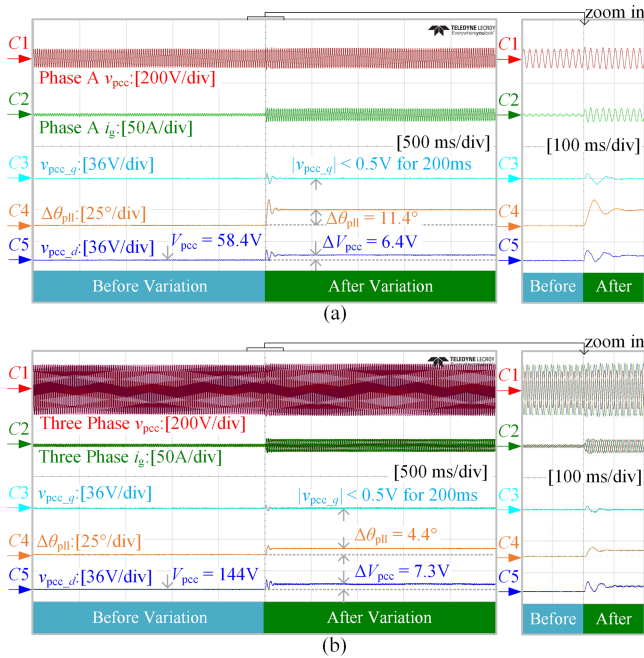


Fig. 7. Experimental waveforms of v_{pcc} , i_g , v_{pcc-q} , v_{pcc-d} , and $\Delta\theta_{pll}$ in Case III when the grid is under (a) a low-voltage fault ($V_g = 40\text{V rms}$) and (b) an unbalanced voltage fault (110 V rms for phase B and C, 80 V rms for phase A).

proposed method in distributed generation systems and energy storage systems. In contrast, the conventional methods can only estimate R_g in this case. Fig. 6(a) also illustrates the comparative percent error of R_g estimation, where the proposed method still exhibits low percent errors under all testing conditions. However, the conventional method's percent error climbs significantly, even reaching as high as 15.7% at $\Delta\theta_{pll} = 8.6^\circ$.

When grid frequency changes to 50.5 Hz, R_g is also accurately estimated as 1.005, 0.998, and 1.011 Ω in Cases I–III with percent errors of only 0.5%, 0.2%, and 1.1%, respectively; L_g is estimated as 4.387, 4.422, and 4.342 mH in Cases I–III, with percent errors of only 0.3%, 0.5%, and 1.3%, respectively. These results verify the proposed method's effectiveness under grid frequency variations by following the step-by-step implementation guideline given in Section III.

Fig. 7(a) shows the experimental waveforms in Case III under a low grid voltage fault ($V_g = 40\text{V rms}$). In this scenario, R_g and L_g are estimated to be 1.007 Ω and 4.413 mH, with percent errors of only 0.7% and 0.3%, respectively. Under this grid condition, an enlarged $\Delta\theta_{pll}$ can be more easily obtained. Therefore, Fig. 6(b) presents a more distinct percent error comparison across a wider $\Delta\theta_{pll}$ range. As shown, the proposed method still consistently maintains all percent errors below 2%, in stark contrast to the conventional methods, which results in R_g estimation errors as high as 66.8% when $\Delta\theta_{pll} = 44.1^\circ$, and L_g estimation errors reaching up to 52.3% when $\Delta\theta_{pll} = 47.8^\circ$.

Fig. 7(b) shows the experimental waveforms in Case III under an unbalanced grid voltage fault (110 V rms for phases B and C, and 80 V rms for phase A), where the proposed method still works effectively by using positive sequence component

extraction techniques in the PLL [20]. In this case, R_g and L_g are estimated to be 0.995 Ω and 4.430 mH, with percent errors of only 0.5% and 0.7%, respectively. These results validate the proposed method's effectiveness under grid voltage variations.

V. CONCLUSION

A noninvasive GIE method for GFIs is proposed in this letter. This method utilizes a natural steady-state transition to estimate both grid resistance and inductance, without causing any intentional perturbations to the grid. Note that such a transition commonly occurs in practical applications, such as when distributed generation systems adjust current references based on higher level controllers, or when energy storage systems change their steady states in response to commands from energy management systems.

Furthermore, the proposed method only uses PLL's inherent operating characteristics, eliminating the need to change PLL's existing configurations. In addition, PLL phase angle variations are considered, guaranteeing precise estimation results across diverse conditions.

The experimental results verify the proposed method's effectiveness. Specifically, it consistently maintains a percent error below 2% when $\Delta\theta_{pll}$ varies from 1.2° to 47.8° . In contrast, traditional methods exhibit significantly higher estimation errors. R_g estimation errors can be as high as 66.8% when $\Delta\theta_{pll}$ is 44.1° . Similarly, L_g estimation errors can reach up to 52.3% when $\Delta\theta_{pll}$ is 47.8° . In addition, following the proposed step-by-step implementation guideline, our method also shows a percent error below 2% under grid frequency or voltage variations.

The first-order Taylor approximation can be applied to simplify calculations of (17)–(20), where the total computation time can be reduced by around 80% in our experimental setup. However, it is important to note that when higher estimation precision is needed, or $\Delta\theta_{pll}$ is not minimal, this approximation may not be suitable. In such cases, the computational load becomes a critical factor to consider for real-time applications.

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