

Letters

A Simple and Reconfigurable Wireless Power Transfer System With Constant Voltage and Constant Current Charging

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Abstract—A typical charging profile for the Li-ion batteries in electric vehicles (EVs) includes a constant current (CC) charging stage and a constant voltage (CV) charging. This letter proposes a simple and reconfigurable topology for CC and CV outputs in an EV wireless charging system. The proposed system can be switched to series-series topology for CC output and inductor-capacitor-capacitor-series topology for CV output. Only one relay is introduced, unlike existing solutions where many passive components and relays are utilized for reconfiguration. Also, there is no need for frequency variation to achieve the shift of CC and CV. An experimental prototype is implemented to validate the proposal.

Index Terms—Constant current (CC) charging, constant voltage (CV), inductor-capacitor-capacitor-series (LCC-S), reconfigurable, series-series (SS), wireless power transfer (WPT).

I. INTRODUCTION

ELECTRIC vehicles (EVs) are developing rapidly as carbon emission can be reduced when replacing gasoline cars with EVs. The charging issue is one of the major technical issues concerning EVs. Wireless charging for EVs, enjoying the advantages of safety, convenience, automation, and freedom from human labor, has received enormous attention and become a research hotspot [1]. Li-ion batteries are widely used in EVs due to its high power density. A typical charging profile for Li-ion batteries includes constant current (CC) charging followed by constant voltage (CV) charging [2]. Thus, to comply with this profile, the wireless charging should have a CC output as well as a CV output.

In the current literatures, there are multiple ways to achieve CC and CV outputs. The first method is to switch the working frequencies to achieve CC and CV outputs at different working frequencies [3]–[6]. However, zero phase angle cannot be guaranteed at these two frequencies points, introducing extra reactive

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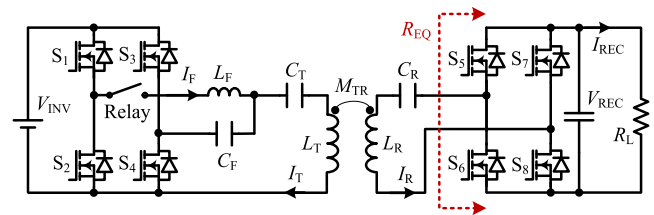


Fig. 1. Proposed topology for CC and CV outputs.

power. In addition, it is not easy to achieve the target within a limited working frequency such as from 79 to 90 kHz according to J2954 from Society of Automotive Engineers.

Another method is to switch topologies to suit for CC and CV charging [7]–[10], such as from the series-series (S-S) topology to the inductor-capacitor-capacitor-series (LCC-S) topology. In the current version using this method, many extra passive components like inductors and capacitors are introduced, or many relays are used for topology reconfiguration.

This letter proposes a novel topology to enable CC and CV outputs without introducing extra passive components. The three resonant loops in the LCC-S topology are employed to form the S-S topology. Only one relay is utilized. In this way, a simple and low-cost solution for CC and CV outputs is offered.

II. PROPOSED CC AND CV CHARGING TOPOLOGY

The proposed topology for CC and CV outputs is depicted in Fig. 1. L_T (L_R), C_T (C_R), and I_T (I_R) are the respective transmitter (receiver) self-inductance, series capacitance, and current. M_{TR} is the mutual inductance. L_F is the compensating inductance and C_F is the parallel capacitance. I_F is the current of L_F . V_{INV} is the inverter dc voltage. R_L is the load resistance. V_{REC} and I_{REC} are the charging voltage and current, respectively.

In the proposed topology, there are three resonant loops: Loop 1 consisting of L_F and C_F , Loop 2 consisting of C_F , C_T , and L_T , and Loop 3 consisting of L_R and C_R . Thus, the resonant frequency ω_0 equals

$$\omega_0 = \frac{1}{\sqrt{L_F C_F}} = \frac{1}{\sqrt{L_F \frac{C_F C_T}{C_F + C_T}}} = \frac{1}{\sqrt{L_R C_R}}. \quad (1)$$

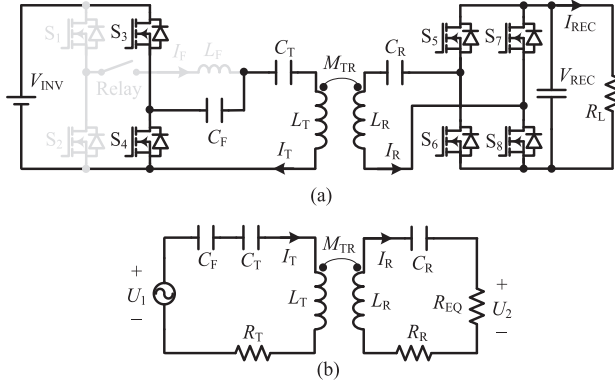


Fig. 2. Proposed system for CC output. (a) Topology. (b) Equivalent circuit.

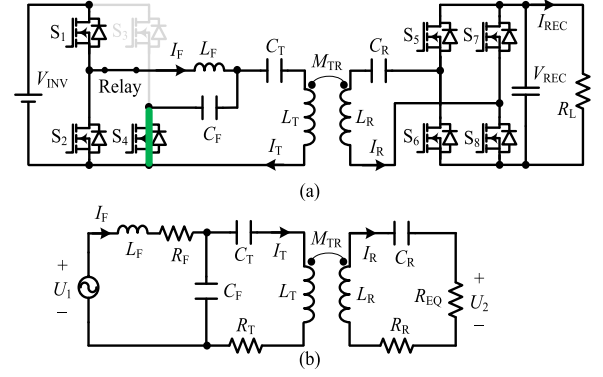


Fig. 4. Proposed system for CV output. (a) Topology. (b) Equivalent circuit.

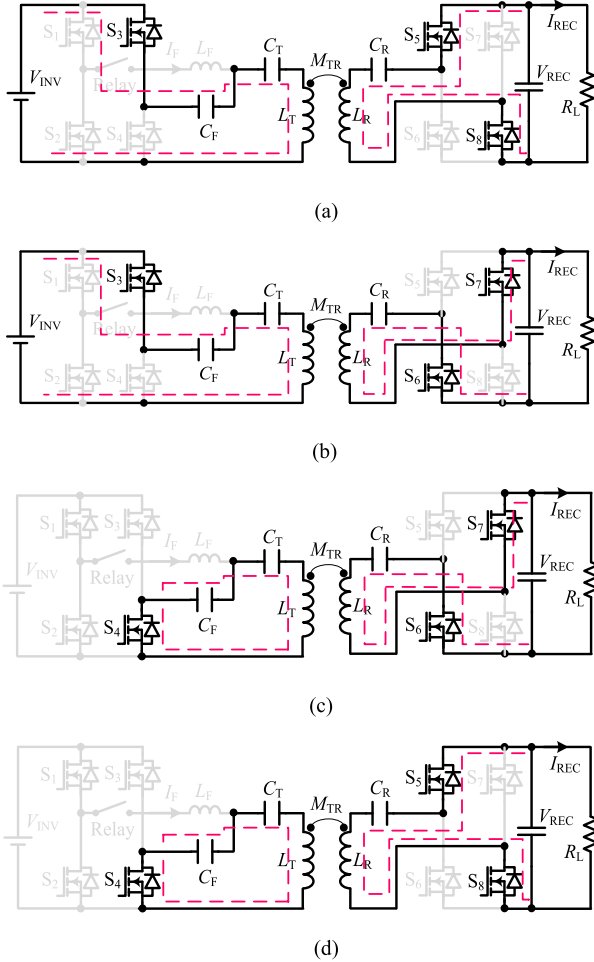


Fig. 3. Current flow paths at the CC mode. (a) With positive U_1 and positive U_2 . (b) With positive U_1 and negative U_2 . (c) With zero U_1 and negative U_2 . (d) With zero U_1 and positive U_2 .

The rectifier works at synchronous rectification. Thus, the equivalent ac load resistance R_{EQ} and the ac voltage of the

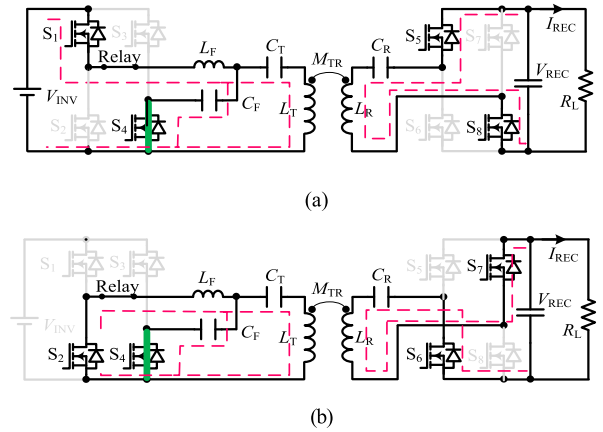


Fig. 5. Current flow paths at the CV mode. (a) With positive U_1 and positive U_2 . (b) With zero U_1 and negative U_2 .

rectifier U_2 can be expressed as

$$\begin{cases} R_{EQ} = \frac{8}{\pi^2} R_L \\ U_2 = \frac{2\sqrt{2}}{\pi} V_{REC}. \end{cases} \quad (2)$$

A. CC Output

For CC output, the relay is OFF and an S-S topology driven by a half-bridge inverter is formed, as shown in Fig. 2(a). The equivalent circuit for CC output is shown in Fig. 2(b). R_T (R_R) is the equivalent resistance of the transmitter (receiver). U_1 is the equivalent ac voltage of the inverter. The phase-shift between U_1 and U_2 is controlled with 90° at the CC mode, then its current flow paths are shown in Fig. 3.

U_1 is expressed as

$$U_1 = \frac{\sqrt{2}}{\pi} V_{INV}. \quad (3)$$

Based on the Kirchhoff voltage law (KVL) and ignoring R_T and R_R , we have

$$\begin{cases} I_T = \frac{U_1 R_{EQ}}{(\omega_0 M_{TR})^2}, I_R = \frac{U_1}{\omega_0 M_{TR}} \\ U_2 = \frac{U_1 R_{EQ}}{\omega_0 M_{TR}}. \end{cases} \quad (4)$$

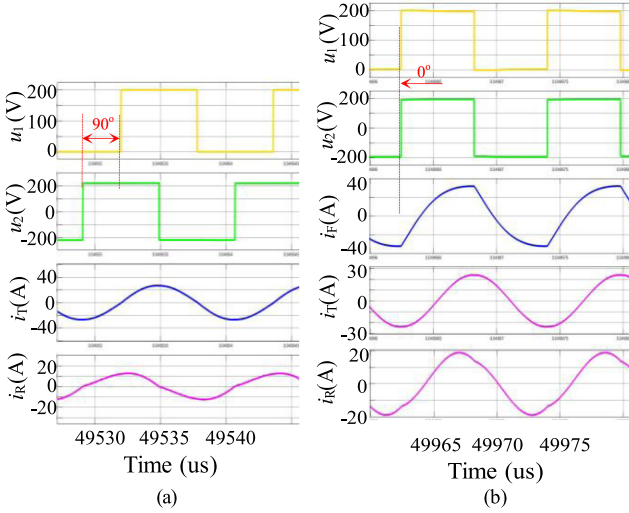


Fig. 6. Key simulation voltage and current waveforms. (a) CC output with $R_L = 28 \Omega$. (b) CV output with $R_L = 28 \Omega$.

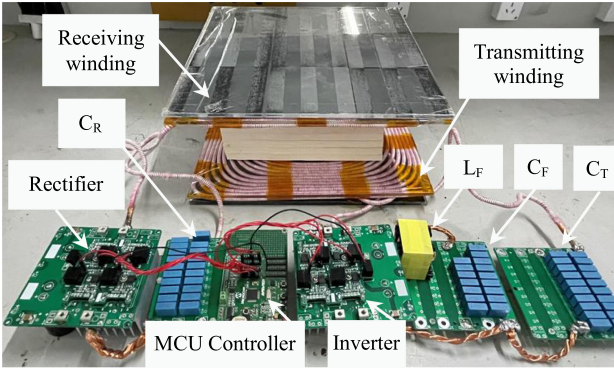


Fig. 7. Photograph of the experimental prototype.

Thus, the dc charging voltage and current are

$$I_{REC} = \frac{4}{\pi^2} \frac{V_{INV}}{\omega_0 M_{TR}}, \quad V_{REC} = \frac{4}{\pi^2} \frac{V_{INV} R_L}{\omega_0 M_{TR}}. \quad (5)$$

Considering R_T and R_R , the ac-ac efficiency can be expressed as

$$\eta = \frac{I_R^2 R_{EQ}}{I_R^2 R_{EQ} + I_T^2 R_T + I_R^2 R_R} = \frac{1}{1 + \frac{R_R}{R_{EQ}} + \frac{R_T R_{EQ}}{(\omega_0 M_{TR})^2}}. \quad (6)$$

The efficiency is maximized when

$$R_{EQ} = \sqrt{\frac{R_R}{R_T}} \omega_0 M_{TR}. \quad (7)$$

B. CV Output

For CV output, the relay and the switch S_4 are constantly ON while the switch S_3 is constantly OFF, forming an LCC-S topology driven by a half-bridge inverter, as shown in Fig. 4(a).

The equivalent circuit for CV output is shown in Fig. 4(b). R_F is the equivalent resistance of L_F . The phase-shift between U_1

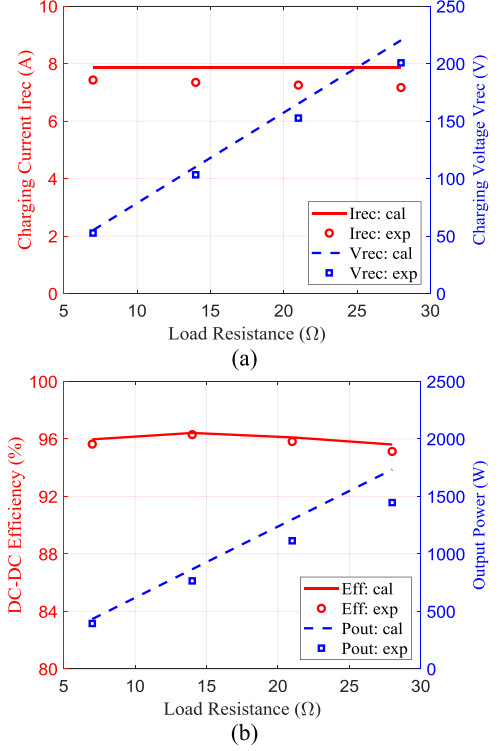


Fig. 8. Calculated and experimental results of CC output. (a) DC charging current I_{REC} and voltage V_{REC} . (b) DC-DC efficiency and output power.

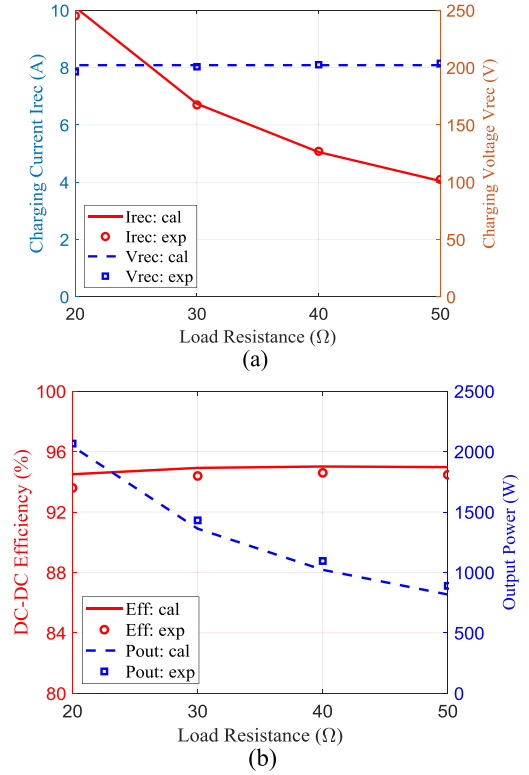
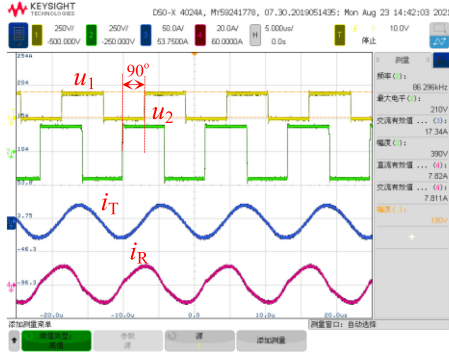
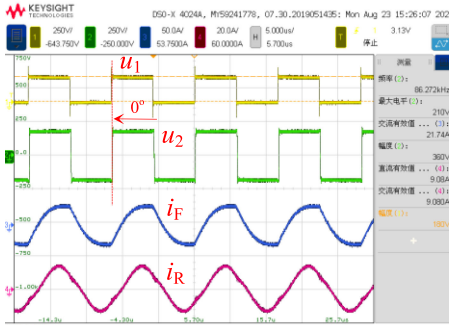


Fig. 9. Calculated and experimental results of CV output. (a) DC charging current I_{REC} and voltage V_{REC} . (b) DC-DC efficiency and output power.



(a)



(b)

Fig. 10. Experimental waveforms. (a) CC output with load resistance $R_L = 28 \Omega$. (b) CV output with load resistance $R_L = 28 \Omega$.

and U_2 is controlled with the same phase at the CV mode, then its current flow paths are shown in Fig. 5.

Based on KVL and ignoring R_F , R_T , and R_R , we have

$$\begin{cases} I_F = \left(\frac{M_{TR}}{L_F}\right)^2 \frac{U_1}{R_{EQ}}, I_T = \frac{U_1}{\omega_0 L_F} \\ I_R = \frac{M_{TR}}{L_F} \frac{U_1}{R_{EQ}}, U_2 = \frac{M_{TR}}{L_F} U_1. \end{cases} \quad (8)$$

Thus, the dc charging voltage and current are

$$\begin{cases} I_{REC} = \frac{1}{2} \frac{M_{TR}}{L_F} \frac{V_{INV}}{R_L} \\ V_{REC} = \frac{1}{2} \frac{M_{TR}}{L_F} V_{INV}. \end{cases} \quad (9)$$

Considering R_F , R_T , and R_R , the ac-ac efficiency can be expressed as

$$\eta = \frac{I_R^2 R_{EQ}}{I_R^2 R_{EQ} + I_F^2 R_F + I_T^2 R_T + I_R^2 R_R}$$

TABLE I
PARAMETERS OF SIMULATION AND EXPERIMENTAL PROTOTYPE

V_{INV} (V)	200	f (kHz)	86	M_{TR} (μ H)	19.1
				k	0.251
L_T (μ H)	76.0	L_R (μ H)	76.0	L_F (μ H)	9.4
C_F (nF)	360.1	C_T (nF)	52.4	C_R (nF)	45.1

$$= \frac{1}{1 + \frac{R_R}{R_{EQ}} + \frac{R_T R_{EQ}}{(\omega_0 M_{TR})^2} + \left(\frac{M_{TR}}{L_F}\right)^2 \frac{R_F}{R_{EQ}}}. \quad (10)$$

The efficiency is maximized when

$$R_{EQ} = \sqrt{\frac{R_R + \left(\frac{M_{TR}}{L_F}\right)^2 R_F}{R_T}} \omega_0 M_{TR}. \quad (11)$$

From (7) and (11), the optimal load resistance in the CC mode is smaller than the CV mode. This matches with the real value of load during CC-CV charging where the load resistance in the CC mode is smaller than in the CV mode.

III. SIMULATIONS AND EXPERIMENTS

Modeling and simulation of the prototype are carried out with MATLAB/Simulink, and the parameters are tabulated in Table I. The key simulation voltage and current waveforms are given in Fig. 6. An experimental prototype is implemented whose photo is depicted in Fig. 7.

The experimental results of the CC and CV outputs are shown in Figs. 8 and 9, respectively. CC and CV outputs have been achieved.

The experimental waveforms of both CC and CV outputs are depicted in Fig. 10. The experimental waveforms are consistent with the simulation.

IV. CONCLUSION

This letter proposed a simple and reconfigurable topology for CC and CV outputs in a wireless charging system. The LCC - S topology can be reconfigured to the S - S topology by using the three resonant loops. Unlike the existing solutions where many passive elements and relays are utilized, the proposed solution only adopts a relay, and no extra inductors or capacitors are introduced compared with the LCC - S topology. In addition, the system works at one single frequency, easily complying with existing standards. A prototype was developed. The experimental results validated the effectiveness of the proposed system.

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