

# Discrete-Time Super-Twisting Sliding Mode Current Controller With Fixed Switching Frequency for Switched Reluctance Motors

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**Abstract**—This article proposes a discrete-time super-twisting sliding mode (DTSTSM) current controller for switched reluctance motors. A novel gain design procedure is proposed, guaranteeing a systematic and straightforward design stage. The method makes use of a cost function, enabling the gains that yield the best trade-off between reference tracking and low current ripple to be determined. The procedure is repeated for different speed values, allowing for a variable gain structure, where the controller gains are adjusted online based on the rotor speed. The proposal presents a simple structure, not requiring knowledge of the machine parameters during implementation, along with a pulsewidth modulation scheme, ensuring a fixed switching frequency. The DTSTSM controller is obtained from a discrete-time model and stability analysis is developed using a Lyapunov approach, taking into account the one sample implementation delay present in digital control applications. Experimental results are provided to demonstrate the effectiveness of the proposed approach, where a conventional hysteresis controller is used as a benchmark. Results show that the proposal is able to deliver adequate reference tracking while using significantly lower sampling frequencies. Moreover, when compared to the hysteresis controller, superior tracking is observed at low speeds, while maintaining a similar level of computational complexity.

**Index Terms**—Current control, discrete-time control, stability, super-twisting sliding mode control, switched reluctance motor.

## I. INTRODUCTION

SWITCHED reluctance motors (SRMs) have gained interest in the past couple of decades, emerging as an alternative for already established electrical machines, such as induction and permanent magnet machines. SRMs present a simple structure, robustness, straightforward manufacturing process, and are

inherently fault tolerant. Additionally, the lack of windings or permanent magnets in the rotor structure is one of its main advantages, driving down the cost of the machine [1]–[4]. It, however, presents significant challenges such as a highly nonlinear behavior, high torque ripple, and acoustic noise [1]. Torque ripple can be reduced with the use of torque sharing functions (TSFs), a technique that allows a constant torque to be produced by generating adequate current references for each active phase. The performance of such strategy, however, relies on accurate current controllers [5].

Hysteresis control is the most commonly used strategy for the current control of SRMs, due to its simplicity and independence of the model [1], [6]. This technique also presents advantages such as fast dynamic response, high robustness, and lack of sensitivity for parametric variations. However, it presents drawbacks such as a variable switching frequency, due to the varying phase inductance. Moreover, during digital implementation, sampling limitations lead to higher current ripple and, consequently, torque ripple. These issues are significantly more noticeable in low-speed conditions and in SRMs with low inductance values. Increasing the sampling frequency can be used to improve the performance of hysteresis controllers, reducing the current ripple [7]. However, it also tends to increase the cost of the overall control system.

In order to avoid the aforementioned issues, SRM current control strategies with fixed switching frequency have been investigated in the past few years [8], [9]. An adaptive pulsewidth modulation (PWM) current controller is detailed in [10]. The proposal makes use of online gain adaptation, warranting robustness to the algorithm while ensuring suitable dynamic responses. In addition, an improved sampling method is designed, which successfully avoids the PWM delay. A variable gain proportional–integral (PI) controller is described in [11]. Gain adaptation is done with respect to current and position, aiming to ensure stability. The back electromotive force (EMF) of the motor is also taken into account to further improve the performance of the controller. These techniques are known as model-dependent, given that they make use of machine magnetization data. This information is normally stored in lookup tables, requiring knowledge of the machine parameters and significant memory allocation.

As an alternative, linearized models have also been used for designing SRM current controllers. In [12], the inductance

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characteristics of the SRM are utilized in order to build a small signal model, which is subsequently employed in the design process of a fixed-gain PI controller. A back-EMF estimation algorithm is also developed and used as a feedforward action. The performance of the controller is comparable to a variable gain PI with compensation. Strategies based on model predictive control (MPC) are presented in [13]–[16]. Continuous control set MPC benefits from a fixed switching frequency, as opposed to finite control set MPC. The technique presents advantages of fast dynamic responses, an optimal control law, and is capable of encompassing nonlinearities in the predictive model. On the flip side, it typically presents high computational burden [16].

The use of sliding mode algorithms for SRMs has been reported in [17] and [18]. The technique is desirable as it delivers robustness, while also enabling PWM implementation. An adaptive sliding mode controller based on a linear SRM model is presented in [17]. In [18], an integral sliding mode current controller with constant switching frequency is proposed. The design is carried out based on the equivalent circuit of the SRM, and a stability analysis is presented with known and bounded uncertain motor parameters. Experimental results show that the proposal has a similar response to a higher sampling hysteresis controller, with advantages such as a lower sampling rate and fixed switching frequency. The algorithm is developed in continuous time and no discrete-time stability is discussed. The technique has been successfully extended to mutually coupled SRMs in [19] and [20].

The main drawback of discrete-time sliding mode controllers is an effect known as chattering, an oscillation of finite amplitude and frequency. Higher order sliding modes, such as the super-twisting algorithm [21], are successful at circumventing this issue without notable compromises [22], [23]. In [24], a second-order sliding mode (SOSM) current controller for SRMs is implemented. A fixed-gain structure is used and no stability analysis is presented. Recently, a SOSM current controller was proposed in [25]. The authors compare it to a traditional sliding mode approach, with the proposal showing superior performance, especially regarding chattering. It is noted, however, that no attempt of a super-twisting controller has been presented in the literature regarding SRM current control.

This article presents as a main contribution a novel discrete-time super-twisting sliding mode (DTSTSM) current control scheme for SRMs. The proposed controller presents robustness, while maintaining a simple structure, not requiring any knowledge of the machine parameters on implementation. In addition, the proposal presents enhanced tracking performance, delivering lower current and torque ripple, especially in the low to medium speed range. The strategy is implemented with the use of PWM, ensuring a fixed switching frequency. The stability analysis is investigated by means of a Lyapunov approach and, unlike what has been previously reported in the literature, the digital control implementation delay is taken into account, through an augmented model. One difficulty of super-twisting sliding mode controllers is the appropriate gain selection. To overcome this issue and guarantee a systematic and straightforward design stage, a third contribution is given by a novel gain design procedure, where a cost function is used to determine the suitable controller

gains, leading to fast dynamic responses and low current ripple. The procedure is repeated for different speeds, allowing the DTSTSM controller gains to be adjusted online, according to the rotor speed. Experimental results are provided to demonstrate the effectiveness of the DTSTSM controller, where a conventional hysteresis controller is used for comparison in terms of sampling frequencies, computational complexity, current, and torque root-mean-square errors (RMSEs).

The main contributions of this article can be summarized as follows.

- 1) A novel DTSTSM current controller with fixed switching frequency for SRMs. Compared with other PWM-based controllers, the proposed controller does not require lookup tables or any knowledge of the machine parameters on hardware implementation, presenting a simple structure and robust performance.
- 2) An original stability analysis, using an augmented model that takes into account the one sample digital implementation delay.
- 3) A systematic and straightforward gain design procedure for super-twisting sliding mode controllers. In addition, gain adaptation is employed with respect to rotor speed, allowing improved tracking over a wide range of speed.

## II. MODELING OF THE SWITCHED RELUCTANCE MOTOR

The SRM is excited exclusively by its stator windings [1]. By disregarding phase coupling, the voltage at the terminals of an SRM is

$$v_{ph} = Ri + \frac{d\phi}{dt} \quad (1)$$

where  $v_{ph}$  is the phase voltage,  $R$  is the winding resistance,  $i$  is the phase current, and  $\phi$  is the flux linkage. As a result of the double salient structure of the machine, flux linkage is a nonlinear function of both current and rotor position,  $\theta$ .

$$\phi(i, \theta) = L(i, \theta)i(t). \quad (2)$$

Substituting (2) in (1) and computing the derivative yields

$$v_{ph} = Ri + L(\theta, i)\frac{di}{dt} + i\frac{dL(\theta, i)}{dt}. \quad (3)$$

Expanding the inductance derivative with respect to time yields

$$\frac{dL(\theta, i)}{dt} = \frac{\partial L(\theta, i)}{\partial \theta} \frac{d\theta}{dt} + \frac{\partial L(\theta, i)}{\partial i} \frac{di}{dt}. \quad (4)$$

Substituting (4) in (3) and rewriting the terms results

$$v_{ph} = Ri + l(\theta, i)\frac{di}{dt} + \epsilon \quad (5)$$

where

$$l(\theta, i) = L(\theta, i)\frac{di}{dt} + i\frac{\partial L(\theta, i)}{\partial i} \quad (6)$$

$$\epsilon = i\omega_r \frac{\partial L(\theta, i)}{\partial \theta}. \quad (7)$$

The first term,  $l(\theta, i)$ , is referred to as incremental inductance [26]. It accounts for the effects of magnetic saturation, by

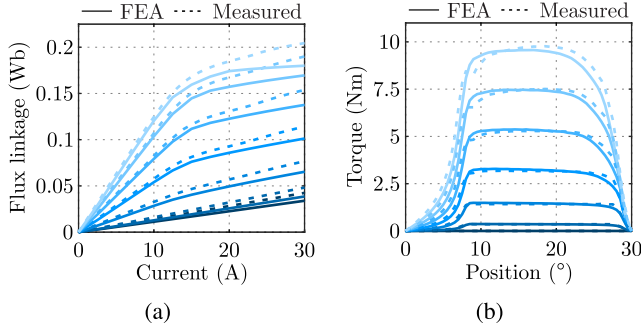


Fig. 1. Characteristics of the 8/6 studied SRM. (a) Flux linkage profile. (b) Torque profile.

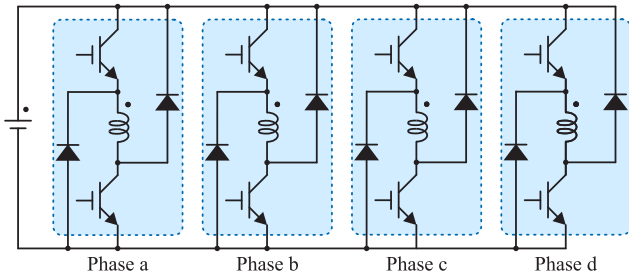


Fig. 2. Four-phase asymmetric half-bridge converter.

TABLE I  
PARAMETERS OF THE STUDIED SRM

Parameter	Value
Rated speed	6000 r/min
Rated power	5.1 kW
Rated torque	8 Nm
Rated voltage	300 V
Rated current (Phase RMS value)	14.35 A
Number of phases	4
Stator poles	8
Rotor poles	6

considering current, self-inductance  $L(\theta, i)$ , and the inductance variation caused by current. The term  $\epsilon$  corresponds to the machine's back-EMF.

A simulation model of the SRM is constructed on MATLAB/Simulink software, using lookup tables based on finite element analysis (FEA) data [1]. The flux linkage and torque profiles of the studied 8/6 SRM are presented in Fig. 1. A four-phase asymmetric half-bridge (AHB) converter, shown in Fig. 2, is used to drive the machine. Additional parameters of the machine are shown in Table I.

### III. DISCRETE-TIME SUPER-TWISTING SLIDING MODE CURRENT CONTROLLER

Conventional discrete-time sliding mode control strategies exhibit chattering as a major drawback in practical applications. Chattering is a phenomenon often referred to as oscillations of finite amplitude and frequency, which occur as a result of the sliding action of the controller. This effect is undesirable as it

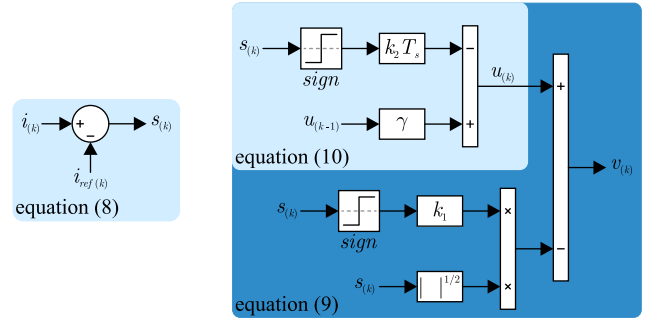


Fig. 3. Block diagram of the DTSTSM controller.

can decrease the control accuracy and, on SRM current controllers, lead to increased current ripple [27]. The super-twisting algorithm, proposed by Levant [21], is able to effectively reduce chattering, without major compromises [22], [23]. In this section, a DTSTSM current controller for an SRM is described. A novel stability proof, considering the digital implementation delay through an augmented model, is also presented.

#### A. Controller Structure

First, consider a sliding surface,  $s(k)$ , defined as

$$s(k) = e(k) = i(k) - i_{ref}(k) \quad (8)$$

where  $e(k)$  is the current tracking error,  $i(k)$  is the measured phase current, and  $i_{ref}(k)$  is the reference current. In accordance with [28], a DTSTSM control law can be written as

$$v(k) = -k_1 |s(k)|^{\frac{1}{2}} \text{sign}(s(k)) + u(k) \quad (9)$$

$$u(k) = \gamma u(k-1) - k_2 T_s \text{sign}(s(k)) \quad (10)$$

where  $v(k)$  is the controller output,  $u(k)$  is the auxiliary variable of the DTSTSM controller,  $\gamma$  is a fixed gain with  $0 < \gamma < 1$ ,  $k_1$  and  $k_2$  are the controller gains, and  $T_s$  is the sampling period.

The signal function,  $\text{sign}(s(k))$ , is defined as

$$\text{sign}(s(k)) = \begin{cases} 1 & \text{if } s(k) > 0 \\ 0 & \text{if } s(k) = 0 \\ -1 & \text{if } s(k) < 0. \end{cases} \quad (11)$$

A block diagram of the DTSTSM controller, described in (8)–(10), is presented in Fig. 3. Note that for implementation, the controller only requires knowledge of the gains as well as the reference and measured currents, not requiring any lookup table or additional machine parameters.

#### B. Stability Analysis

In digitally controlled motor drives, it is a common practice to sample the phase currents and rotor position at the same instant that the voltage commands are updated. In symmetric PWM applications, these variables are updated once per cycle, thus resulting in an intrinsic delay of  $T_s$ , as depicted in Fig. 4 [29]. If not properly addressed, this phenomenon may affect the stability of the closed-loop system.

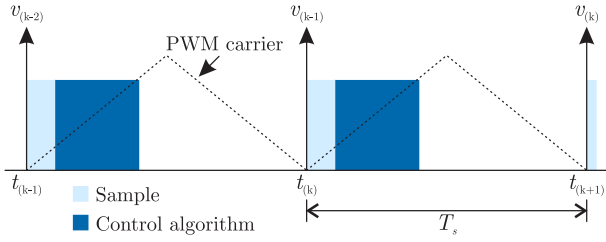


Fig. 4. Digital implementation delay in discrete-time control systems using symmetric PWM.

In order to address this issue, first, the discrete form of (5) can be obtained by using the forward Euler method, as follows:

$$\dot{i}_{(k+1)} = \left(1 - T_s \frac{R}{l(\theta, i)}\right) \dot{i}_{(k)} - \frac{T_s}{l(\theta, i)} \epsilon^{(k)} + \frac{T_s}{l(\theta, i)} v_{(k)}. \quad (12)$$

Next, an augmented model can be derived from (12), where an additional state,  $v_{d(k)}$ , is included to account for the digital control implementation delay

$$\begin{bmatrix} \dot{i}_{(k+1)} \\ v_{d(k+1)} \end{bmatrix} = \begin{bmatrix} 1 - T_s \frac{R}{l(\theta, i)} & \frac{T_s}{l(\theta, i)} \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \dot{i}_{(k)} \\ v_{d(k)} \end{bmatrix} + \begin{bmatrix} 0 \\ 1 \end{bmatrix} v_{(k)} + \begin{bmatrix} -\frac{T_s}{l(\theta, i)} \epsilon^{(k)} \\ 0 \end{bmatrix}. \quad (13)$$

Then, considering the model presented in (13) and the controller given in (9) and (10), the nonlinear augmented system can be rewritten in the form of

$$\mathbf{x}_{(k+1)} = \mathbf{A} \mathbf{x}_{(k)} + \mathbf{B}_{(k)} s_{(k)} + \boldsymbol{\xi}_{(k)} \quad (14)$$

where

$$\begin{aligned} \mathbf{x}_{(k)} &= [x_{1(k)} \quad x_{2(k)} \quad x_{3(k)}]^T \\ &= [s_{(k)} \quad v_{d(k)} \quad u_{(k)}]^T \end{aligned} \quad (15)$$

$$\mathbf{A} = \begin{bmatrix} 1 - T_s \frac{R}{l(\theta, i)} & \frac{T_s}{l(\theta, i)} & 0 \\ 0 & 0 & 1 \\ 0 & 0 & \gamma \end{bmatrix} \quad (16)$$

$$\mathbf{B}_{(k)} = \begin{bmatrix} 0 & -k_1 |s_{(k)}|^{\frac{1}{2}} & -k_2 T_s \end{bmatrix}^T \quad (17)$$

$$\begin{aligned} \boldsymbol{\xi}_{(k)} &= [\xi_{1(k)} \quad 0 \quad 0]^T \\ &= \left[ -\frac{T_s}{l(\theta, i)} \epsilon^{(k)} - \left( i_{ref(k+1)} - \left(1 - T_s \frac{R}{l(\theta, i)}\right) i_{ref(k)} \right) \quad 0 \quad 0 \right]^T. \end{aligned} \quad (18)$$

The stability of the closed-loop system, taking the one sample delay into account, is given by the following theorem.

**Theorem 1:** Consider the nonlinear system given in (14), with gains chosen as  $k_1 > 0$ ,  $k_2 > 0$ ,  $0 < \gamma < 1$  and if the following linear matrix inequality (LMI) [23], [30], [31]

$$\mathbf{A}^T (\mathbf{P} + \mathbf{P} (\boldsymbol{\Lambda}_1 + \boldsymbol{\Lambda}_2) \mathbf{P}) \mathbf{A} - (1 - \rho) \mathbf{P} + \mathbf{Q} \leq 0 \quad (19)$$

has a positive-definite solution  $\mathbf{P} = \mathbf{P}^T > 0$  for a given  $\mathbf{Q} = \mathbf{Q}^T > 0$ ,  $\boldsymbol{\Lambda}_1 = \boldsymbol{\Lambda}_1^T > 0$ , and  $\boldsymbol{\Lambda}_2 = \boldsymbol{\Lambda}_2^T > 0$ , then the trajectories of the system described in (14) converge asymptotically to a

region centered at the origin  $\mathbf{O} = \{x : \|x\|^2 < r\}$ , with a radius given by

$$r = \frac{c}{\rho} \quad (20)$$

where

$$0 < \rho < 1$$

$$c = \left( \frac{\alpha_2}{2\sqrt{\alpha_1}} \right)^2 + \alpha_3$$

$$\alpha_1 = \lambda_{\min}(\mathbf{Q}) > 0$$

$$\alpha_2 = \lambda_{\max}(\mathbf{Z}) k_1^2$$

$$\alpha_3 = \lambda_{\max}(\mathbf{Z}) (k_2 T_s)^2 + \lambda_{\max}(\mathbf{H}) \xi_{1(k)}^2$$

$$\mathbf{Z} = \mathbf{Z}^T = \boldsymbol{\Lambda}_1^{-1} + \boldsymbol{\Lambda}_3^{-1} + \mathbf{P} > 0$$

$$\mathbf{H} = \mathbf{H}^T = \boldsymbol{\Lambda}_2^{-1} + \mathbf{P} \boldsymbol{\Lambda}_3 \mathbf{P} + \mathbf{P} > 0.$$

The detailed proof for Theorem 1 is presented in the Appendix.

#### IV. GAIN DESIGN PROCEDURE

The proper selection of a DTSTSM controller gains is necessary in order to guarantee acceptable performance and low chattering [22]. Due to the wide gain constraint intervals, traditional sliding mode control design is not an efficient approach. Moreover, for the SRM current control application, different sets of gains lead to different settling times and current ripple values. Generally, larger gains lead to faster settling times; however, they also tend to increase overshoot and current ripple. Smaller gains, on the other hand, often present a slower response along with a lower current ripple. Given the highly nonlinear behavior of the SRM, the use of analytical formulations to determine the controller gains becomes a complex task. Additionally, the back-EMF of the machine plays an important role, generally degrading the tracking capability of the controller at higher speeds [12].

In this context, an optimization-based design can be an interesting tool. Using an offline grid search algorithm allows several points of a defined search space to be evaluated, where a cost function is used to determine suitable controller gains, which present the best trade-off between reference tracking and low current ripple. Moreover, the performance of the controller can be improved by online gain adaption, with respect to the rotor speed. In the following subsections, a novel gain design procedure for a DTSTSM controller and a design example are presented, respectively.

##### A. Design Procedure

To guide the algorithm, a cost function,  $F(k_1, k_2 T_s)$ , is defined as the sum of the absolute value of the current tracking error

$$F(k_1, k_2 T_s) = \max \left( \sum_0^{\theta_{exc}} |s_{(k)}| \right) \quad (21)$$

where  $\theta_{exc}$  is the excitation period.

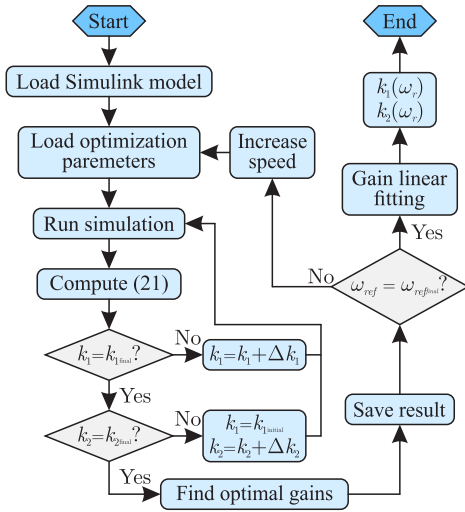


Fig. 5. Flowchart of the proposed gain design procedure.

This choice is made as a means to find a set of gains that present good tracking capability, with both a fast transient response and low current ripple. Note that the cost function is calculated during the excitation interval, when the reference phase current is greater than zero.

The proposed design procedure is presented in Fig. 5. The procedure is performed offline and starts with the simulation model being loaded in MATLAB/Simulink. Then, the optimization parameters are set, them being the search space and the gain increments. Following this, the simulation procedure starts, where one simulation is performed for each pair of gains. At the end of each simulation, the cost function is calculated and the value is stored. Once every simulation has been executed, the gains that yielded the lowest cost function value are stored as the optimal gains for that particular speed. Following this, the reference speed is incremented and the procedure is repeated until the every desired speed value is evaluated. Finally, a linear fitting is performed, where the optimal gains are fitted to a straight line, allowing simple digital implementation.

### B. Design Example

This section aims to present a gain design example for the DTSTSM current controller using the proposed methodology, while also providing experimental validation in the following section.

Consider the 8/6 SRM with the parameters shown in Table I and Fig. 1, using the SRM model depicted in the end of Section II. The sampling and switching frequencies are set to 30 kHz, and a PWM with triangular carrier is used. The reference phase currents are generated according to the TSF presented in [32]. In this case, the search space was defined as  $50 \leq k_1 \leq 300$ , evaluated at  $\Delta k_1 = 5$  increments, and  $1 \leq k_2 T_s \leq 15$ , evaluated at  $\Delta k_2 T_s = 0.5$  increments. Simulations are performed for every gain combination inside the interval. In order to show the effects of different gain combinations, Fig. 6 presents the simulation results for four different sets of gains at a speed of 1000 r/min. Note how different gains affect tracking capability

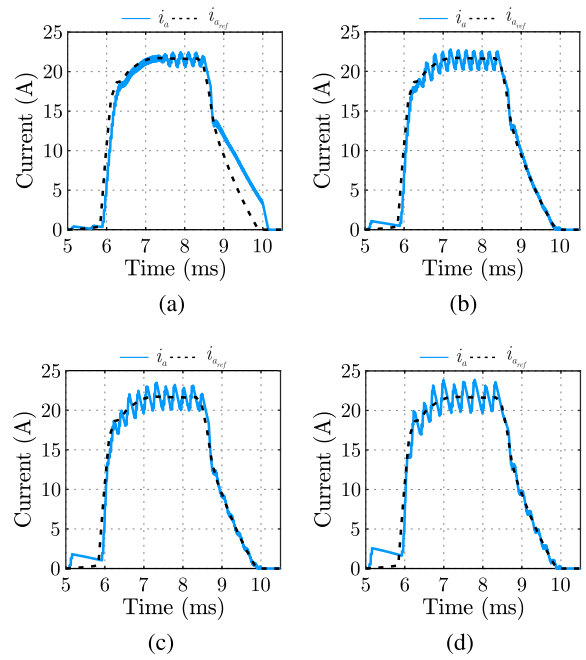


Fig. 6. Simulation results for different gain combinations at 1000 r/min with a 6 N·m load. (a)  $k_1 = 50$  and  $k_2 T_s = 1$ . (b)  $k_1 = 100$  and  $k_2 T_s = 5$ . (c)  $k_1 = 150$  and  $k_2 T_s = 10$ . (d)  $k_1 = 200$  and  $k_2 T_s = 15$ .

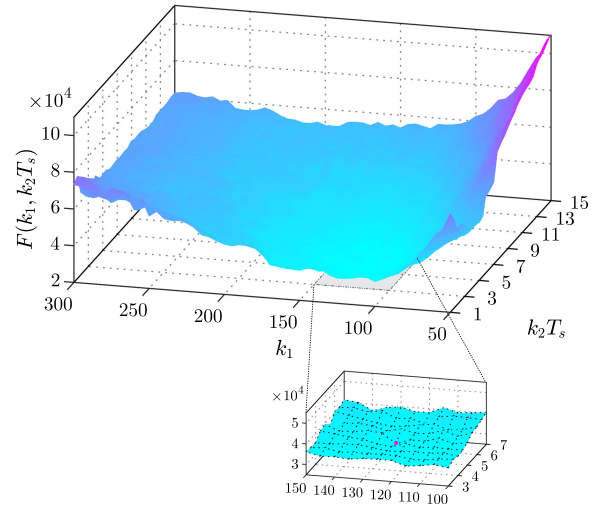


Fig. 7. Cost function values at a speed of 1000 r/min, highlighting the region of the search space containing the optimal gain combination.

and current ripple. The cost function values for the entire search space at the same speed are presented in Fig. 7. In this case, the gains that yielded the best performance (i.e., minimize the cost function) are  $k_1 = 125$  and  $k_2 T_s = 5$ . The phase current and cost function waveforms for this set of gains, at a speed of 1000 r/min, are presented in Fig. 8.

Given that the performance of the controller can be enhanced by gain adjustment, the procedure is repeated in 500 r/min increments, until reaching the speed of 3000 r/min. After that, a linear fitting of the resulting gains is performed, allowing for simple implementation. The optimal results as well as the linear regressions are presented in Fig. 9.

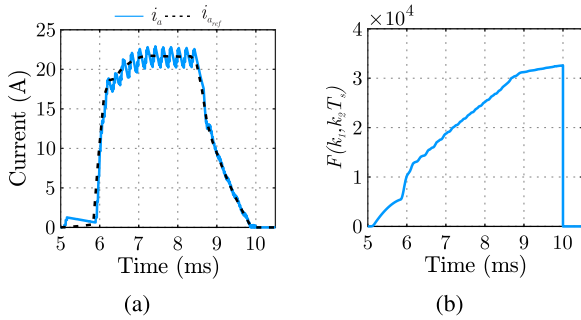


Fig. 8. Simulation results for the optimal gains at a speed of 1000 r/min. (a) Phase current. (b) Cost function.

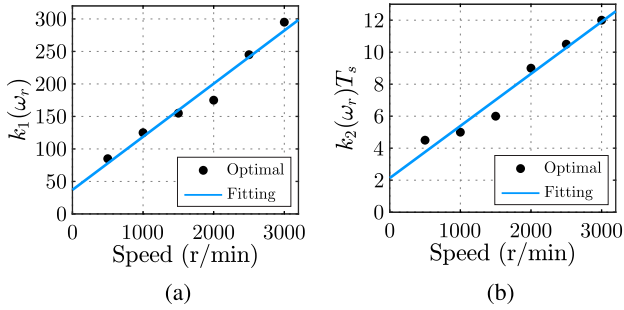


Fig. 9. Optimal gains for the proposed DTSTSM controller as a function of speed. (a)  $k_1$ . (b)  $k_2 T_s$ .

For digital implementation, gains  $k_1$  and  $k_2 T_s$  are calculated according to (22) and (23), respectively

$$k_1(\omega_r) = 0.08171 |\omega_r| + 37 \quad (22)$$

$$k_2(\omega_r) T_s = 0.003257 |\omega_r| + 2.133. \quad (23)$$

It is important to highlight that the proposed design procedure is carried out offline, not demanding any sort of online tuning. In addition, it must be pointed out that the gains obtained by simulation results are directly used for experimental validation, not requiring any additional design steps. Finally, note that multiple load values are not considered in the gain design procedure. This is done as a means to maintain a simple controller structure, avoiding the use of lookup tables during implementation and ensuring a faster design stage. Moreover, given the robustness of the DTSTSM controller, the above-mentioned are reasonable design choices.

## V. EXPERIMENTAL RESULTS

In this section, experimental tests are conducted in order to verify the effectiveness of the proposed DTSTSM current controller. The characteristics of the SRM used are presented in Fig. 1, while additional parameters of the machine are given in Table I. Fig. 10 shows the experimental test bench used to obtain the results. The setup is composed of an SRM, a four-phase AHB converter, and an induction machine (IM), coupled to the SRM. The load torque is imposed by the IM, which operates as a generator, while the SRM is controlled with predetermined current references [33].

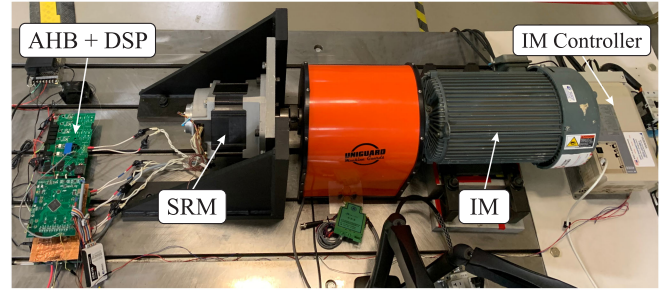


Fig. 10. Experimental test bench encompassing the drive, the SRM, and an IM, used to provide a mechanical load.

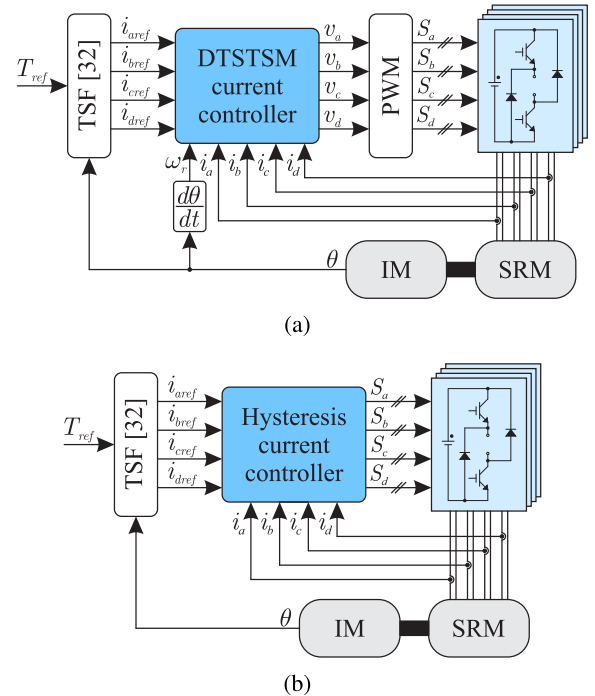


Fig. 11. Block diagram of the current control strategies. (a) DTSTSM controller. (b) Hysteresis controller.

Fig. 11(a) shows the block diagram of the proposed current control structure. A digital signal processor (DSP) TMS320F28335 is used for digital implementation. The sampling and switching frequencies are set to  $f_s = f_{sw} = 30$  kHz, and a PWM with triangular carrier is used. The duty cycle used in the PWM is given by  $v_{(k)}/V_{dc}$  in soft chopping mode and by  $0.5 + 0.5(v_{(k)}/V_{dc})$  in hard chopping mode, where  $V_{dc}$  is the dc bus voltage [10]. The process is depicted in Fig. 12. Soft chopping is applied while  $\theta < \theta_{off}$ , while hard chopping is used after  $\theta > \theta_{off}$ , with the turn-OFF angles being stored in a lookup table. Analogous reasoning is used for hysteresis current control.

The reference phase currents are generated according to the TSF presented in [32]. For digital implementation, the DTSTSM controller gains are calculated according to (22) and (23). For comparison purposes, hysteresis current control with a hysteresis band of 1 A was applied to the same SRM, as depicted in Fig. 11(b). The sampling frequency was set to 57 kHz, the highest achievable value considering the limited performance

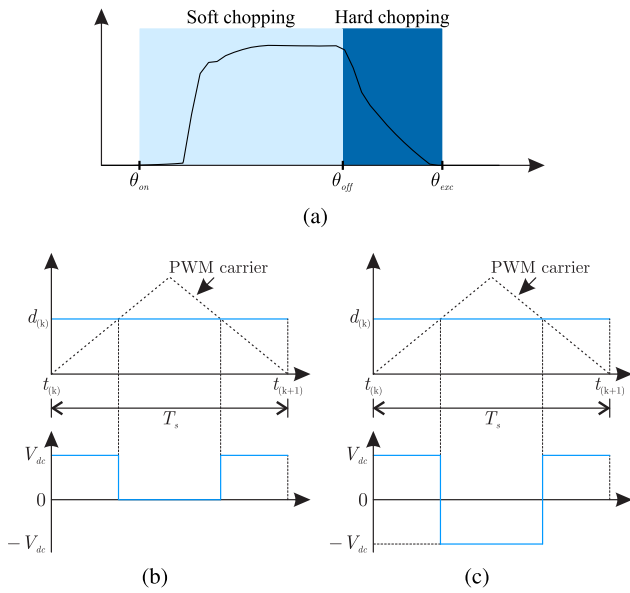


Fig. 12. PWM characteristics. (a) Current reference example with chopping regions highlighted. (b) Soft chopping waveforms. (c) Hard chopping waveforms.

of the DSP used [33]. The higher value when compared to the DTSTSM controller is chosen purposefully, allowing for a comparable performance with the proposed controller [18]. Due to digital implementation, the hysteresis controller also presents a one sample delay.

As a means to measure the tracking capability and current ripple of the proposed controller, which directly affect the torque ripple characteristic of the SRM, measurements of the current and torque RMSE are performed [18], [34]. The current and torque RMSE are defined as, respectively

$$i_{\text{RMSE}} = \sqrt{\frac{1}{N} \sum_{n=1}^N (i_{\text{ref}(k)} - i_{(k)})^2} \quad (24)$$

$$T_{\text{RMSE}} = \sqrt{\frac{1}{N} \sum_{n=1}^N (T_{\text{ref}(k)} - T_{e(k)})^2} \quad (25)$$

where  $N$  is the number of data points.

### A. Steady-State Performance

Fig. 13 shows the experimental results for the proposed DTSTSM and hysteresis controllers under a 3 N·m load at a speed of 500 r/min. A 12-bit digital-to-analog converter is used to measure the reference current and instantaneous torque waveforms, with the latter making use of a lookup table containing characteristics of the studied SRM. It can be observed that the proposal presents a fast transient response as well as adequate reference tracking. The robustness of the proposal can also be observed, as it operates properly in the region of magnetic saturation. When compared with the hysteresis controller, significantly lower current and torque ripple are observed, while using close to a 50% smaller sampling frequency.

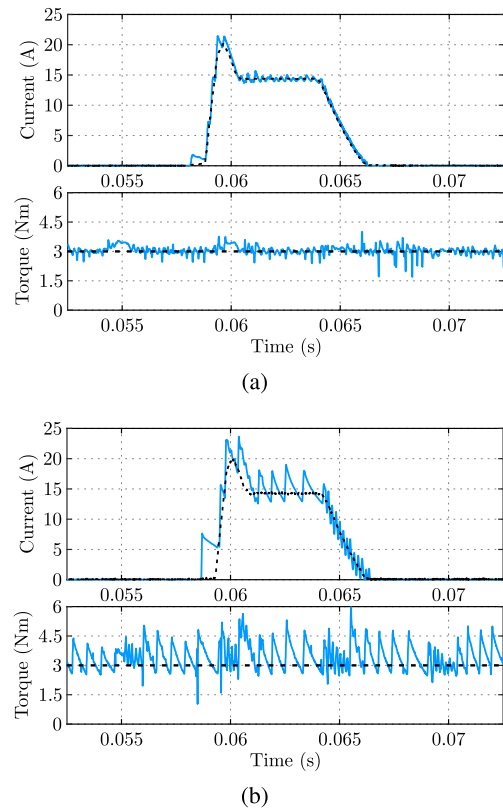


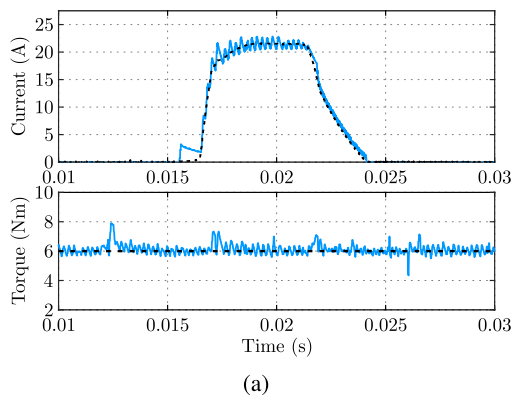
Fig. 13. Experimental results for a speed of 500 r/min and a 3 N·m load. (a) DTSTSM controller ( $f_s=30$  kHz). (b) Hysteresis controller ( $f_s=57$  kHz).

In order to verify the robustness of the controller to different load levels, Fig. 14 presents the experimental results for the proposed DTSTSM and hysteresis controllers under a 6 N·m load, still at a speed of 500 r/min. Once more, the proposal presents suitable tracking along with reduced current ripple. In addition, it can be seen that the controller is able to maintain the expected behavior for different load conditions, while making use of the same gains.

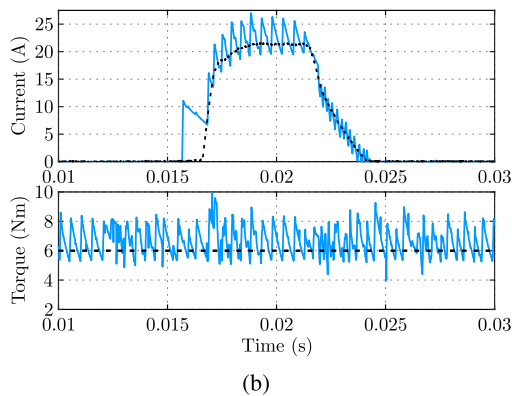
In order to evaluate the performance of the controllers at different speeds, experimental results for both controllers under a 6 N·m load at the speeds of 1000, 2000, and 3000 r/min are presented in Figs. 15–17, respectively. It can be seen that the proposal presents good reference tracking even at higher speeds. As an example, it can be seen that at 3000 r/min, the DTSTSM is able to present comparable tracking to the hysteresis controller, while making use of a reduced sampling rate and guaranteeing a fixed switching frequency.

A comprehensive quantitative analysis is shown in Tables II and III, where measurements of current and torque RMSE are presented for both controllers under 3 and 6 N·m loads, respectively. The tests are carried out at the speeds of 500, 1000, 2000, and 3000 r/min.

As seen in Tables II and III, the proposed controller presents smaller current and torque RMSE values across the evaluated speed range when compared to hysteresis control. It should be noted that with the increase in speed, and consequently back-EMF, the response of the hysteresis controller improves;

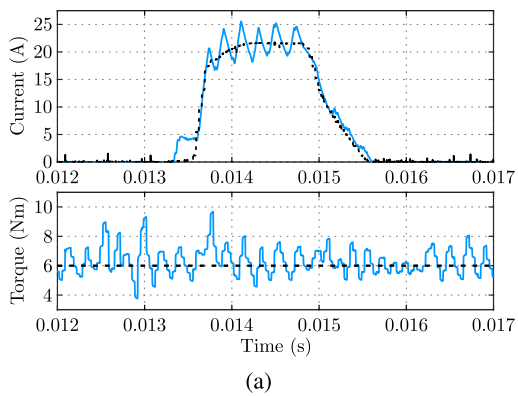


(a)

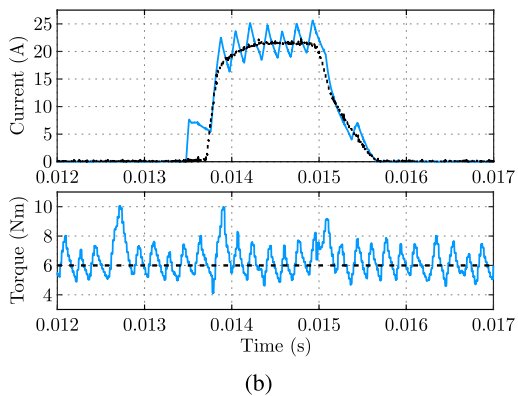


(b)

Fig. 14. Experimental results for a speed of 500 r/min and a 6 N-m load. (a) DTSTSM controller ( $f_s=30$  kHz). (b) Hysteresis controller ( $f_s=57$  kHz).

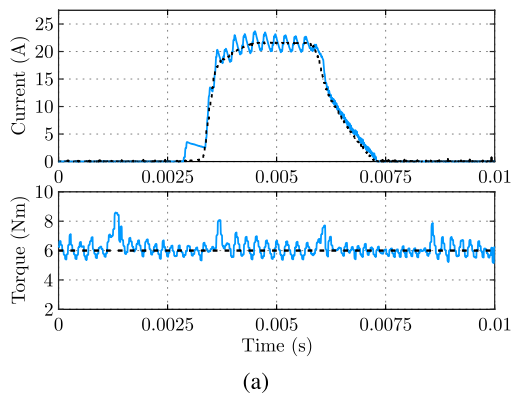


(a)

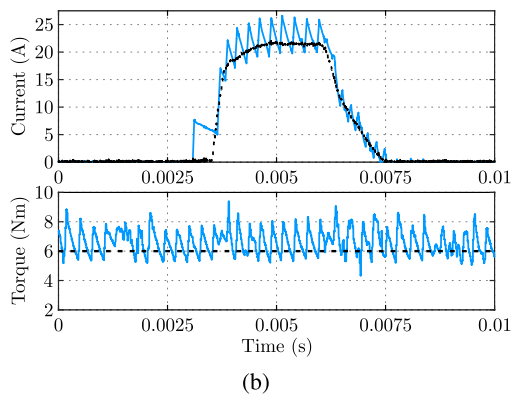


(b)

Fig. 16. Experimental results for a speed of 2000 r/min and a 6 N-m load. (a) DTSTSM controller ( $f_s=30$  kHz). (b) Hysteresis controller ( $f_s=57$  kHz).

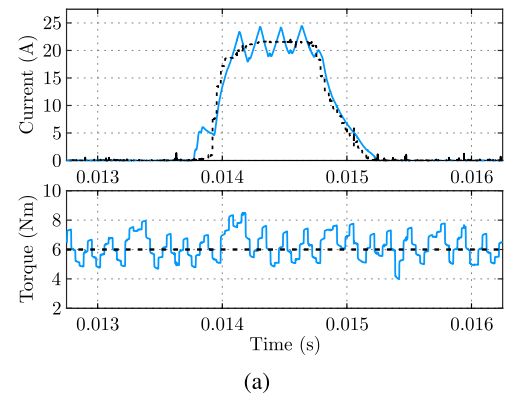


(a)

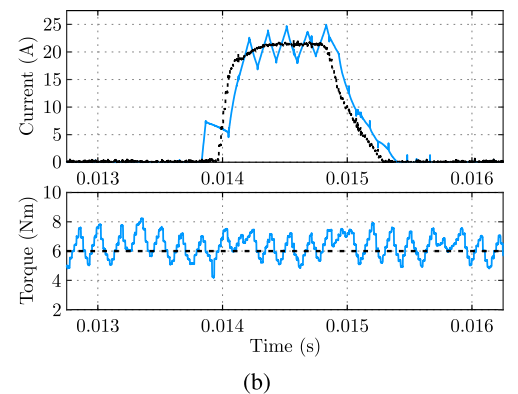


(b)

Fig. 15. Experimental results for a speed of 1000 r/min and a 6 N-m load. (a) DTSTSM controller ( $f_s=30$  kHz). (b) Hysteresis controller ( $f_s=57$  kHz).



(a)



(b)

Fig. 17. Experimental results for a speed of 3000 r/min and a 6 N-m load. (a) DTSTSM controller ( $f_s=30$  kHz). (b) Hysteresis controller ( $f_s=57$  kHz).

TABLE II  
COMPARISON OF THE DTSTSM AND HYSTERESIS CURRENT CONTROLLERS  
FOR A 3 N·m LOAD

Speed (r/min)	DTSTSM controller		Hysteresis controller	
	$i_{RMSE}$ (A)	$T_{RMSE}$ (N·m)	$i_{RMSE}$ (A)	$T_{RMSE}$ (N·m)
500	0.4567	0.2252	1.6515	0.7859
1000	0.6356	0.3015	1.7522	0.6859
2000	1.1098	0.6016	1.4706	0.6502
3000	1.5780	1.0626	1.9762	0.8626

TABLE III  
COMPARISON OF THE DTSTSM AND HYSTERESIS CURRENT CONTROLLERS  
FOR A 6 N·m LOAD

Speed (r/min)	DTSTSM controller		Hysteresis controller	
	$i_{RMSE}$ (A)	$T_{RMSE}$ (N·m)	$i_{RMSE}$ (A)	$T_{RMSE}$ (N·m)
500	0.7191	0.3524	2.3321	1.1198
1000	0.9427	0.5488	2.0863	1.0270
2000	1.3433	0.9811	1.8360	1.0978
3000	1.5577	0.9820	2.5513	0.8511

however, the DTSTSM controller is still able to present a comparable performance at the speed of 3000 r/min. Moreover, the proposal has the benefit of ensuring a fixed switching frequency while also making use of a significantly lower sampling rate.

### B. Dynamic Performance

As a means to further evaluate the proposal, dynamics tests are carried out. First, to evaluate the robustness to load disturbances, Fig. 18 shows the experimental results of a dynamic load test for the proposed DTSTSM and hysteresis controllers, at a speed of 500 r/min. Initially, the motor operates under a 3 N·m load, and at  $t = 10$  s, the load is increased to 6 N·m.

It can be observed that the proposal presents a fast transient response as well as good disturbance rejection. Similarly to the steady-state results, significantly lower current and torque ripple are observed when compared to the hysteresis controller, while using approximately half the sampling frequency.

In order to test the performance in variable speed conditions, a dynamic speed test for the proposed DTSTSM and hysteresis controllers is presented in Figs. 19 and 20. The motor is initially controlled to 500 r/min, and then the speed is increased in a ramp until reaching 1000 r/min, under a 3 N·m load.

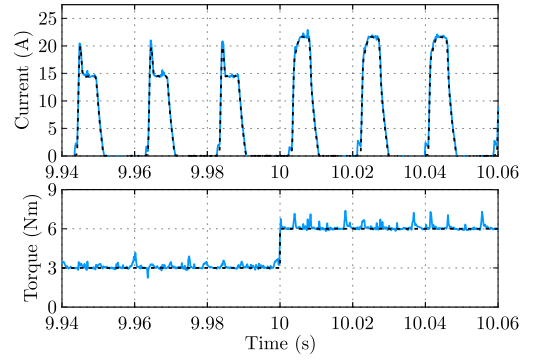
It is observed that the proposal presents suitable reference current and torque tracking throughout the test, including the speed variation period. Again, reduced current and torque ripple are verified when compared to the hysteresis controller.

### C. Efficiency Comparison

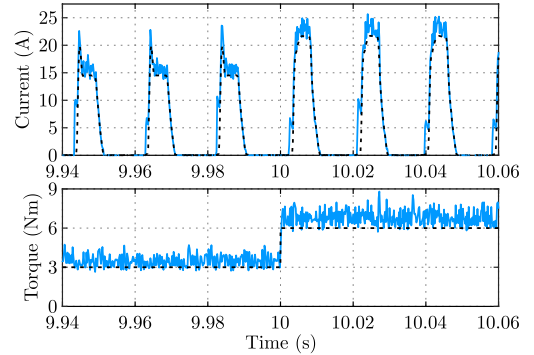
In order to further evaluate the proposal, an efficiency comparison is carried out. The efficiency,  $\eta$ , is calculated as

$$\eta = \frac{P_{out}}{P_{in}} \quad (26)$$

where  $P_{in}$  is the input power and  $P_{out}$  is the output power.



(a)



(b)

Fig. 18. Experimental results for a dynamic load condition, at a speed of 500 r/min. (a) DTSTSM controller ( $f_s=30$  kHz). (b) Hysteresis controller ( $f_s=57$  kHz).

TABLE IV  
EFFICIENCY COMPARISON OF THE DTSTSM AND HYSTERESIS CURRENT CONTROLLERS

Speed (r/min)	3 N·m		6 N·m	
	DTSTSM controller	Hysteresis controller	DTSTSM controller	Hysteresis controller
500	47.73%	51.12%	57.79%	57.81%
1000	57.36%	62.71%	67.40%	68.27%
2000	68.84%	70.59%	75.25%	75.55%
3000	73.21%	73.94%	79.05%	79.09%

The input power is defined as

$$P_{in} = V_{dc} i_{avg} \quad (27)$$

where  $i_{avg}$  is the average dc bus current.

The output power is defined as

$$P_{out} = \omega_r T_{avg} \quad (28)$$

where  $T_{avg}$  is the average electromagnetic torque.

The efficiency results for both controllers in the operating conditions of the steady-state tests are presented in Table IV. Moreover, the RMS current values are given in Table V. The switching frequency value for the hysteresis controller varies from  $f_{sw} = 1$  to 22 kHz. The proposal, as previously mentioned, presents a fixed switching frequency of  $f_{sw} = 30$  kHz.

It can be seen that the DTSTSM controller presents a slightly lower efficiency, when compared to the hysteresis controller.

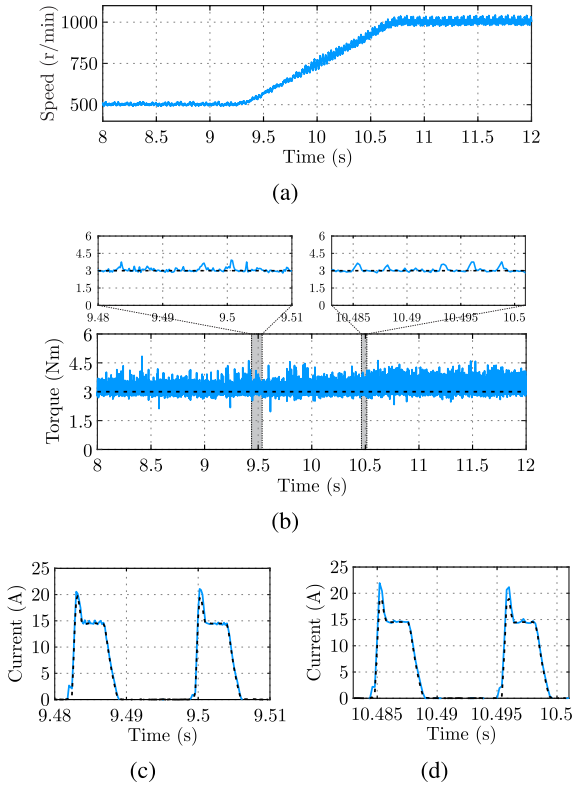


Fig. 19. Experimental results for the DTSTSM controller ( $f_s=30$  kHz) in a variable speed condition, under a 3 N-m load. (a) Rotor speed. (b) Torque. (c) Phase current highlight 1. (d) Phase current highlight 2.

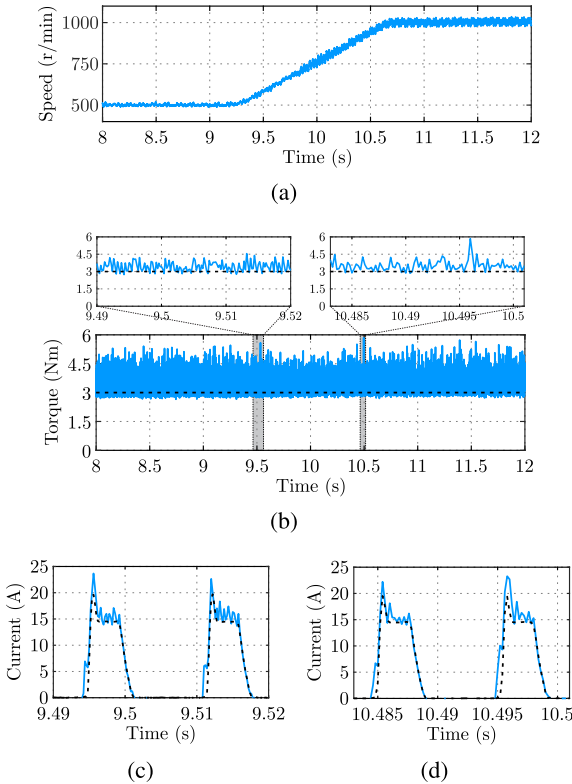


Fig. 20. Experimental results for the hysteresis controller ( $f_s=57$  kHz) in a variable speed condition, under a 3 N-m load. (a) Rotor speed. (b) Torque. (c) Phase current highlight 1. (d) Phase current highlight 2.

TABLE V  
RMS CURRENT COMPARISON OF THE DTSTSM AND HYSTERESIS CURRENT CONTROLLERS

Speed (r/min)	3 N-m		6 N-m	
	DTSTSM controller	Hysteresis controller	DTSTSM controller	Hysteresis controller
500	8.4791 A	9.0857 A	11.5301 A	12.3963 A
1000	8.6429 A	9.1585 A	11.4211 A	12.1394 A
2000	8.8416 A	8.8331 A	11.3540 A	11.8267 A
3000	8.7602 A	8.6766 A	11.4554 A	11.6424 A

TABLE VI  
COMPARISON OF THE EXECUTION TIME OF THE CONTROL ALGORITHMS

Algorithm	DTSTSM	Hysteresis
Total execution time	18.44 $\mu$ s	17.32 $\mu$ s

From Table V, it can be observed that the proposal has overall lower RMS current values, leading to smaller copper losses. However, due to the increased switching frequency of the DTSTSM controller, the switching losses of the proposed controller are higher, leading to a slightly smaller system efficiency, more evident in the lower speed range.

#### D. Computational Burden Comparison

In order to evaluate the computational complexity of both controllers, a measurement of the execution time of the control algorithm is presented in Table VI. The measure takes into consideration the torque sharing as well as the current controller, with both of the controllers being tested in the same hardware and without any code optimizations. Note that only the resulting current reference waveforms from [32] are stored in the DSP, with the cumbersome reference current-generation process being carried out entirely offline. It can be observed that the DTSTSM proposal presents an execution time very similar to the hysteresis approach, meaning it presents comparable computational complexity and showing that it could be used in low-cost microcontrollers available in industry- and product-oriented applications.

## VI. CONCLUSION

In this article, a DTSTSM current control scheme for SRMs is proposed. The proposal is implemented via PWM, ensuring a fixed switching frequency, while maintaining a simple structure, not requiring any knowledge of the machine parameters on implementation. A novel procedure for gain design is described, using a cost function to determine gains which ensure good tracking capability, enabling a systematic and straightforward design stage. The procedure is repeated for different speed values, allowing for a variable gain structure, where the gains are adjusted during operation based on the rotor speed. The control strategy is developed in discrete-time and the stability analysis is carried out considering the digital control implementation delay, by means of Lyapunov analysis. Experimental results show that the proposal has a comparable performance to a traditional hysteresis controller, while using a significantly lower sampling frequency and ensuring a fixed switching frequency.

Moreover, current and torque RMSE measurements attest to the good performance of the controller, showing a superior tracking performance especially at the lower speed range. Finally, the proposed controller presents a similar computational complexity to the hysteresis approach, making it a viable candidate for low-cost microcontrollers present in industry- and product-oriented applications.

#### APPENDIX: PROOF OF THEOREM I

*Proof:* The proof of Theorem 1 is based on [23], [30], and [31].

Consider the Lyapunov candidate function

$$V_{(k)} := \|\mathbf{x}_{(k)}\|_P^2 = \mathbf{x}_{(k)}^T \mathbf{P} \mathbf{x}_{(k)}. \quad (29)$$

So,  $\Delta V_{(k)} = V_{(k+1)} - V_{(k)}$  can be rewritten as

$$\Delta V_{(k)} = \mathbf{x}_{(k+1)}^T \mathbf{P} \mathbf{x}_{(k+1)} - \mathbf{x}_{(k)}^T \mathbf{P} \mathbf{x}_{(k)} \quad (30)$$

where  $\mathbf{P} = \mathbf{P}^T > 0$ .

Replacing the augmented model, (14), in (30), results

$$\begin{aligned} \Delta V_{(k)} &= (\mathbf{A} \mathbf{x}_{(k)} + \mathbf{B}_{(k)} \text{sign}(s_{(k)}) + \boldsymbol{\xi}_{(k)})^T \mathbf{P} (\mathbf{A} \mathbf{x}_{(k)} \\ &\quad + \mathbf{B}_{(k)} \text{sign}(s_{(k)}) + \boldsymbol{\xi}_{(k)}) - \mathbf{x}_{(k)}^T \mathbf{P} \mathbf{x}_{(k)}. \end{aligned} \quad (31)$$

Expanding (31) results in

$$\begin{aligned} \Delta V_{(k)} &= \mathbf{x}_{(k)}^T (\mathbf{A}^T \mathbf{P} \mathbf{A} - \mathbf{P}) \mathbf{x}_{(k)} + \mathbf{x}_{(k)}^T \mathbf{A}^T \mathbf{P} \mathbf{B}_{(k)} \text{sign}(s_{(k)}) \\ &\quad + \mathbf{x}_{(k)}^T \mathbf{A}^T \mathbf{P} \boldsymbol{\xi}_{(k)} + \mathbf{B}_{(k)}^T \mathbf{P} \mathbf{A} \mathbf{x}_{(k)} \text{sign}(s_{(k)}) \\ &\quad + \mathbf{B}_{(k)}^T \mathbf{P} \mathbf{B}_{(k)} + \mathbf{B}_{(k)}^T \mathbf{P} \boldsymbol{\xi}_{(k)} \text{sign}(s_{(k)}) \\ &\quad + \boldsymbol{\xi}_{(k)}^T \mathbf{P} \mathbf{A} \mathbf{x}_{(k)} + \boldsymbol{\xi}_{(k)}^T \mathbf{P} \mathbf{B}_{(k)} \text{sign}(s_{(k)}) \\ &\quad + \boldsymbol{\xi}_{(k)}^T \mathbf{P} \boldsymbol{\xi}_{(k)}. \end{aligned} \quad (32)$$

Using the  $\Lambda$  inequality [35]

$$\mathbf{X}^T \mathbf{Y} + \mathbf{Y}^T \mathbf{X} \leq \mathbf{X}^T \boldsymbol{\Lambda} \mathbf{X} + \mathbf{Y}^T \boldsymbol{\Lambda}^{-1} \mathbf{Y} \quad (33)$$

allows for the following terms to be obtained:

$$\begin{aligned} &\mathbf{x}_{(k)}^T \mathbf{A}^T \mathbf{P} \mathbf{B}_{(k)} \text{sign}(s_{(k)}) + \mathbf{B}_{(k)}^T \mathbf{P} \mathbf{A} \mathbf{x}_{(k)} \text{sign}(s_{(k)}) \\ &\leq \mathbf{x}_{(k)}^T \mathbf{A}^T \mathbf{P} \boldsymbol{\Lambda}_1 \mathbf{P} \mathbf{A} \mathbf{x}_{(k)} + \mathbf{B}_{(k)}^T \boldsymbol{\Lambda}_1^{-1} \mathbf{B}_{(k)} \\ &\mathbf{x}_{(k)}^T \mathbf{A}^T \mathbf{P} \boldsymbol{\xi}_{(k)} + \boldsymbol{\xi}_{(k)}^T \mathbf{P} \mathbf{A} \mathbf{x}_{(k)} \\ &\leq \mathbf{x}_{(k)}^T \mathbf{A}^T \mathbf{P} \boldsymbol{\Lambda}_2 \mathbf{P} \mathbf{A} \mathbf{x}_{(k)} + \boldsymbol{\xi}_{(k)}^T \boldsymbol{\Lambda}_2^{-1} \boldsymbol{\xi}_{(k)} \\ &\mathbf{B}_{(k)}^T \mathbf{P} \boldsymbol{\xi}_{(k)} \text{sign}(s_{(k)}) + \boldsymbol{\xi}_{(k)}^T \mathbf{P} \mathbf{B}_{(k)} \text{sign}(s_{(k)}) \\ &\leq \boldsymbol{\xi}_{(k)}^T \mathbf{P} \boldsymbol{\Lambda}_3 \mathbf{P} \boldsymbol{\xi}_{(k)} + \mathbf{B}_{(k)}^T \boldsymbol{\Lambda}_3^{-1} \mathbf{B}_{(k)} \end{aligned} \quad (34)$$

where  $\boldsymbol{\Lambda}_1 = \boldsymbol{\Lambda}_1^T > 0$ ,  $\boldsymbol{\Lambda}_2 = \boldsymbol{\Lambda}_2^T > 0$ , and  $\boldsymbol{\Lambda}_3 = \boldsymbol{\Lambda}_3^T > 0$ .

Then, substituting (34) in (32) results

$$\begin{aligned} \Delta V_{(k)} &\leq \mathbf{x}_{(k)}^T (\mathbf{A}^T \mathbf{P} \mathbf{A} - \mathbf{P}) \mathbf{x}_{(k)} + \mathbf{x}_{(k)}^T \mathbf{A}^T \mathbf{P} \boldsymbol{\Lambda}_1 \mathbf{P} \mathbf{A} \mathbf{x}_{(k)} \\ &\quad + \mathbf{B}_{(k)}^T \boldsymbol{\Lambda}_1^{-1} \mathbf{B}_{(k)} + \mathbf{x}_{(k)}^T \mathbf{A}^T \mathbf{P} \boldsymbol{\Lambda}_2 \mathbf{P} \mathbf{A} \mathbf{x}_{(k)} \\ &\quad + \boldsymbol{\xi}_{(k)}^T \boldsymbol{\Lambda}_2^{-1} \boldsymbol{\xi}_{(k)} + \boldsymbol{\xi}_{(k)}^T \mathbf{P} \boldsymbol{\Lambda}_3 \mathbf{P} \boldsymbol{\xi}_{(k)} \end{aligned}$$

$$+ \mathbf{B}_{(k)}^T \boldsymbol{\Lambda}_3^{-1} \mathbf{B}_{(k)} + \mathbf{B}_{(k)}^T \mathbf{P} \mathbf{B}_{(k)} + \boldsymbol{\xi}_{(k)}^T \mathbf{P} \boldsymbol{\xi}_{(k)}. \quad (35)$$

Equation (35) can be rewritten as

$$\begin{aligned} \Delta V_{(k)} &\leq \mathbf{x}_{(k)}^T \{ \mathbf{A}^T [\mathbf{P} + \mathbf{P} (\boldsymbol{\Lambda}_1 + \boldsymbol{\Lambda}_2) \mathbf{P}] \mathbf{A} - \mathbf{P} \} \mathbf{x}_{(k)} \\ &\quad + \mathbf{B}_{(k)}^T (\boldsymbol{\Lambda}_1^{-1} + \boldsymbol{\Lambda}_3^{-1} + \mathbf{P}) \mathbf{B}_{(k)} \\ &\quad + \boldsymbol{\xi}_{(k)}^T (\boldsymbol{\Lambda}_2^{-1} + \mathbf{P} \boldsymbol{\Lambda}_3 \mathbf{P} + \mathbf{P}) \boldsymbol{\xi}_{(k)}. \end{aligned} \quad (36)$$

By adding and subtracting  $\rho V_{(k)}$  to (36) and rearranging the terms results

$$\begin{aligned} \Delta V_{(k)} &\leq \mathbf{x}_{(k)}^T \{ \mathbf{A}^T [\mathbf{P} + \mathbf{P} (\boldsymbol{\Lambda}_1 + \boldsymbol{\Lambda}_2) \mathbf{P}] \mathbf{A} - (1 - \rho) \mathbf{P} \} \mathbf{x}_{(k)} \\ &\quad + \mathbf{B}_{(k)}^T (\boldsymbol{\Lambda}_1^{-1} + \boldsymbol{\Lambda}_3^{-1} + \mathbf{P}) \mathbf{B}_{(k)} \\ &\quad + \boldsymbol{\xi}_{(k)}^T (\boldsymbol{\Lambda}_2^{-1} + \mathbf{P} \boldsymbol{\Lambda}_3 \mathbf{P} + \mathbf{P}) \boldsymbol{\xi}_{(k)} - \rho V_{(k)} \end{aligned} \quad (37)$$

where  $0 < \rho < 1$ .

If the LMI

$$\mathbf{A}^T [\mathbf{P} + \mathbf{P} (\boldsymbol{\Lambda}_1 + \boldsymbol{\Lambda}_2) \mathbf{P}] \mathbf{A} - (1 - \rho) \mathbf{P} = -\mathbf{Q} \quad (38)$$

is feasible for a given  $\mathbf{Q} = \mathbf{Q}^T > 0$ , inequality (37) can be rewritten as

$$\begin{aligned} \Delta V_{(k)} &\leq -\mathbf{x}_{(k)}^T \mathbf{Q} \mathbf{x}_{(k)} + \mathbf{B}_{(k)}^T (\boldsymbol{\Lambda}_1^{-1} + \boldsymbol{\Lambda}_3^{-1} + \mathbf{P}) \mathbf{B}_{(k)} \\ &\quad + \boldsymbol{\xi}_{(k)}^T (\boldsymbol{\Lambda}_2^{-1} + \mathbf{P} \boldsymbol{\Lambda}_3 \mathbf{P} + \mathbf{P}) \boldsymbol{\xi}_{(k)} - \rho V_{(k)}. \end{aligned} \quad (39)$$

Equation (39) can be presented as

$$\Delta V_{(k)} \leq -\mathbf{x}_{(k)}^T \mathbf{Q} \mathbf{x}_{(k)} + \mathbf{B}_{(k)}^T \mathbf{Z} \mathbf{B}_{(k)} + \boldsymbol{\xi}_{(k)}^T \mathbf{H} \boldsymbol{\xi}_{(k)} - \rho V_{(k)} \quad (40)$$

where  $\mathbf{Z} = \boldsymbol{\Lambda}_1^{-1} + \boldsymbol{\Lambda}_3^{-1} + \mathbf{P}$  and  $\mathbf{H} = \boldsymbol{\Lambda}_2^{-1} + \mathbf{P} \boldsymbol{\Lambda}_3 \mathbf{P} + \mathbf{P}$ , with  $\mathbf{Z} = \mathbf{Z}^T > 0$  and  $\mathbf{H} = \mathbf{H}^T > 0$ .

Considering the following definitions:

$$\begin{aligned} \mathbf{x}_{(k)}^T \mathbf{Q} \mathbf{x}_{(k)} &= \|\mathbf{x}_{(k)}\|_Q^2 \\ \mathbf{B}_{(k)}^T \mathbf{Z} \mathbf{B}_{(k)} &= \|\mathbf{B}_{(k)}\|_Z^2 \\ \boldsymbol{\xi}_{(k)}^T \mathbf{H} \boldsymbol{\xi}_{(k)} &= \|\boldsymbol{\xi}_{(k)}\|_H^2 \end{aligned} \quad (41)$$

the following inequalities can be derived:

$$\begin{aligned} \lambda_{\min}(\mathbf{Q}) \|\mathbf{x}_{(k)}\|^2 &\leq \|\mathbf{x}_{(k)}\|_Q^2 \leq \lambda_{\max}(\mathbf{Q}) \|\mathbf{x}_{(k)}\|^2 \\ \lambda_{\min}(\mathbf{Z}) \|\mathbf{B}_{(k)}\|^2 &\leq \|\mathbf{B}_{(k)}\|_Z^2 \leq \lambda_{\max}(\mathbf{Z}) \|\mathbf{B}_{(k)}\|^2 \\ \lambda_{\min}(\mathbf{H}) \|\boldsymbol{\xi}_{(k)}\|^2 &\leq \|\boldsymbol{\xi}_{(k)}\|_H^2 \leq \lambda_{\max}(\mathbf{H}) \|\boldsymbol{\xi}_{(k)}\|^2. \end{aligned} \quad (42)$$

Then, (40) can be rewritten as

$$\begin{aligned} \Delta V_{(k)} &\leq -\lambda_{\min}(\mathbf{Q}) \|\mathbf{x}_{(k)}\|^2 + \lambda_{\max}(\mathbf{Z}) \|\mathbf{B}_{(k)}\|^2 \\ &\quad + \lambda_{\max}(\mathbf{H}) \|\boldsymbol{\xi}_{(k)}\|^2 - \rho V_{(k)}. \end{aligned} \quad (43)$$

By expanding the terms  $\|\mathbf{B}_{(k)}\|^2$  and  $\|\boldsymbol{\xi}_{(k)}\|^2$ , it results

$$\begin{aligned} \Delta V_{(k)} &\leq -\lambda_{\min}(\mathbf{Q}) \|\mathbf{x}_{(k)}\|^2 \\ &\quad + \lambda_{\max}(\mathbf{Z}) \left[ k_1^2 |x_{1(k)}| + (k_2 T_s)^2 \right] \\ &\quad + \lambda_{\max}(\mathbf{H}) \xi_{1(k)}^2 - \rho V_{(k)}. \end{aligned} \quad (44)$$

Given that  $\|x_{1(k)}\| \leq \|\mathbf{x}(k)\|$ , (44) can be expressed as

$$\begin{aligned} \Delta V_{(k)} &\leq -\alpha_1 \|\mathbf{x}(k)\|^2 + \alpha_2 \|\mathbf{x}(k)\| + \alpha_3 - \rho V_{(k)} \\ &= -\left(\sqrt{\alpha_1} \|\mathbf{x}(k)\| - \frac{\alpha_2}{2\sqrt{\alpha_1}}\right)^2 + \left(\frac{\alpha_2}{2\sqrt{\alpha_1}}\right)^2 \\ &\quad + \alpha_3 - \rho V_{(k)} \end{aligned} \quad (45)$$

where  $\alpha_1 = \lambda_{\min}(\mathbf{Q}) > 0$ ,  $\alpha_2 = \lambda_{\max}(\mathbf{Z})k_1^2$ , and  $\alpha_3 = \lambda_{\max}(\mathbf{Z})(k_2 T_s)^2 + \lambda_{\max}(\mathbf{H})\xi_1^2$ .

Then,

$$\Delta V_{(k)} \leq c - \rho V_{(k)} \quad (46)$$

where  $c = \left(\frac{\alpha_2}{2\sqrt{\alpha_1}}\right)^2 + \alpha_3$ .

Equation (46) can be expressed as

$$V_{(k+1)} \leq (1 - \rho) V_{(k)} + c \quad (47)$$

whose solution is given by

$$V_{(k)} \leq (1 - \rho)^k V_{(0)} + \sum_{i=0}^{k-1} (1 - \rho)^{k-1-i} c \quad (48)$$

and the upper limit can be calculated by

$$\lim_{k \rightarrow \infty} V_{(k)} \leq \frac{c}{\rho}. \quad (49)$$

Thus, the convergence radius of the controller is

$$r \leq \frac{c}{\rho}. \quad (50)$$

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