

Letters

Reference-Feedforward Power-Synchronization Control

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Abstract—In this letter, an enhancement of power-synchronization control is proposed, whereby pole-zero cancellation in the closed-loop system is achieved. An effect thereof is that step-response ringing and overshoot are eliminated. For strong grids, the closed-loop bandwidth increases, allowing a shorter step-response rise time.

Index Terms—Grid-connected converters, robustness, stability analysis, voltage-source converters (VSCs).

I. INTRODUCTION

POWER-SYNCHRONIZATION control (PSC) [1] is based on emulating the dynamics of a synchronous machine by a grid-connected voltage-source converter (VSC). The scheme was originally conceived to allow a stable interconnection with a very weak grid [2]. Its properties have been studied in detail over the years. Two recent examples are the large-signal transient stability analysis presented in [3] and the analytic selection of the power-control gain derived in [4]. In addition, in [4], an empirical selection recommendation for the so-called active resistance (which resembles the proportional gain of a current controller) is given. A robust design, with guaranteed stability margins of the power control loop, is obtained irrespective of the grid strength.

The great majority of papers on PSC consider weak-grid connections and/or grid-forming control, e.g., [5]–[8]. For robustness it is desirable that PSC should perform well also in a strong-grid connection. Unfortunately, even with the robust design in [4], the performance of PSC is inferior to that of traditional vector current control with cascaded outer loops. The closed-loop bandwidth is inherently limited and typically the step response exhibits overshoot and/or ringing.

This shortcoming is here rectified by the enhancement reference-feedforward PSC (RFPSC). In addition to using the

power reference in the power control law, it is fed forward to the active-resistance part [9]. It is shown that this places the zeros of the closed-loop system so that they (near-exactly) cancel a complex pole pair, reducing the system order from three to one. The pole-zero cancellation occurs irrespective of the grid inductance, thus ensuring robustness.

Design and analysis of RFPSC are presented in Section II, followed by experimental verification in Section III.

II. THEORETICAL RESULTS

A. Design

In PSC, the converter voltage, expressed in the stationary $\alpha\beta$ frame, is given as

$$\mathbf{v}^s = \mathbf{v}e^{j\theta} \quad (1)$$

assuming operation in the linear pulsewidth-modulation region, controller latency neglected, and switching harmonics disregarded. With current and power direction out of the converter, angle θ —which defines the synchronously rotating dq frame—is governed by the (active) power control law

$$\frac{d\theta}{dt} = \omega_1 + K_p(P_{\text{ref}} - P) \quad (2)$$

where ω_1 is the fundamental angular frequency, K_p is the power-control gain, and P_{ref} is the power reference

$$P = \kappa \text{Re}\{\mathbf{v}^s(\mathbf{i}^s)^*\} = \kappa \text{Re}\{\mathbf{v}\mathbf{i}^*\}, \quad \kappa = \frac{3}{2} K^2 \quad (3)$$

is the active output power, and K is the space-vector scaling constant. For per-unit (p.u.) normalization of the quantities or power-invariant vector scaling ($K = \sqrt{3/2}$), $\kappa = 1$. Moreover, PSC gives the dq -frame converter voltage as

$$\mathbf{v} = V + R_a(\mathbf{i}_{\text{ref}} - \mathbf{i}) \quad (4)$$

where V is considered constant (nominally 1 p.u.), although in practice it may be varied via a closed control loop for the point-of-common-coupling voltage or the reactive power [1], [10]. The second term is that of the active resistance R_a , expressed as a proportional control law. In conventional PSC, \mathbf{i}_{ref} is selected as a filtering of the converter current $\mathbf{i} = i_d + j i_q$ by the low-pass filter $H(s) = \omega_b / (s + \omega_b)$ [4].

In the steady state, $\mathbf{i}_{\text{ref}} - \mathbf{i} = 0$, simplifying (3) to $P = \kappa V i_d$. This motivates selecting the d component of \mathbf{i}_{ref} as $P_{\text{ref}} / (\kappa V)$, whereas the low-pass filtering for the q component remains, i.e.

$$\mathbf{i}_{\text{ref}} = \frac{P_{\text{ref}}}{\kappa V} + jH(s)\text{Im}\{\mathbf{i}\} \quad (5)$$

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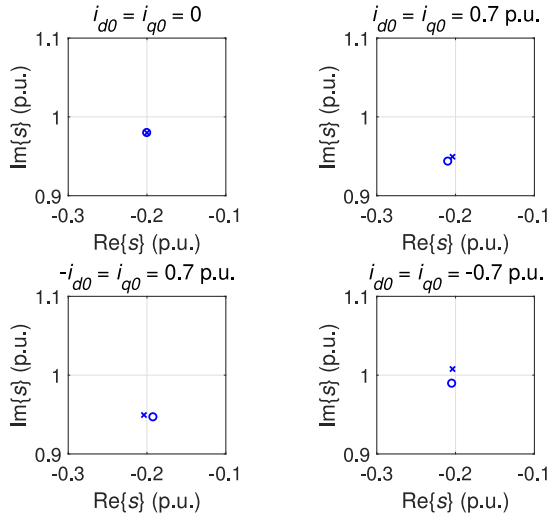


Fig. 3. Pole-zero plots for $V = \omega_1 = 1$ p.u., $L = 0.5$ p.u., $R_a = 0.2$ p.u., and various operating points $\mathbf{i}_0 = i_{d0} + ji_{q0}$. Exact pole-zero cancellation is obtained for $\mathbf{i}_0 = 0$, near-exact otherwise.

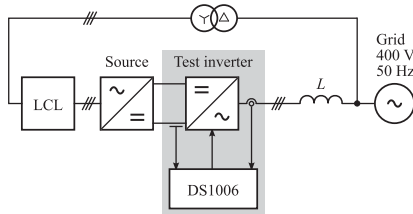


Fig. 4. Schematic of the experimental setup.

TABLE I
TEST-SYSTEM DATA

Variable/parameter	Actual value	Normalized value
Rated power	12.7 kVA	1 p.u.
Rated voltage	$\sqrt{2/3} \cdot 400$ V	1 p.u.
Rated current	$\sqrt{2} \cdot 18.3$ A	1 p.u.
Base impedance	12.6 Ω	1 p.u.
Fundamental frequency	50 Hz	1 p.u.
Sampling frequency	8 kHz	160 p.u.
Switching frequency	4 kHz	80 p.u.
Active resistance R_a	4.4 Ω	0.2 p.u.
Filter bandwidth ω_b	31 rad/s	0.1 p.u.

Block-diagram reductions in Fig. 2 give $G_c(s) = [G_p(s) + \xi G_{rP}(s)]/[1 + G_p(s)]$. Substituting (15) and (16) in this relation yields

$$G_c(s) = \frac{\xi cs^3 + k_2 s^2 + k_1 s + (1 + a + b)\alpha\omega_1^2}{s^3 + (2 + a)\alpha s^2 + (\alpha^2 + \omega_1^2)s + (1 + a + b)\alpha\omega_1^2} \quad (19)$$

where $k_2 = [a + \xi(1 + c)]\alpha$ and $k_1 = \xi[(c + d)\omega_1^2 + \alpha^2]$. The denominator can be approximately factorized as $[s + (1 + a + b)\alpha](s^2 + \alpha s + \omega_1^2)$, expanding to $s^3 + (2 + a + b)\alpha s^2 + [(1 + a + b)\alpha^2 + \omega_1^2]s + (1 + a + b)\alpha\omega_1^2$. The coefficients for s^2 and s are, thus, imperfectly matched. An approximate factorization of the numerator is, for $\xi = 1$, given as $[cs + (1 + a + b)\alpha](s^2 + \alpha s + \omega_1^2)$. Expansion shows that the coefficients for s^2 and s are imperfect matches here as well. However, all

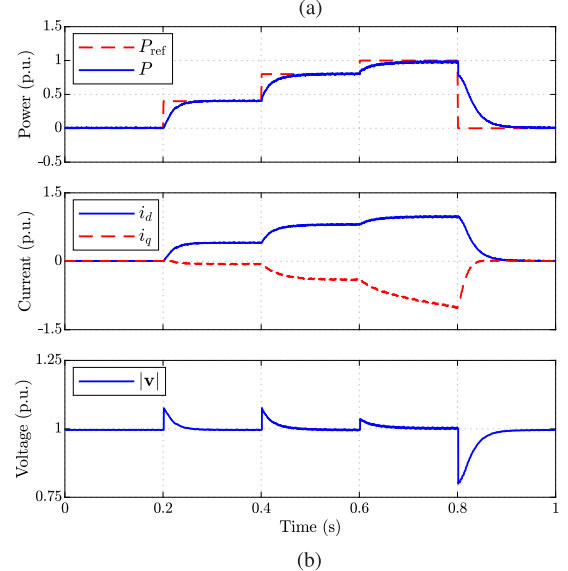
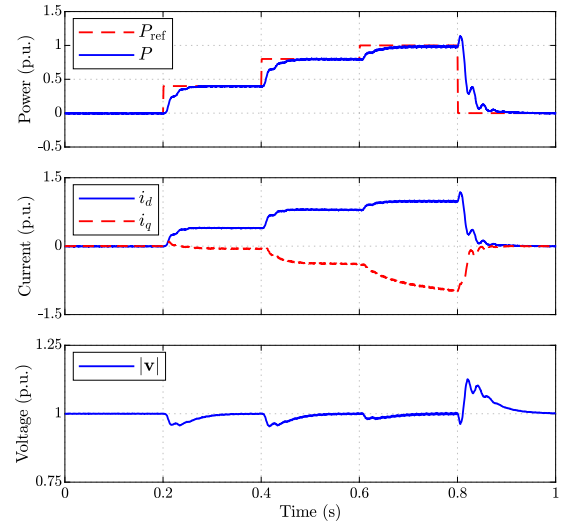


Fig. 5. Step responses for a weak grid, $L = 1$ p.u. (a) Conventional PSC. (b) RFPSC.

mismatches are small for $\{|a|, |b|, |c|\} \ll 1$. This is normally the case, as long as R_a is moderate; $R_a = 0.2$ p.u. is suggested in [4]. By the approximate factorizations, the second-degree factors cancel, reducing (19) to the first-order system

$$G_c(s) \approx \frac{cs + (1 + a + b)\alpha}{s + (1 + a + b)\alpha}. \quad (20)$$

RFPSC (i.e., $\xi = 1$) with the gain selection in (18) is required for this pole-zero cancellation to occur. For $\mathbf{i}_0 = 0 \Rightarrow a = b = c = 0$, the polynomial factorizations are exact, and, thus, also the pole-zero cancellation. For other operating points \mathbf{i}_0 , yet a near-exact cancellation is obtained, as exemplified in Fig. 3.

As found in [4], the cancelled pole pair is dominant for strong grids (i.e., small L), effectively limiting the closed-loop bandwidth to $\omega_1 = 1$ p.u. for conventional PSC. On the contrary, for RFPSC, the remaining pole of (20) gives the closed-loop bandwidth $(1 + a + b)\alpha$, which for a strong grid easily exceeds 1 p.u. For example, $L = 0.1$ p.u., $R_a = 0.2$ p.u., and $\mathbf{i}_0 = 0$ give $(1 + a + b)\alpha = \alpha = 2$ p.u. In addition, since the pole pair

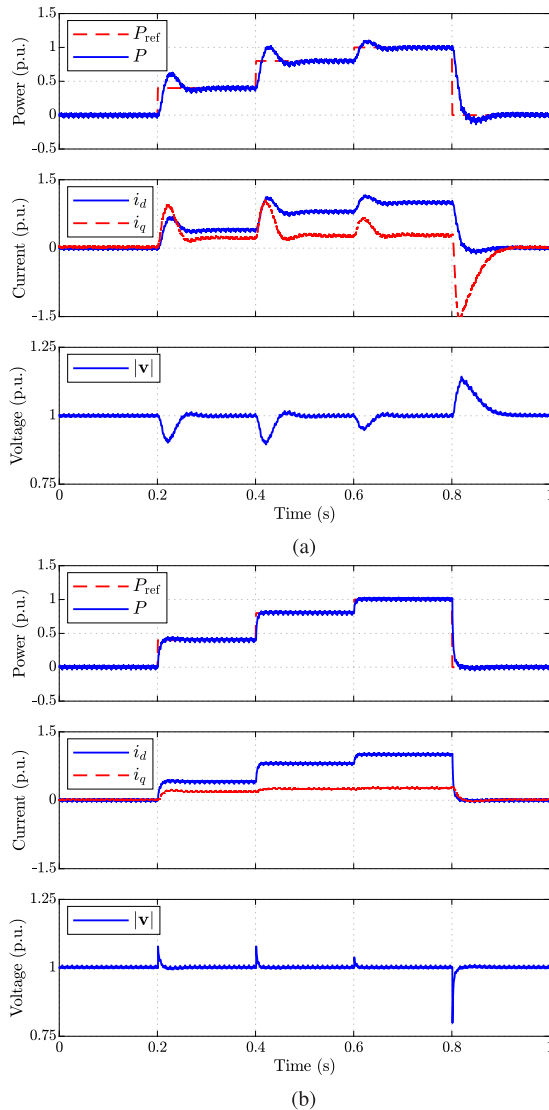


Fig. 6. Step responses for a strong grid, $L = 0.1$ p.u. (a) Conventional PSC. (b) RFPSC.

is located fairly close to the imaginary axis (see Fig. 3), its cancellation generally improves damping.

III. EXPERIMENTAL RESULTS

RFPSC is here experimentally compared with conventional PSC, using the same back-to-back (grid and dc source) two-level VSC system as in [4]—see the schematic depicted in Fig. 4—whose data are given in Table I. Control is implemented on a dSPACE DS1006 processor board. The dc link is controlled from the dc source.

Figs. 5 and 6 show results for four successive steps in P_{ref} , respectively, for a weak and a strong grid, with conventional PSC as well as with RFPSC. (The subfigures for conventional PSC are repeated from [4], for clarity.) The following can be observed.

- 1) The step-response rise times in the weak-grid case are similar for conventional PSC and RFPSC. This was to be

expected, since, for a weak grid, the real pole of the closed-loop system is dominant. Cancellation of the complex pole pair only gives a slight increase of the closed-loop bandwidth. On the other hand, the tendency to ringing in the step response is eliminated.

- 2) In the strong-grid case, RFPSC gives shorter rise times than conventional PSC and, perhaps even more importantly, eliminates the overshoots. In addition, the voltage-magnitude transients are significantly reduced.
- 3) In accordance with the model in (20) resulting from the pole-zero cancellation, all step responses for RFPSC resemble first-order exponentials.

IV. CONCLUSION

In this letter, the enhancement RFPSC of conventional PSC was presented. It involves feeding the power reference forward to the active-resistance part of the control law. When observing the robustifying gain selection in (18), the complex pole pair of the closed-loop system is (near-exactly) cancelled. Compared with conventional PSC, this was shown to eliminate step-response ringing for weak grids. For strong grids, a shorter step-response rise time is obtained and overshoot is avoided, allowing performance similar to that of vector current control. The design was shown to be robust in the sense that, irrespective of the grid inductance L , the step response resembles a first-order exponential whose rise time is proportional to L . Knowledge of L is not required for the robust design, as fundamentally shown by the gain selection given in (18). A suitable topic for further research is performance analysis for a generic grid impedance.

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