

Direct Power Flow Controller—A New Concept in Power Transmission

Youjun Zhang , Guoqing Lu, Wajid Ali Khan , Yuzhen Zhang, and Qixin Zhu 

Abstract—For power transmission in grid, a new concept of direct power flow controller (DPFC) was proposed based on single-stage ac–ac converter with controllable phase and amplitude. By connecting its regulated output compensation voltage with grid in series, DPFC is able to regulate the amplitude and phase angle of grid node voltage, and thus can effectively control active and reactive power flow in grid, respectively and simultaneously. Compared with unified power flow controller (UPFC), DPFC also has a parallel transformer and a series transformer but has no dc energy storage element that easily leads to high equipment failure rate. Furthermore, UPFC is similar to two-stage conversion circuit, while DPFC has only one-stage conversion circuit, which consists of three single-phase buck-type ac units and a three-phase output filter. With respect to the input voltage of DPFC basic circuit, the phase regulation range of its output compensation voltage is 60° , which is easy to extend to 360° with two selection switches changing the connection groups of the input and output transformer. The topology structure and operational principle of DPFC were presented, and the experimental results of a prototype showed its feasibility and verified the theoretical analysis of DPFC.

Index Terms—AC–AC converter with controllable phase and amplitude (ACCPA), compensation voltage, direct power flow controller (DPFC), grid voltage, phase regulation, power transmission.

I. INTRODUCTION

FLEXIBLE ac transmission system (FACTS) technology has been widely applied in power grid, such as

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improving the power transmission ability and the stability of power system and increasing the effectiveness of power flow control [1]–[4].

Usually shunt FACTS devices are employed to compensate the reactive power in grid and prevent grid voltage from dropping [5], [6], which are in essence var loads or var sources and cannot control the current in individual branches and the active power flow in meshed systems effectively. Static synchronous compensator (STATCOM) [7]–[11] and static var compensator [12]–[15] are two kinds of widely used shunt FACTS devices.

Gate turn-OFF thyristor-controlled series capacitor [16], thyristor-controlled series capacitor [17]–[20], and static synchronous series compensator (SSSC) [21]–[26] are series FACTS devices and often connected in series with power transmission line. Series FACTS devices are equivalent to regulating the line impedance in grid and then controlling the line current and active power flow, but they are not able to control active and reactive power flow independently and simultaneously [1].

Unified power flow controller (UPFC) [27]–[33], shown in Fig. 1, is similar to the combination of a series SSSC and a shunt STATCOM, and can realize functions of shunt compensation, phase shifting, and series compensation, and control active and reactive power flow, respectively.

UPFC, STATCOM, and SSSC all contain big volume dc energy storage element, which usually results in high equipment failure rate, short life cycle, and high maintenance cost. In order to obtain a kind of device without dc energy storage element, the concept of virtual quadrature source was adopted [5]; a method for power flow control, i.e., ac–ac converter with controllable phase and amplitude (ACCPA), was presented [1], [34]–[36], which not only has the functions of voltage step-up and step-down, but also can regulate the phase angle of the output voltage lead or lag with respect to the input.

Both cascaded ACCPA [34], [35] and π model ACCPA [36] have a two-stage circuit structure, whose front stage is a buck-type ac converter (or unit) and is used to regulate the phase angle of its output voltage, and whose back stage is a boost-type ac converter (or unit) for regulating the amplitude of its output voltage with the front stage. The back stage only provides the function of voltage step-up for cascaded ACCPA and π model ACCPA. Prior to the use of two-stage ACCPA for power transmission in high-voltage grid, the problem of high-voltage stress should be solved first because power switches at present can only support limited voltage (much lower than that of high-voltage grid), even if multilevel ac converter [37] replaces two-level ac

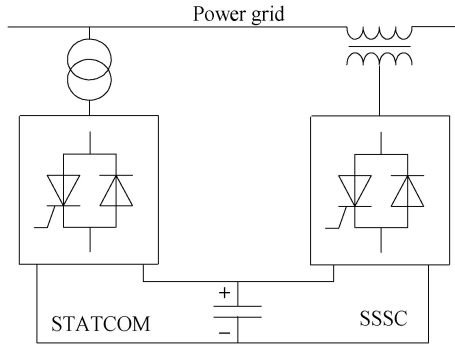


Fig. 1. Structure of UPFC.

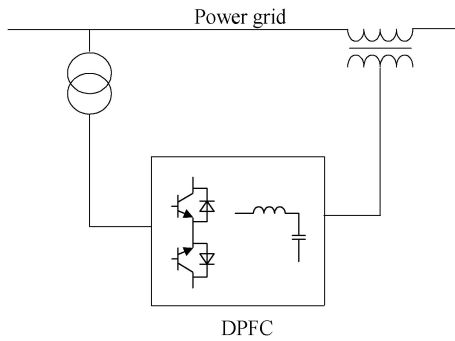


Fig. 2. Concept of DPFC.

converter in ACCPA. Taking into consideration the aforementioned problem, single-stage ACCPA [1] was developed, which introduces an output transformer to provide the function of voltage step-up and take the place of the boost-type ac converter (or unit) in two-stage ACCPA. Compared with two-stage ACCPA, single-stage ACCPA not only decreases the number of switching devices in circuit and reduces the complexity of system, but also decreases the number of power conversion stages (it usually means lower power loss and higher efficiency).

It is known that, if the phase and amplitude of the sending-end voltage in grid can be regulated, the effective control of active power flow and reactive power flow would be implemented [6]. For power transmission in grid, based on single-stage ACCPA, the concept of direct power flow controller (DPFC) was proposed, as shown in Fig. 2. DPFC does not contain dc energy storage element and has only one-stage conversion circuit. By connecting its regulated output compensation voltage with grid in series, DPFC is able to regulate the amplitude and phase angle of grid node voltage, respectively and simultaneously. The phase regulation range of output compensation voltage in DPFC is 360° . So, with respect to the original grid voltage, the regulated grid voltage can be controlled by DPFC to be step-up or step-down for its amplitude, and lead or lag for its phase angle. Thus, DPFC can effectively control active power flow and reactive power flow in grid separately. The topology structure and operational principle of DPFC were studied in detail. The regulation range of phase angle and amplitude of DPFC basic

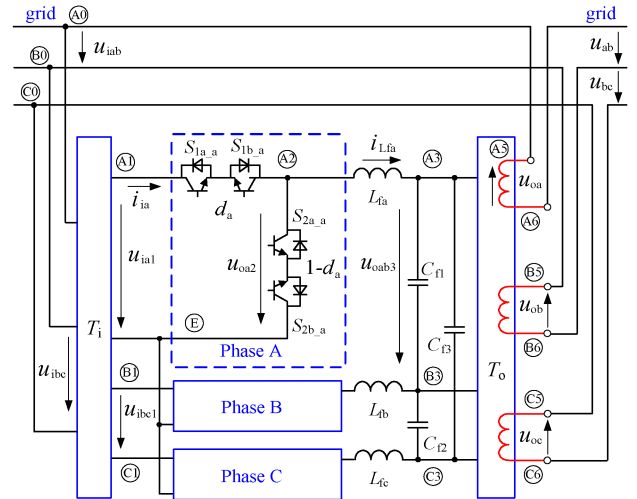


Fig. 3. Basic topology structure of DPFC.

circuit was described under 36 different combinations of connection groups of the input and output transformer. Experimental results of the prototype were shown to verify the feasibility of DPFC.

II. DIRECT POWER FLOW CONTROLLER

A. Topology Structure and Operational Principle

UPFC and the proposed DPFC are both for power flow control. Compared with UPFC, the proposed DPFC also has a parallel transformer and a series transformer, but has no dc energy storage element that easily leads to high equipment failure rate, short life cycle, and high maintenance cost. Furthermore, UPFC is similar to a two-stage conversion circuit, while DPFC has only one-stage conversion circuit. Usually, decreasing the number of power conversion stages is an effective method to achieve simpler structure, a smaller number of switching devices in circuit, higher stability, lower power loss, and higher efficiency.

Fig. 3 shows one of the DPFC basic circuits, whose three-phase input transformer T_i (or transformer group) is paralleled with grid, while the output voltage of three-phase output transformer T_o (or transformer group) is connected with grid in series. Three single-phase buck-type ac units and a three-phase output filter constitute its one-stage conversion circuit.

Assume that

- 1) original grid voltage is sinusoidal with angular frequency $\omega (=2\pi f$, where f is its frequency);
- 2) circuit components are ideal;
- 3) as shown in Fig. 4, T_i is of $\Delta/Yn11$ -type connection group with turn ratio N_i , and T_o is of $\Delta/Y1$ -type connection group with turn ratio N_o (note that points A5, B5, and C5 are not connected together, and the three secondary windings of T_o are separately connected with power transmission line in series);
- 4) low-frequency voltage drop across the inductor L_{fx} ($x = a, b, c$, where x is the name of phase in lowercase letter) is not taken into account.

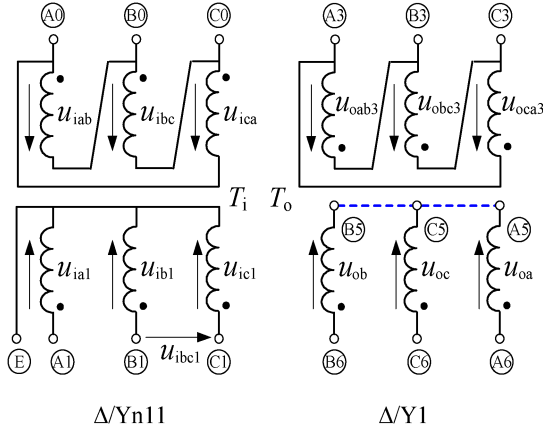


Fig. 4. Assumed connection groups.

If the original grid line voltages u_{iab} , u_{ibc} , u_{ica} and the duty ratio d_x are as below

$$\begin{cases} u_{iab} = N_i u_{ia1} = U_{imL} \sin \omega t \\ u_{ibc} = N_i u_{ib1} = U_{imL} \sin(\omega t - 120^\circ) \\ u_{ica} = N_i u_{ic1} = U_{imL} \sin(\omega t + 120^\circ) \end{cases} \quad (1)$$

$$\begin{cases} d_a = k_0 + k_2 \sin(2\omega t + \beta_2) \\ d_b = k_0 + k_2 \sin[2(\omega t - 120^\circ) + \beta_2] \\ \quad = k_0 + k_2 \sin(2\omega t + \beta_2 + 120^\circ) \\ d_c = k_0 + k_2 \sin[2(\omega t + 120^\circ) + \beta_2] \\ \quad = k_0 + k_2 \sin(2\omega t + \beta_2 - 120^\circ) \end{cases} \quad (2)$$

where u_{ia1} , u_{ib1} , and u_{ic1} are the input voltages of three single-phase buck-type ac units, U_{imL} is the amplitude of u_{iab} , β_2 is the initial phase angle of the ac component of d_a [5], and coefficients k_0 and k_2 are nonnegative, one knows that $k_2 \leq k_0$ at $0 \leq k_0 \leq 0.5$, or $k_2 \leq 1 - k_0$ at $0.5 \leq k_0 \leq 1$.

Delta (Δ)-type connection is adopted for capacitors of the three-phase output filter to cancel out the third harmonic voltages. After L_{fx} filters out the high-frequency component of u_{ox2} (output voltage of buck-type ac unit), the phase voltage u_{ox3} (between points X3 and E, not shown in Fig. 3, X = A, B, C, where X is the name of phase in uppercase letter) is output, then one can obtain the voltages u_{oab3} , u_{obc3} , u_{oca3} , u_{oa} , u_{ob} , and u_{oc} [1]

$$\begin{cases} u_{oab3} = u_{oa3} - u_{ob3} = d_a u_{ia1} - d_b u_{ib1} \\ \quad = U_{mL3} \sin(\omega t + \varphi_t) \\ u_{obc3} = u_{ob3} - u_{oc3} = d_b u_{ib1} - d_c u_{ic1} \\ \quad = U_{mL3} \sin(\omega t + \varphi_t - 120^\circ) \\ u_{oca3} = u_{oc3} - u_{oa3} = d_c u_{ic1} - d_a u_{ia1} \\ \quad = U_{mL3} \sin(\omega t + \varphi_t + 120^\circ) \end{cases} \quad (3)$$

$$\begin{cases} u_{oa} = \frac{U_{mL3}}{N_o} \sin(\omega t + \varphi_t - 60^\circ) \\ u_{ob} = \frac{U_{mL3}}{N_o} \sin(\omega t + \varphi_t - 180^\circ) \\ u_{oc} = \frac{U_{mL3}}{N_o} \sin(\omega t + \varphi_t + 60^\circ) \end{cases} \quad (4)$$

$$\begin{cases} U_{mL3} = \frac{\sqrt{3}k_0 U_{imL}}{N_i} \sqrt{1 - \frac{k_2}{k_0} \sin \beta_2 + \frac{1}{4} \left(\frac{k_2}{k_0}\right)^2} \\ \varphi_t = \arctan \frac{(k_2/k_0) \cos \beta_2}{2 - (k_2/k_0) \sin \beta_2} + 30^\circ \end{cases} \quad (5)$$

where U_{mL3} is the amplitude of u_{oab3} with phase angle φ_t . In positive sequence, a phase difference of 120° is present among u_{oab3} , u_{obc3} , and u_{oca3} (or u_{oa} , u_{ob} , and u_{oc}).

The regulated grid line voltages (u_{ab} , u_{bc} , and u_{ca}) are

$$\begin{cases} u_{ab} = u_{iab} + (u_{oa} - u_{ob}) = u_{iab} + u_{oab} \\ \quad = u_{iab} + \Delta u_{ab} = U_{mL} \sin(\omega t + \varphi_r) \\ u_{bc} = u_{ibc} + (u_{ob} - u_{oc}) = u_{ibc} + u_{obc} \\ \quad = u_{ibc} + \Delta u_{bc} = U_{mL} \sin(\omega t + \varphi_r - 120^\circ) \\ u_{ca} = u_{ica} + (u_{oc} - u_{oa}) = u_{ica} + u_{oca} \\ \quad = u_{ica} + \Delta u_{ca} = U_{mL} \sin(\omega t + \varphi_r + 120^\circ) \end{cases} \quad (6)$$

$$\begin{cases} u_{oab} = \Delta u_{ab} = U_{omL} \sin(\omega t + \varphi_o) \\ \quad = \frac{\sqrt{3}U_{mL3}}{N_o} \sin(\omega t + \varphi_t - 30^\circ) \\ u_{obc} = \Delta u_{bc} = \frac{\sqrt{3}U_{mL3}}{N_o} \sin(\omega t + \varphi_t - 150^\circ) \\ u_{oca} = \Delta u_{ca} = \frac{\sqrt{3}U_{mL3}}{N_o} \sin(\omega t + \varphi_t + 90^\circ) \end{cases} \quad (7)$$

where U_{mL} and φ_r are the amplitude and phase angle of u_{ab} , and we define $u_{oab} = u_{oa} - u_{ob} = \Delta u_{ab}$, $u_{obc} = u_{ob} - u_{oc} = \Delta u_{bc}$, and $u_{oca} = u_{oc} - u_{oa} = \Delta u_{ca}$. Apparently, u_{ox} is the compensation phase voltage of grid, while u_{oab} (Δu_{ab}), u_{obc} (Δu_{bc}), and u_{oca} (Δu_{ca}) are the compensation line voltages of grid (φ_o is the phase angle of u_{oab} with amplitude U_{omL}).

Here, φ_o , U_{omL} , φ_r , and U_{mL} would be given as

$$\varphi_o = \varphi_t - 30^\circ = \arctan \frac{(k_2/k_0) \cos \beta_2}{2 - (k_2/k_0) \sin \beta_2} \quad (8)$$

$$U_{omL} = \frac{3k_0 U_{imL}}{N_i N_o} \sqrt{1 - \frac{k_2}{k_0} \sin \beta_2 + \frac{1}{4} \left(\frac{k_2}{k_0}\right)^2} \quad (9)$$

$$\varphi_r = \arctan \frac{U_{omL} \sin \varphi_o}{U_{imL} + U_{omL} \cos \varphi_o} \quad (10)$$

$$U_{mL} = \sqrt{(U_{imL} + U_{omL} \cos \varphi_o)^2 + (U_{omL} \sin \varphi_o)^2} \quad (11)$$

From (8)–(11) we know that φ_o of the grid compensation line voltage u_{oab} (Δu_{ab}) is controlled by two parameters (k_2/k_0 and β_2), U_{omL} is controlled by three parameters (k_0 , k_2/k_0 , and β_2), and so are U_{mL} and φ_r .

B. Range of the Grid Compensation Voltage

From (6), (10), and (11), one knows that the phase angle φ_r and the amplitude U_{mL} of the regulated grid voltage are dependent on the range of grid compensation voltage.

With the change of k_2/k_0 (letting $\alpha = k_2/k_0$, $0 \leq \alpha \leq 1$, $0 \leq k_0 \leq 1/(1 + \alpha) \leq 1$), the maximum and minimum

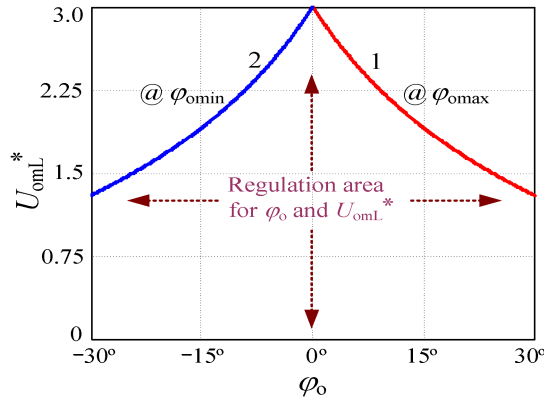


Fig. 5. Regulation range of U_{omL}^* and φ_o .

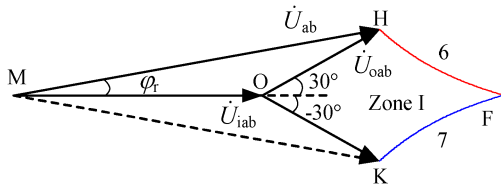


Fig. 6. Basic regulation zone I of DPFC.

of φ_o would, respectively, be

$$\begin{cases} \varphi_{omax} = \arcsin \frac{\alpha}{2} = \beta_2, & \beta_2 \in [0^\circ, 90^\circ] \\ \varphi_{omin} = -\arcsin \frac{\alpha}{2} = \beta_2 - 180^\circ, & \beta_2 \in (90^\circ, 180^\circ]. \end{cases} \quad (12)$$

The regulation range of φ_o would increase from zero (at $\alpha = 0$) to $[-30^\circ, 30^\circ]$ (at $\alpha = 1$) with the increase of α . Known from (12), there would be $0 \leq \sin \beta_2 = 0.5\alpha \leq 0.5$ if φ_o is equal to extremum. It indicates that $\beta_2 \in [0^\circ, 30^\circ]$ or $[150^\circ, 180^\circ]$ in the range of $0-360^\circ$.

According to (9) and (12), one knows that

$$0 \leq U_{omL}^* = N_i N_o \frac{U_{omL}}{U_{imL}} \leq \frac{3}{1 + \alpha} \sqrt{1 - \frac{1}{4}\alpha^2}. \quad (13)$$

At extremum of φ_o , with the increase of α (k_2/k_0) the regulation range of U_{omL}^* ($N_i N_o U_{omL}/U_{imL}$) decreases.

Substituting (12) into (13), we have

$$0 \leq U_{omL}^* \leq \frac{3}{1 + 2|\sin \varphi_{oext}|} \sqrt{1 - (\sin \varphi_{oext})^2} \quad (14)$$

where φ_{oext} is the extremum of φ_o . As shown in Fig. 5, the regulation range of φ_o and that of U_{omL}^* form a regulation area [1]. Curve 1 shows the maximum of U_{omL}^* at φ_{omax} , while curve 2 shows the maximum of U_{omL}^* at φ_{omin} .

According to Fig. 5, one can obtain Fig. 6, in which zone I (O-H-F-K) is the regulation area for U_{omL} and φ_o of the compensation grid voltage, the same as that in Fig. 5 (note that there is a different coordinate system). Zone I is the basic regulation zone of DPFC. Here, with respect to the original grid voltage u_{iab} , the phase angle φ_r of the regulated grid voltage u_{ab} can be regulated lead or lag, but its regulated amplitude U_{mL} can only become larger than the amplitude U_{imL} of u_{iab} . If we want to regulate U_{mL} to be smaller than U_{imL} , it is necessary to research on other basic regulation zones of DPFC.

III. FULL REGULATION ZONE WITH 360° RANGE

A. Different Combinations of Connection Groups of Transformers

As shown in Figs. 5 and 6, when T_i is of $\Delta/Yn11$ -type connection group and T_o is of $\Delta/Y1$ -type connection group, the regulation range of φ_o is $[-30^\circ, 30^\circ]$ and the maximum of U_{omL}^* is 3. Obviously, if the connection groups of T_i and T_o are changed, the phase angle and amplitude of the compensation grid voltage and the regulated grid voltage will be slightly changed from (8), (9), (10), and (11), respectively.

It is necessary to adopt the input connection of T_i with Δ type to prevent third harmonic current from flowing into the grid. The output connection of T_i should be Y type with neutral line to provide input voltages for three single-phase buck-type ac units. Similarly, Δ type is adopted for the capacitors of the three-phase output filter and the input connection of T_o , to cancel out third harmonic voltage. For connecting the output voltage of T_o with grid in series, only Y type is suitable for the output connection of T_o (note that its three secondary windings are not connected together).

Six types of connection groups ($\Delta/Yn1$, $\Delta/Yn3$, $\Delta/Yn5$, $\Delta/Yn7$, $\Delta/Yn9$, and $\Delta/Yn11$) are suitable for input transformer T_i , as shown in Fig. 7. For output transformer T_o , there are also six suitable types of connection groups ($\Delta/Y1$, $\Delta/Y3$, $\Delta/Y5$, $\Delta/Y7$, $\Delta/Y9$, and $\Delta/Y11$), as shown in Fig. 8. Thus, DPFC has at least 36 different combinations of connection groups of the input and output transformer.

Table I shows the regulation range of phase angle φ_o for each kind of combination. Totally, there are six ranges, $[-30^\circ, 30^\circ]$, $[30^\circ, 90^\circ]$, $[90^\circ, 150^\circ]$, $[150^\circ, 210^\circ]$, $[210^\circ, 270^\circ]$, and $[-90^\circ, -30^\circ]$, corresponding to zone I, zone II (O-H-L-G, as shown in Fig. 9), zone III (O-G-J-P), zone IV (O-P-Q-R), zone V (O-R-U-V), and zone VI (O-V-W-K), respectively. The maximum of U_{om}^* of each zone is 3.

For example, if T_i is kept as of $\Delta/Yn11$ -type connection group and T_o is changed to $\Delta/Y9$ type, the regulation area for φ_o (comparing (16) and (8), the compensation line voltage phase angle φ_o has increased by 120° while moving from zone I to zone III) and U_{omL} of compensation grid voltage is in zone III (O-G-J-P), where it shows that

$$\dot{U}_{ab} = \dot{U}_{iab} + \dot{U}_{oab} \quad (15)$$

$$\varphi_o = (\varphi_t - 30^\circ) + 120^\circ = 120^\circ + \arctan \frac{(k_2/k_0) \cos \beta_2}{2 - (k_2/k_0) \sin \beta_2}. \quad (16)$$

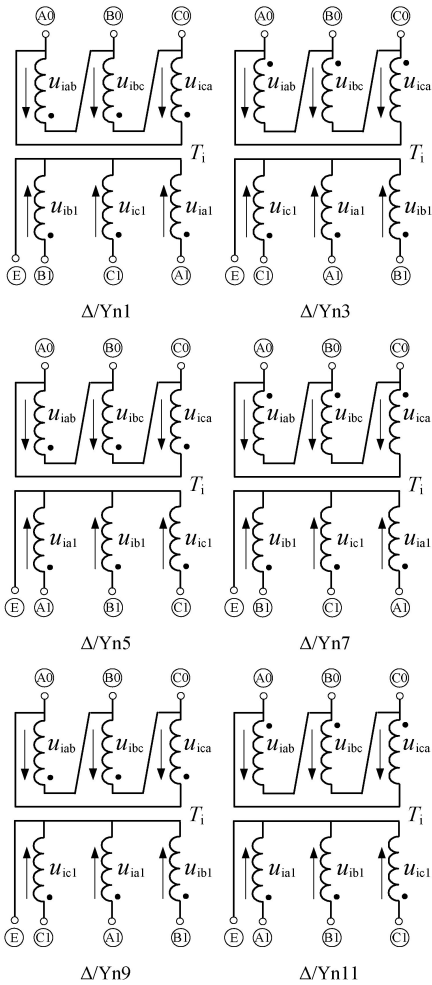
From (8) and (16), Table I, and Fig. 9, according to the number of connection groups of T_i and T_o , one obtains a general equation of φ_o for all the zones

$$\varphi_o = \varphi_t - 30^\circ - (n_i + n_o) \times 30^\circ \quad (17)$$

where n_i is the number of connection groups of T_i , and n_o is that of T_o .

TABLE I
 REGULATION RANGE OF φ_o FOR 36 DIFFERENT COMBINATIONS

The regulation range of $\varphi_o/^\circ$		T_i					
		$\Delta/Yn11$	$\Delta/Yn9$	$\Delta/Yn7$	$\Delta/Yn5$	$\Delta/Yn3$	$\Delta/Yn1$
T_o	$\Delta/Y1$	$[-30^\circ, 30^\circ]$	$[30^\circ, 90^\circ]$	$[90^\circ, 150^\circ]$	$[150^\circ, 210^\circ]$	$[210^\circ, 270^\circ]$	$[-90^\circ, -30^\circ]$
	$\Delta/Y3$	$[-90^\circ, -30^\circ]$	$[-30^\circ, 30^\circ]$	$[30^\circ, 90^\circ]$	$[90^\circ, 150^\circ]$	$[150^\circ, 210^\circ]$	$[210^\circ, 270^\circ]$
	$\Delta/Y5$	$[210^\circ, 270^\circ]$	$[-90^\circ, -30^\circ]$	$[-30^\circ, 30^\circ]$	$[30^\circ, 90^\circ]$	$[90^\circ, 150^\circ]$	$[150^\circ, 210^\circ]$
	$\Delta/Y7$	$[150^\circ, 210^\circ]$	$[210^\circ, 270^\circ]$	$[-90^\circ, -30^\circ]$	$[-30^\circ, 30^\circ]$	$[30^\circ, 90^\circ]$	$[90^\circ, 150^\circ]$
	$\Delta/Y9$	$[90^\circ, 150^\circ]$	$[150^\circ, 210^\circ]$	$[210^\circ, 270^\circ]$	$[-90^\circ, -30^\circ]$	$[-30^\circ, 30^\circ]$	$[30^\circ, 90^\circ]$
	$\Delta/Y11$	$[30^\circ, 90^\circ]$	$[90^\circ, 150^\circ]$	$[150^\circ, 210^\circ]$	$[210^\circ, 270^\circ]$	$[-90^\circ, -30^\circ]$	$[-30^\circ, 30^\circ]$

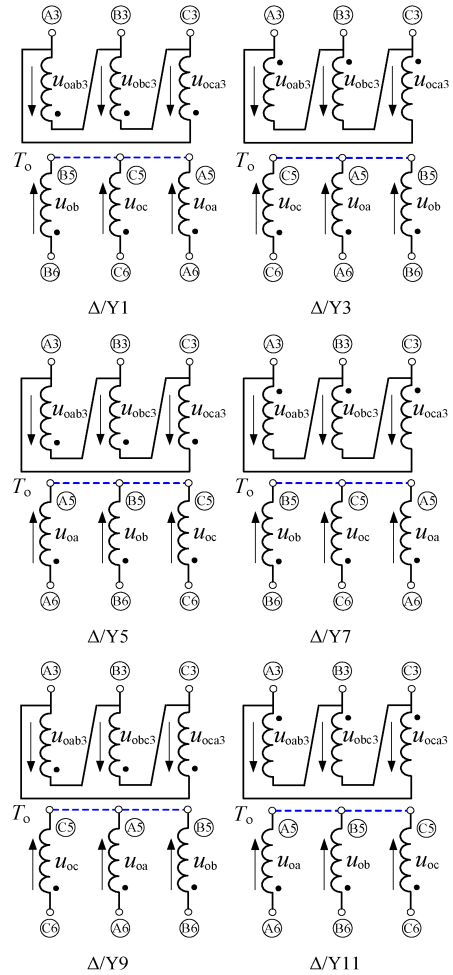

 Fig. 7. Six suitable connection groups for T_i .

Then, the regulation range of φ_o is

$$-30^\circ - (n_i + n_o) \times 30^\circ \leq \varphi_o \leq 30^\circ - (n_i + n_o) \times 30^\circ. \quad (18)$$

B. DPFC With Full 360° Regulation Zone

Comparing Figs. 6 and 9, one knows that DPFC would have a full 360° regulation zone for compensation line voltages (u_{oab} , u_{obc} , and u_{oca}) of grid if six basic regulation zones can be combined. It means that the regulation area of voltage u_{ab} in Fig. 9


 Fig. 8. Six suitable connection groups for T_o .

is six times of that in Fig. 6. So, in Fig. 9, with respect to the original grid voltage u_{iab} , not only the phase angle φ_r of the regulated grid voltage u_{ab} can be regulated lead or lag, but also its regulated amplitude U_{mL} can become larger or smaller than the amplitude U_{imL} of u_{iab} .

DPFC with full 360° regulation zone is shown in Fig. 10, where two selection switches (S_{Gi} and S_{Go}) are added based on the topology structure shown in Fig. 3. S_{Gi} has three selection modes, while S_{Go} has two modes.

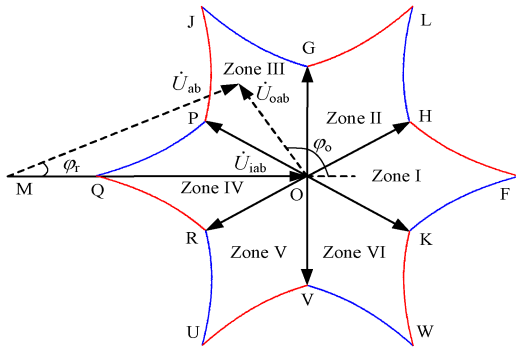


Fig. 9. Six basic regulation zones of DPFC.

TABLE II
REGULATION RANGE OF φ_0 IN DPFC

S_{Gi} mode	S_{Go} mode	Zone	Range of φ_0
First	First	I	$[-30^\circ, 30^\circ]$
Second	First	III	$[90^\circ, 150^\circ]$
Third	First	V	$[210^\circ, 270^\circ]$
First	Second	IV	$[150^\circ, 210^\circ]$
Second	Second	VI	$[-90^\circ, -30^\circ]$
Third	Second	II	$[30^\circ, 90^\circ]$

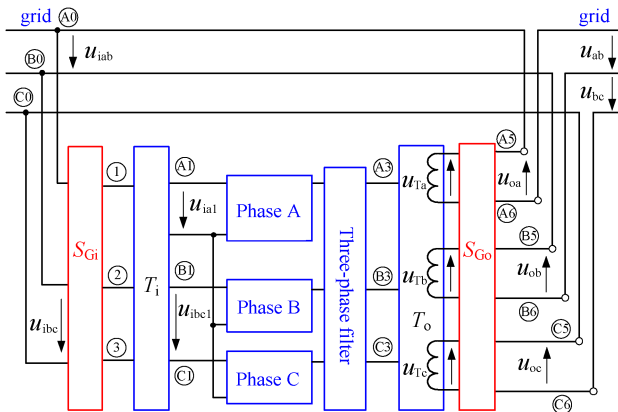


Fig. 10. DPFC with full 360° regulation zone.

Say, the first mode of S_{Gi} is to connect point A0 with point 1, point B0 with point 2, and point C0 with point 3. The second mode of S_{Gi} is to connect point A0 with point 2, point B0 with point 3, and point C0 with point 1. The third mode of S_{Gi} is to connect point A0 with point 3, point B0 with point 1, and point C0 with point 2. The first mode of S_{Go} is to let $u_{oa} = u_{Ta}$, $u_{ob} = u_{Tb}$, and $u_{oc} = u_{Tc}$, where u_{Ta} , u_{Tb} , and u_{Tc} are the output phase voltages of T_o . The second mode of S_{Go} is to let $u_{oa} = -u_{Ta}$, $u_{ob} = -u_{Tb}$, and $u_{oc} = -u_{Tc}$.

If assuming T_i is of $\Delta/Yn1$ -type connection group under the first mode of S_{Gi} , then T_i is of $\Delta/Yn7$ type under the second mode of S_{Gi} , and of $\Delta/Yn3$ type under the third mode of S_{Gi} . If assuming T_o is of $\Delta/Y1$ -type connection group under the first mode of S_{Go} , then T_o is of $\Delta/Y7$ type under the second mode of S_{Go} .

Apparently, under different combination modes of S_{Gi} and S_{Go} , compensation grid voltages can be regulated in a different zone, as shown in Table II. There are six different combination modes of selection switches that lead to six different zones; thus, DPFC has full 360° regulation zone for compensation of grid voltages.

Selection switch S_{Gi} can also be added after the secondary windings of input transformer T_i , where S_{Gi} has two selection modes to change the input voltage direction of each single-phase buck-type ac unit (for instance, if T_i is of $\Delta/Yn1$ -type connection group under the first mode of S_{Gi} , then T_i is of $\Delta/Yn5$ type under the second mode of S_{Gi}), while selection switch S_{Go} can

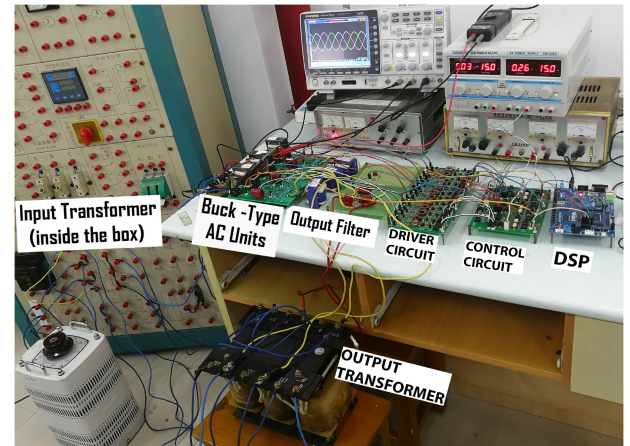


Fig. 11. DPFC prototype.

be added in front of the primary windings of output transformer T_o , where S_{Go} has three selection modes to change the connection group of T_o (for instance, if T_o is of $\Delta/Y1$ -type connection group under the first mode of S_{Go} , then T_o is of $\Delta/Y9$ type under the second mode of S_{Go} , and T_o is of $\Delta/Y5$ type under the third mode of S_{Go}).

Here, S_{Gi} has two selection modes, while S_{Go} has three modes. According to connection group of the input and output transformer, from Table I we know that 1) when S_{Gi} is under the first mode, the regulation area of DPFC is zones I, III, and V, corresponding to the first, second, and third modes of S_{Go} , respectively; and 2) when S_{Gi} is under the second mode, the regulation area of DPFC is zones IV, VI, and II, corresponding to the first, second, and third modes of S_{Go} , respectively.

There are other four options to place S_{Gi} and S_{Go} . Option 1 is that S_{Gi} is added before the primary windings of T_i and S_{Go} is added after the secondary windings of T_i . Option 2 is that S_{Gi} and S_{Go} are added before the primary windings of T_i and T_o , respectively. Option 3 is that S_{Gi} is added before the primary windings of T_o and S_{Go} is added after the secondary windings of T_o . Option 4 is that S_{Gi} and S_{Go} are added after the secondary windings of T_i and T_o , respectively.

IV. EXPERIMENTAL RESULTS

A prototype shown in Fig. 11 was manufactured, and experiments were performed for different transformer connection groups (the connection groups are changed manually; selection

TABLE III
 EXPERIMENTAL SPECIFICATIONS

specification	value
amplitude of original grid line voltage: U_{imL}	$200\sqrt{2}$ V
frequency of grid voltage: f	50 Hz
switching frequency: f_s	22.5 kHz
turn ratio of the input transformer T_i : N_i	200/121
turn ratio of the output transformer T_o : N_o	220/130
output filter inductance: $L_{fa}=L_{fb}=L_{fc}$	0.52 mH
output filter capacitor: $C_{f1}=C_{f2}=C_{f3}$	6.6 μ F
switch device	IRFP460A
DSP IC	TMS320F2812

switch will be discussed in further work) to verify the feasibility of DPFC. Table III lists its specifications.

When T_i is of $\Delta/Yn11$ -type connection group while T_o is of $\Delta/Y1$ (regulation area is zone I), $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$, Fig. 12 shows the experimental waveforms of DPFC in zone I.

For phase A buck-type ac unit in DPFC, its input voltage u_{ia1} and its output voltages u_{oa2} and u_{oa3} (between points A3 and E in Fig. 3) are shown in Fig. 12(a). Here, u_{ia1} is modulated by phase A buck-type ac unit with duty ratio d_a , u_{oa2} is a high-frequency pulse sequence, and u_{ia1} is its envelope curve. After most of the high-frequency components of u_{oa2} are filtered out by L_{fa} , u_{oa3} is obtained, which has not only fundamental voltage component, but also third harmonic voltage component and a small amount of high-frequency components.

The experimental waveforms of u_{oa3} , u_{ob3} (between points B3 and E in Fig. 3), u_{oab3} (the voltage across capacitor C_{f1}), and u_{oa} (phase A compensation voltage of grid) are shown in Fig. 12(b). u_{ob3} , the output voltage of phase B buck-type ac unit after the inductor L_{fb} , is similar to u_{oa3} but lags it by 120° at fundamental frequency. u_{oc3} (between points C3 and E in Fig. 3, but not shown in Fig. 12(b)), the output voltage of phase C buck-type ac unit after the inductor L_{fc} , is also similar to u_{oa3} but leads it by 120° at fundamental frequency. We know that $u_{oab3} = (u_{oa3} - u_{ob3})$, which is the voltage between points A3 and B3, and in theory only contains fundamental voltage, because the third harmonic voltage components in u_{oa3} and u_{ob3} cancel each other out, and capacitor C_{f1} filters off their high-frequency voltage components. Known from Figs. 8 and 12(b), u_{oa} lags u_{oab3} by -60° .

The experimental waveforms of the original grid line voltage u_{iab} (namely, input line voltage of DPFC), u_{oa} , u_{ob} (phase B compensation voltage of grid, lagging u_{oa} by 120°), and the regulated grid line voltage u_{ab} (where $u_{ab} = u_{iab} + u_{oa} - u_{ob}$) are shown in Fig. 12(c). u_{iab} is compensated by u_{oa} and u_{ob} to obtain u_{ab} , which has different phase and amplitude from u_{iab} . With the help of Code Composer Studio software and a DSP emulator, it is easy to watch variables. One can measure that in Fig. 12(c) u_{ab} is leading u_{iab} by phase angle φ_r of 7.99° and has an amplitude U_{mL} of $295\sqrt{2}$ V larger than that of u_{iab}

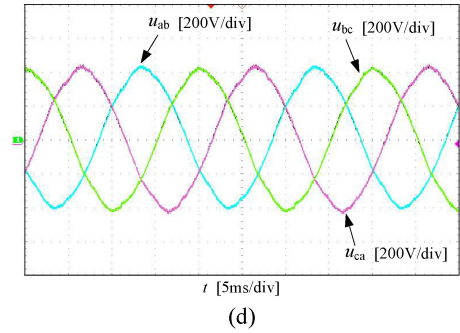
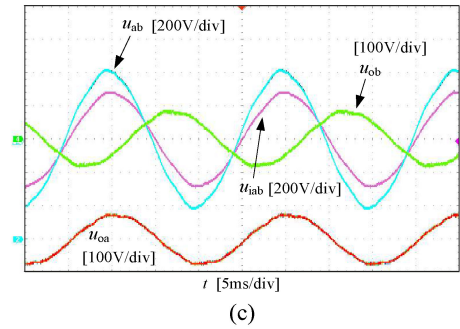
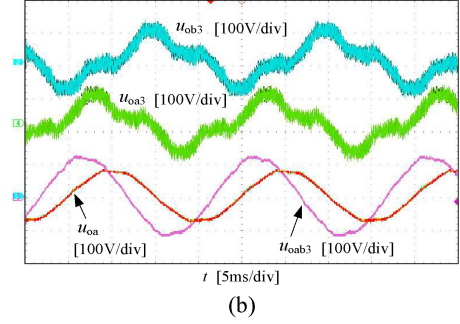
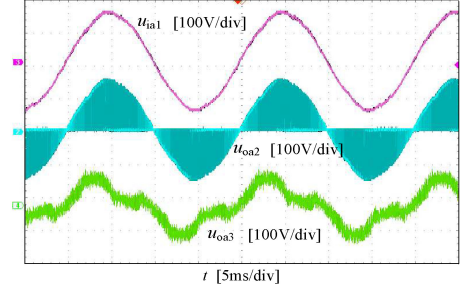


Fig. 12. Experimental waveforms of DPFC at zone I when $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$. (a) u_{ia1} , u_{oa2} , and u_{oa3} . (b) u_{oa3} , u_{ob3} , and u_{oab3} . (c) u_{iab} , u_{oa} , u_{ob} , and u_{ab} . (d) u_{ab} , u_{bc} , and u_{ca} .

(the measured values are almost the same as calculated values of 8.07° and $292\sqrt{2}$ V, respectively). As we can see, with open-loop control, the difference between the measured value and the calculated value is very small. It verifies the correctness of theoretical analysis of DPFC.

The experimental waveforms of the regulated grid line voltages u_{ab} , u_{bc} ($u_{bc} = u_{ibc} + u_{ob} - u_{oc}$), and u_{ca} ($u_{ca} = u_{ica} + u_{oc} - u_{oa}$) are shown in Fig. 12(d). With a phase difference of 120° , u_{ab} , u_{bc} , and u_{ca} are symmetrical in positive sequence (usually the original grid line voltages u_{iab} , u_{ibc} , and u_{ica} are symmetrical, which clearly means that the

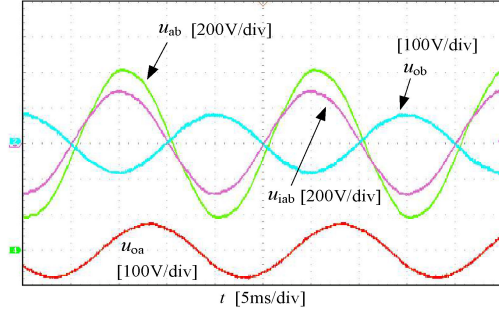


Fig. 13. Experimental waveforms of DPFC at zone I when $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = -24.8^\circ$.

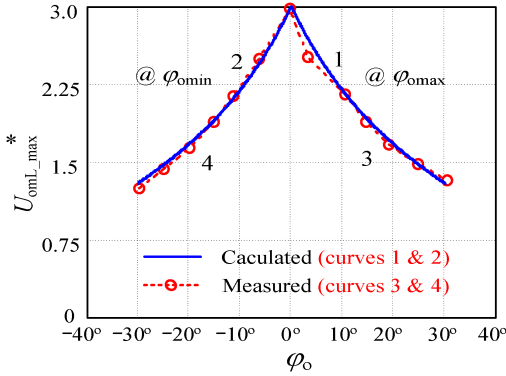


Fig. 14. Measured maximum U_{omL}^* and φ_{omax} or φ_{omin} .

compensation phase voltages u_{oa} , u_{ob} , and u_{oc} must be symmetrical).

While $k_0 = 0.500$, $k_2 = 0.419$, $\beta_2 = -24.8^\circ$, and regulation area is zone I, the experimental waveforms of u_{iab} , u_{oa} , u_{ob} , and u_{ab} are shown in Fig. 13, where u_{ab} lags u_{iab} by -8.10° and has an amplitude of $296\sqrt{2}$ V larger than that of u_{iab} (the measured values are almost the same as calculated values of -8.07° and $292\sqrt{2}$ V, respectively).

We know that, for obtaining the desired regulated grid line voltage u_{ab} (or u_{bc} and u_{ca}), the compensation line voltage of grid Δu_{ab} ($\Delta u_{ab} = u_{oab} = u_{oa} - u_{ob}$, $\Delta u_{bc} = u_{obc} = u_{ob} - u_{oc}$, and $\Delta u_{ca} = u_{oca} = u_{oc} - u_{oa}$) should be controlled. When T_i is of $\Delta/Yn11$ -type connection group and T_o is of $\Delta/Y1$ type (u_{oab} , u_{obc} , and u_{oca} can be measured easily when points A5, B5, and C5 are connected together while the three secondary windings of the output transformer T_o are not connected to the grid), the measurement results (the maximum of U_{omL}^* and φ_{omax} or φ_{omin}) are shown in Fig. 14.

Curves 3 and 1 in Fig. 14 show the measured and calculated φ_{omax} and the maximum of U_{omL}^* , respectively. While curves 4 and 2 show the measured and calculated φ_{omin} and the maximum of U_{omL}^* , respectively. The measured regulation area of the phase angle φ_o and the amplitude U_{omL}^* of u_{oab} is covered by curves 3 and 4. (The calculated regulation area of φ_o and U_{omL}^* is covered by curves 1 and 2.)

While T_i is of $\Delta/Yn3$ -type connection group and T_o is of $\Delta/Y7$ type (regulation area is zone II), $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$, the experimental waveforms of the original grid line voltage u_{iab} , the grid compensation phase voltages u_{oa} and u_{ob} , and the regulated grid line voltage u_{ab} are shown in Fig. 15,

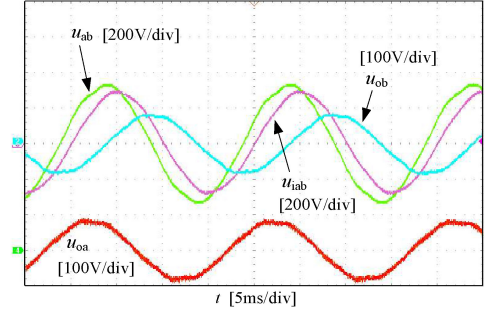


Fig. 15. Experimental waveforms of DPFC at zone II when $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$.

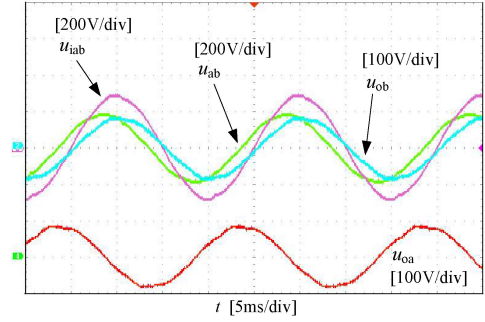


Fig. 16. Experimental waveforms of DPFC at zone III when $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$.

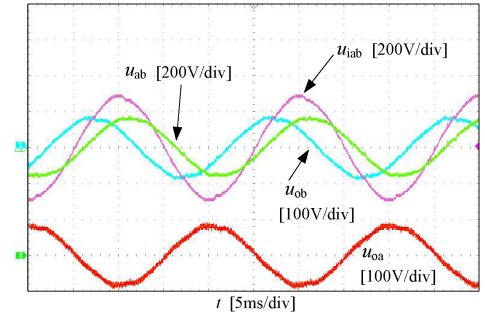


Fig. 17. Experimental waveforms of DPFC at zone IV when $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$.

where u_{ab} is leading u_{iab} by 24.97° and has an amplitude of $237\sqrt{2}$ V larger than that of u_{iab} (the measured values are almost the same as calculated values of 24.94° and $233\sqrt{2}$ V, respectively).

While T_i is of $\Delta/Yn7$ -type connection group and T_o is of $\Delta/Y1$ type (regulation area is zone III), $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$, the experimental waveforms of u_{iab} , u_{oa} , u_{ob} , and u_{ab} are shown in Fig. 16, where u_{ab} is leading u_{iab} by 25.11° and has an amplitude of $133\sqrt{2}$ V smaller than that of u_{iab} (the calculated values are 25.06° and $131\sqrt{2}$ V, respectively, which are almost the same as measured values).

While T_i is of $\Delta/Yn11$ -type connection group and T_o is of $\Delta/Y7$ type (regulation area is zone IV), $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$, the experimental waveforms of u_{iab} , u_{oa} , u_{ob} , and u_{ab} are shown in Fig. 17, where u_{ab} lags u_{iab} by -21.19° and has an amplitude of $114\sqrt{2}$ V smaller than that of u_{iab} (the calculated values are -20.16° and $112\sqrt{2}$ V, respectively, which are very close to the measured values).

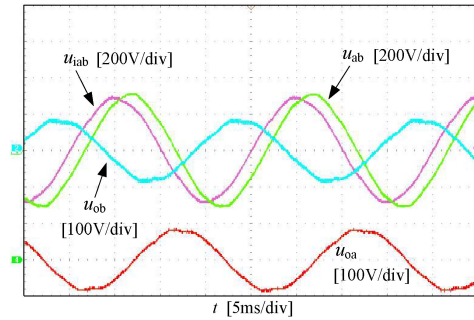


Fig. 18. Experimental waveforms of DPFC at zone V when $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$.

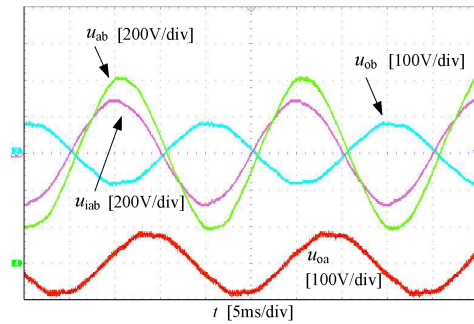


Fig. 19. Experimental waveforms of DPFC at zone VI when $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$.

While T_1 is of $\Delta/Yn3$ -type connection group and T_o is of $\Delta/Y1$ type (regulation area is zone V), $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$, the experimental waveforms of u_{iab} , u_{oa} , u_{ob} , and u_{ab} are shown in Fig. 18, where u_{ab} lags u_{iab} by -26.74° and has an amplitude of $225\sqrt{2}$ V larger than that of u_{iab} (the calculated values are -26.94° and $222\sqrt{2}$ V, respectively, which are almost the same as measured values).

While T_1 is of $\Delta/Yn7$ -type connection group and T_o is of $\Delta/Y7$ type (regulation area is zone VI), $k_0 = 0.500$, $k_2 = 0.419$, and $\beta_2 = 24.8^\circ$, the experimental waveforms of u_{iab} , u_{oa} , u_{ob} , and u_{ab} are shown in Fig. 19, where u_{ab} lags u_{iab} by -11.50° and has an amplitude of $293\sqrt{2}$ V larger than that of u_{iab} (the measured values are almost the same as calculated values of -11.37° and $290\sqrt{2}$ V, respectively).

V. CONCLUSION

For power transmission in grid, a new concept of DPFC was proposed on the basis of single-stage ACCPA. Similar to UPFC, DPFC also has a parallel transformer and a series transformer but has only one-stage conversion circuit and has no dc energy storage element that usually results in high failure rate. By connecting its regulated output compensation voltage with grid in series, DPFC is able to regulate the amplitude and phase angle of grid node voltage, respectively and simultaneously. Thus, DPFC can effectively control active power flow and reactive power flow in grid separately.

The basic circuit structure of DPFC is simple, and only consists of three single-phase buck-type ac units with a three-phase output filter. With respect to the input voltage of DPFC basic

circuit, the phase regulation width of its output voltage is 60° , which is easy to extend to 360° with two groups of selection switches that change the connection groups of the input transformer and output transformer. The experimental results of the prototype showed its feasibility and verified the theoretical analysis of DPFC.

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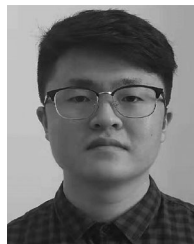
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