

# Robust Stability Analysis of Synchronverters Operating in Parallel

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**Abstract**—Recent studies have shown how synchronization units of converters operating nearby may interact with each other, affecting the stability of the system. Synchronverters are able to self-synchronize to the grid without the need of a dedicated unit because they can reproduce the power synchronization mechanism of synchronous machines. Recently, the robust stability of a synchronverter has been investigated by means of structured singular values (commonly called  $\mu$ -analysis). In this paper,  $\mu$ -analysis is performed to investigate how the robust stability of a synchronverter is affected by the presence of another converter of the same type operating in parallel. It is demonstrated that the parallel operation of synchronverters reduces their robust stability and a possible solution is proposed, based on the implementation of virtual impedances in the control algorithm. An accurate state-space model of the system under study is developed by adopting the component connection method and the robust stability analysis is validated against time-domain simulations in MATLAB/Simulink/PLECS and experimental results with a power-hardware-in-the-loop test bench.

**Index Terms**— $\mu$ -Analysis, parallel operation of synchronverters, structured singular values (SSVs), synchronverter robust stability analysis, virtual impedance.

## I. INTRODUCTION

THE growth of power-electronics-based generation has raised concerns among system operators about power system stability. New control strategies, which guarantee stable operation of the system even in scenarios with high penetration of inverters, are requested. Virtual synchronous machines (VSMs) have been proposed as possible solutions and their capability to self-synchronize themselves to the main utility grid without the need of a dedicated unit is appealing [1]–[6]. In fact, recent studies have shown that the synchronization unit of a grid-connected converter has a significant impact on the frequency behavior of the converter itself, with consequences for its stability [7], [8]. Furthermore, interaction between synchronization units of converters operating nearby may occur, especially under weak

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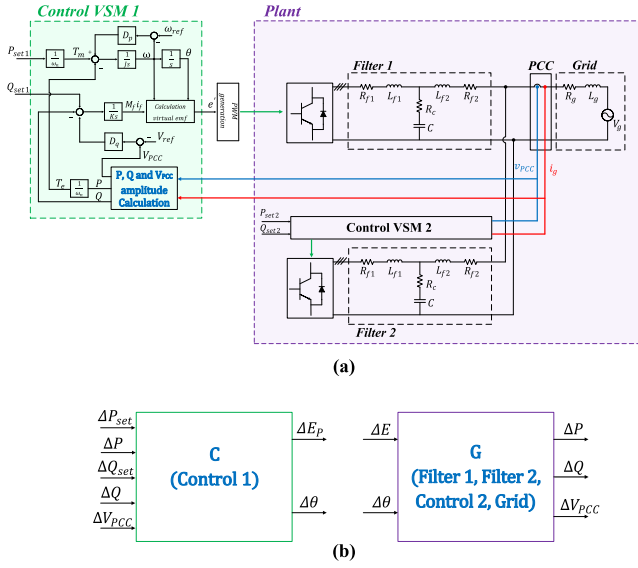


Fig. 1. (a) Scheme of the studied system. (b) Inputs and outputs of the linearized control and plant model.

the robust stability of synchronverters operating in parallel by means of  $\mu$ -analysis. Similarly to [10], the system is split into two parts, namely the control and the plant, whose model is obtained adopting the approach presented in [23]. The presence of one or more units connected in parallel affects the characteristics of the plant where the synchronverter is connected, with consequent effects on system stability.

This paper is structured as follows. The system under study is introduced in Section II, along with the CCM technique adopted to obtain the state-space representation of the system. In Section III, the concept of  $\mu$ -analysis is briefly introduced and then applied to the investigated case. In Section IV, experimental results performed with a power-hardware-in-the-loop (PHIL) test bench are reported and the developed model is validated against time-domain simulations and measurement. Finally, conclusions are drawn in Section V.

## II. MODELING OF THE SYSTEM

A scheme of the system under study is shown in Fig. 1(a). It is composed of two inverters with their respective controls and output filters, sharing the same point of common coupling (PCC). Similarly to [20], in order to perform the  $\mu$ -analysis, the system is split into two parts, namely the control and the plant, whose inputs and outputs are shown in Fig. 1(b). Differently from [20], the plant not only consists of the converter filter and the grid, but it also includes the control and the filter of the second synchronverter connected in parallel. Nevertheless, the small-signal model presented in [20] cannot be extended easily to the case of multiple synchronverters operating in parallel, since the state-space representation of the control and the plant, obtained through linearization of the respective equations, has been adapted to the specific case. The most efficient way for including other inverters in the analysis is to adopt the CCM technique [23], [24]. It consists of connecting state-space

representations of several subsystems in a modular manner, such that the analysis can be extended easily to a more complex system. The state-space representation of the overall system is therefore obtained by connecting to each other independent subsystems by means of the so-called interconnection matrices. The approach presented in this paper is based on [23], where the CCM was used to obtain the linearized model of two synchronverters operating in parallel. However, differently from [23], here the CCM is used in order to obtain the model of the plant  $G$ , indicated in Fig. 1(b).

In Fig. 2, the block diagram representation of the overall system under study is shown, along with inputs and outputs of each subsystem. The dark red and dark blue arrows indicate inputs and outputs of the plant, labeled as  $u_{\text{plant}}$  and  $y_{\text{plant}}$ , respectively. The state-space representation of the plant is given as follows:

$$\begin{cases} \dot{x}_{\text{plant}} = F_{\text{int}}x_{\text{plant}} + G_{\text{int}}u_{\text{plant}} \\ y_{\text{plant}} = H_{\text{int}}x_{\text{plant}} + J_{\text{int}}u_{\text{plant}} \end{cases} \quad (1)$$

where  $F_{\text{int}}$ ,  $G_{\text{int}}$ ,  $H_{\text{int}}$ , and  $J_{\text{int}}$  are defined as

$$\begin{cases} F_{\text{int}} = A_d + B_d L_{11} (I - D_d L_{11})^{-1} C_d \\ G_{\text{int}} = B_d L_{11} (I - D_d L_{11})^{-1} D_d L_{12} + B_d L_{12} \\ H_{\text{int}} = B_d L_{21} (I - D_d L_{11})^{-1} C_d \\ J_{\text{int}} = L_{21} (I - D_d L_{11})^{-1} D_d L_{12} + L_{22}. \end{cases} \quad (2)$$

$A_d$ ,  $B_d$ ,  $C_d$ , and  $D_d$  are sparse block diagonal matrices obtained from the state-space matrices of the single subsystems, namely

$$\begin{cases} A_d = \text{diag}\{A_{c2}, A_{LCL1}, A_{LCL2}, A_g, A_{PQ}\} \\ B_d = \text{diag}\{B_{c2}, B_{LCL1}, B_{LCL2}, B_g, B_{PQ}\} \\ C_d = \text{diag}\{C_{c2}, C_{LCL1}, C_{LCL2}, C_g, C_{PQ}\} \\ D_d = \text{diag}\{D_{c2}, D_{LCL1}, D_{LCL2}, D_g, D_{PQ}\} \end{cases} \quad (3)$$

whereas  $L_{11}$ ,  $L_{12}$ ,  $L_{21}$ , and  $L_{22}$  represent the aforementioned interconnection matrices, indicating how inputs and outputs of the subsystems are connected to each other. Defining  $u_{\text{sub}} = [u_{c2} u_{LCL1} u_{LCL2} u_g u_{PQ}]^T$  as the vector containing all the inputs of the subsystems and  $y_{\text{sub}} = [y_{c2} y_{LCL1} y_{LCL2} y_g y_{PQ}]^T$  as the vector containing all the outputs, the following relation is valid:

$$\begin{cases} u_{\text{sub}} = L_{11}y_{\text{sub}} + L_{12}u_{\text{plant}} \\ y_{\text{plant}} = L_{21}y_{\text{sub}} + L_{22}u_{\text{plant}} \end{cases} \quad (4)$$

where  $u_{\text{plant}}$  and  $y_{\text{plant}}$  are the vectors containing inputs and outputs of the interconnected system, namely  $u_{\text{plant}} = [\Delta E \ \Delta \theta]^T$  and  $y_{\text{plant}} = [\Delta P_1 \ \Delta Q_1 \ \Delta V_{\text{PCC}}]^T$ . According to [23], the state-space representation of each subsystem of Fig. 2 is provided in the following.

### A. Control

The description of the synchronverter control, along with its state-space representation, is reported in [10]. The state-space

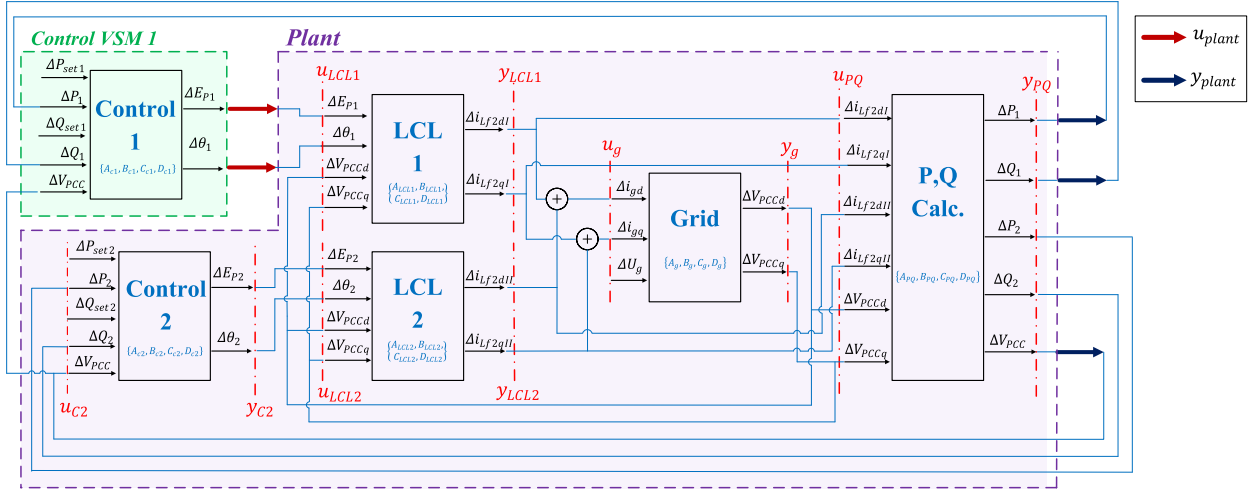


Fig. 2. Structure of the system for CCM analysis.

matrices  $A_c$ ,  $B_c$ ,  $C_c$ , and  $D_c$  of the control block in Fig. 2 are shown as follows:

$$\begin{aligned}
 A_c &= \begin{bmatrix} 0 & 0 & 0 \\ 0 & -\frac{D_p}{J} & 0 \\ 0 & 1 & 0 \end{bmatrix}, \\
 B_c &= \begin{bmatrix} 0 & 0 & \frac{1}{K} & -\frac{1}{K} & -\frac{D_q}{K} \\ \frac{1}{\omega_0 J} & -\frac{1}{\omega_0 J} & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 \end{bmatrix} \\
 C_c &= \begin{bmatrix} \omega_0 & M_f i_{f0} & 0 \\ 0 & 0 & 1 \end{bmatrix}, D_c = [0^{2 \times 5}]. \quad (5)
 \end{aligned}$$

$D_p$ ,  $D_q$ ,  $K$ , and  $J$  are the control parameters shown in the control block diagram in Fig. 1(a), whereas  $\omega_0$  and  $M_f i_{f0}$  are initial conditions of the system states [10].

### B. LCL Filter

Choosing the current of the converter-side inductor  $i_{L1}$ , the current of the grid-side inductor  $i_{L2}$ , and the capacitor voltage  $v_c$  as state-space variables of the subsystem, the equations of the LCL filter shown in Fig. 1 are reported as follows [10]:

$$\begin{cases} L_{f1} \frac{di_{L1}}{dt} = e - v_c - R_c(i_{L1} - i_{L2}) - R_{f1}i_{L1} \\ C \frac{dv_c}{dt} = i_{L1} - i_{L2} \\ L_{f2} \frac{di_{L2}}{dt} = v_c - v_{PCC} + R_c(i_{L1} - i_{L2}) - R_{f2}i_{L2}. \end{cases} \quad (6)$$

Writing (6) in  $dq$  coordinates, the following state-space matrices  $A_{LCL}$ ,  $B_{LCL}$ ,  $C_{LCL}$ , and  $D_{LCL}$  are obtained:

$$\begin{aligned}
 A_{LCL} &= \begin{bmatrix} \frac{-R_c - R_{f1}}{L_{f1}} & \omega_0 & -\frac{1}{L_{f1}} & 0 & \frac{R_c}{L_{f1}} & 0 \\ -\omega_0 & \frac{-R_c - R_{f1}}{L_{f1}} & 0 & -\frac{1}{L_{f1}} & 0 & \frac{R_c}{L_{f1}} \\ \frac{1}{C_f} & 0 & 0 & \omega_0 & -\frac{1}{C_f} & 0 \\ 0 & \frac{1}{C_f} & -\omega_0 & 0 & 0 & -\frac{1}{C_f} \\ \frac{R_c}{L_{f2}} & 0 & \frac{1}{L_{f2}} & 0 & \frac{-R_c - R_{f2}}{L_{f2}} & \omega_0 \\ 0 & \frac{R_c}{L_{f2}} & 0 & \frac{1}{L_{f2}} & -\omega_0 & \frac{-R_c - R_{f2}}{L_{f2}} \end{bmatrix} \\
 B_{LCL} &= \begin{bmatrix} \frac{\cos \theta_0}{L_{f1}} & -\frac{V_0 \sin \theta_0}{L_{f1}} & 0 & 0 \\ \frac{\sin \theta_0}{L_{f1}} & -\frac{V_0 \cos \theta_0}{L_{f1}} & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & -\frac{1}{L_{f2}} & 0 \\ 0 & 0 & 0 & -\frac{1}{L_{f2}} \end{bmatrix} \\
 C_{LCL} &= \begin{bmatrix} 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}, D_{LCL} = [0^{2 \times 4}]. \quad (7)
 \end{aligned}$$

### C. Grid

The grid has been modeled using its Thévenin equivalent at the PCC. In order to obtain the information about the voltage at the connection point, another state has been introduced in the grid model, namely a shunt capacitor at the input terminal, as shown in Fig. 3. Choosing very high values for the shunt components  $R_s$  and  $C_s$ , so that all the current  $i_g$  flows into the grid, the PCC voltage is represented by the voltage  $v_{cs}$  across the shunt capacitor. The corresponding grid equations are provided

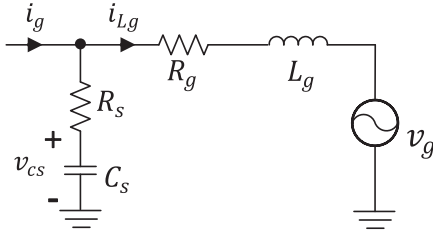


Fig. 3. Equivalent grid model.

as follows:

$$\begin{cases} L_g \frac{di_{Lg}}{dt} = R_s(i_g - i_{Lg}) + v_{cs} - v_g - R_g i_{Lg} \\ C_s \frac{dv_{cs}}{dt} = i_g - i_{Lg} \end{cases} \quad (8)$$

which transformed in  $dq$  coordinates result in the following state-space matrices of the grid subsystem:

$$\begin{aligned} A_g &= \begin{bmatrix} \frac{-R_s - R_g}{L_g} & \omega_0 & \frac{1}{L_g} & 0 \\ -\omega_0 & \frac{-R_s - R_g}{L_g} & 0 & \frac{1}{L_g} \\ -\frac{1}{C_s} & 0 & 0 & \omega_0 \\ 0 & -\frac{1}{C_s} & -\omega_0 & 0 \end{bmatrix}, \\ B_g &= \begin{bmatrix} \frac{R_s}{L_g} & 0 & -1 \\ 0 & \frac{R_s}{L_g} & 0 \\ \frac{1}{C_s} & 0 & 0 \\ 0 & \frac{1}{C_s} & 0 \end{bmatrix} \\ C_g &= \begin{bmatrix} 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}, D_g = [0^{2 \times 3}]. \end{aligned} \quad (9)$$

#### D. $P$ , $Q$ , and $V_{PCC}$ Calculation

Calculation of active and reactive power of each converter, along with the amplitude of the PCC voltage, involves algebraic equations. In order to obtain a state-space formulation out of these calculations, a first-order low-pass filter with a very high cutoff frequency  $\omega_c$  is introduced in the calculation of  $\Delta P$ ,  $\Delta Q$ , and  $\Delta V_{PCC}$  reported in [10]

$$\begin{cases} \Delta P = \left(\frac{\omega_c}{s + \omega_c}\right) \frac{3}{2} (I_{Lf2d0} \Delta v_{PCCd} + I_{Lf2q0} V_{PCCq} \\ \quad + V_{PCCd0} \Delta i_{Lf2d} + V_{PCCq0} \Delta i_{Lf2q}) \\ \Delta Q = \left(\frac{\omega_c}{s + \omega_c}\right) \frac{3}{2} (-I_{Lf2d0} \Delta v_{PCCd} + I_{Lf2d0} V_{PCCq} \\ \quad + V_{PCCq0} \Delta i_{Lf2d} - V_{PCCd0} \Delta i_{Lf2q}) \\ \Delta V_{PCC} = \left(\frac{\omega_c}{s + \omega_c}\right) \frac{V_{PCCd0} \Delta v_{PCCd} + V_{PCCq0} \Delta v_{PCCq}}{\sqrt{V_{PCCd0}^2 + V_{PCCq0}^2}} \end{cases} \quad (10)$$

where the quantities with the subscript “0” indicate the values at the operating point and  $i_{Lf2dq} = \{i_{Lf2dqI}, i_{Lf2dqII}\}$ .

The corresponding state-space matrices are provided as follows:

$$A_{PQ} = \omega_c [I^{(5)}], C_{PQ} = [I^{(5)}], D_{PQ} = [0^{5 \times 6}]$$

$$B_{PQ}$$

$$= \omega_c \frac{3}{2} \begin{bmatrix} I_{Lf2dI_0} & I_{Lf2qI_0} & V_{d_0} & V_{q_0} & 0 & 0 \\ -I_{Lf2qI_0} & I_{Lf2dI_0} & V_{q_0} & -V_{d_0} & 0 & 0 \\ I_{Lf2dII_0} & I_{Lf2qII_0} & 0 & 0 & V_{d_0} & V_{q_0} \\ -I_{Lf2qII_0} & I_{Lf2dII_0} & 0 & 0 & V_{q_0} & -V_{d_0} \\ \frac{V_{d_0}}{\Delta} & \frac{V_{q_0}}{\Delta} & 0 & 0 & 0 & 0 \end{bmatrix} \quad (11)$$

where  $V_{d_0} = V_{PCCd_0}$  and  $\Delta = \sqrt{V_{PCCd_0}^2 + V_{PCCq_0}^2}$ .

#### E. Interconnection Matrices

$L_{11}$ ,  $L_{12}$ ,  $L_{21}$ , and  $L_{22}$  in (4) are sparse matrices of zeros and ones accounting for the connection among inputs and outputs of the subsystems composing the overall interconnected system. These are reported in the Appendix of the paper and their sizes are shown as follows:

$$\begin{cases} u_{\text{sub}} = [L_{11}]^{22 \times 13} y_{\text{sub}} + [L_{12}]^{22 \times 2} u_{\text{plant}} \\ y_{\text{plant}} = [L_{21}]^{3 \times 13} y_{\text{sub}} + [L_{22}]^{3 \times 2} u_{\text{plant}} \end{cases} \quad (12)$$

Although already defined at the beginning of this section,  $u_{\text{sub}}$ ,  $u_{\text{plant}}$ ,  $y_{\text{sub}}$ , and  $y_{\text{plant}}$  are reported in the following for simplicity, where the notations of Fig. 2 for inputs and outputs of each subsystem are adopted:

$$\begin{cases} u_{\text{sub}} = [u_{c2} \ u_{LCL1} \ u_{LCL2} \ u_g \ u_{PQ}]^T \\ y_{\text{sub}} = [y_{c2} \ y_{LCL1} \ y_{LCL2} \ y_g \ y_{PQ}]^T \\ u_{\text{plant}} = [\Delta E_{p1} \ \Delta \theta_1]^T \\ y_{\text{plant}} = [\Delta P_1 \ \Delta Q_1 \ \Delta V_{PCC}]^T \end{cases} \quad (13)$$

### III. ROBUST STABILITY ANALYSIS

#### A. Introduction to $\mu$ -Analysis

Eigenvalue analysis is among the commonly adopted approaches for stability assessment of grid-connected converters and generally for power systems studies [25]. By means of modal analysis, the states mainly contributing to the oscillatory modes of a large interconnected system can be determined [25], [27]. For instance, this approach has been adopted in [23] in order to investigate the parallel operation of synchronverters. Although through eigenvalue analysis system stability can be efficiently assessed, it is well known that for MIMO systems, eigenvalues are a poor measure of robustness [18]. Indeed, eigenvalues provide only information about a specific configuration of the system when inputs and outputs are in the same direction, namely the direction of the eigenvectors. They do not take into account the possible interactions between different channels that typically occur in a MIMO system. For single-in single-out (SISO) analysis, gain and phase margin are identified

TABLE I  
SIMULATION PARAMETERS

Description	Symbol	Value
Inverter rated power	$S_{n1}=S_{n2}$	300 kVA
Line-to-line voltage	$V_{LL}$	400 V (rms)
Rated grid frequency	$f_g$	50 Hz
Grid inductance	$L_g$	0.05 pu
Inverter-side filter inductance	$L_{f1I}=L_{f1II}$	0.08 pu
Grid-side filter inductance	$L_{f2I}=L_{f2II}$	0.02 pu
Grid resistance	$R_g$	0.005 pu
Inverter-side filter resistance	$R_{f1I}=R_{f1II}$	0.02 pu
Grid-side filter resistance	$R_{f2I}=R_{f2II}$	0.02 pu
Capacitor damping resistance	$R_{cI}=R_{cII}$	0.18 pu
Filter capacitor	$C_I=C_{II}$	0.05 pu
Virtual inertia	$J_1=J_2$	0.6687
K factor	$K_1=K_2$	37459
P-Droop coefficient	$D_{p1}=D_{p2}$	60.8 (5%)
Q-Droop coefficient	$D_{q1}=D_{q2}$	18371 (5%)

as indicators of robustness. However, the definition of phase and gain margin of SISO systems cannot be easily extended to MIMO systems. For this reason, for accurate robustness assessment of MIMO systems, multivariable analysis is required and different methods have been developed, such as the SSVs or  $\mu$ -analysis [18]–[20]. To this extent, singular values provide better information about the gains of the plants and the robustness of the control is verified against a defined set of system uncertainties. This method allows to span a set of possible system configurations instead of verifying stability only for a specific condition.

In [20], the robust stability of a synchronverter connected to the grid through an *LCL* filter has been investigated by means of  $\mu$ -analysis. The calculated  $\mu$ -factor provides an indication of system stability, namely the inverse of the highest peak of  $\mu$  over the investigated frequency range represents the stability margin of the system [18]–[20]. The results of the performed analysis are strongly related to the chosen plant uncertainty and, in [20], a multiplicative input uncertainty with specific characteristics has been defined. Among the results presented in [20], the fact that the robust stability of the synchronverter is augmented when the converter is connected to a weaker grid is probably the most interesting one. In fact, this represents an opposite trend compared to the behavior shown by converters relying on dedicated synchronization units [9]. In the following, the  $\mu$ -analysis performed in [20] is extended to the case of two synchronverters operating in parallel. The presence of the second synchronverter modifies the characteristics of the plant with consequences on the results obtained in [20].

### B. Application to the System Under Study

The same operating conditions of the converter considered in [20] are assumed for the analysis. Filter and control parameters of the two synchronverters are reported in Table I, whereas the considered frequency-dependent uncertainty function is shown in Fig. 4. It presents an amplitude of 50% at low frequency, increasing till 500% at very high frequencies. This accounts for

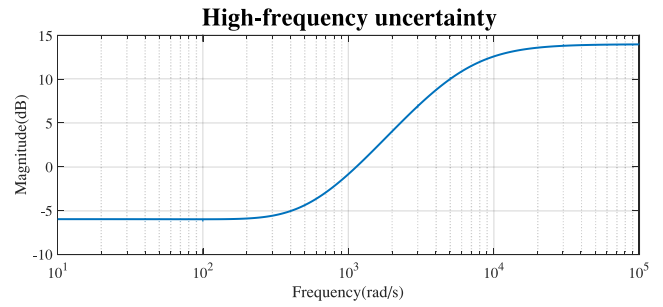


Fig. 4. Multiplicative input uncertainty used for the  $\mu$ -analysis.

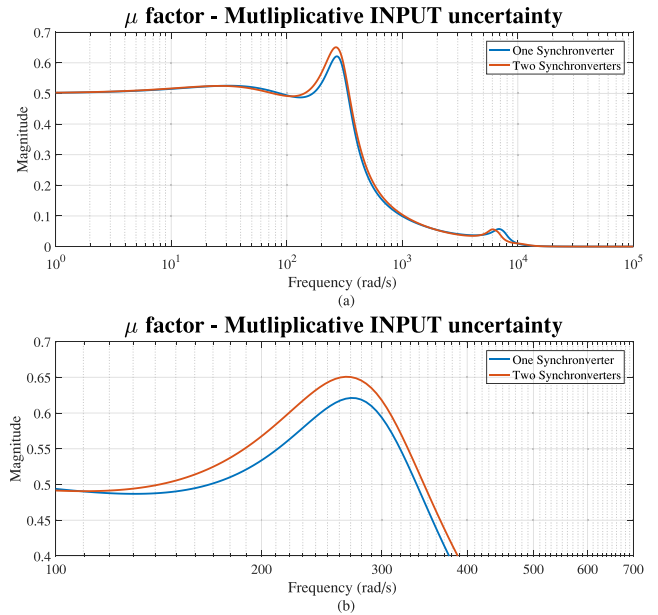


Fig. 5.  $\mu$  factor. (a) Whole frequency range. (b) Zoom.

low frequency uncertainties due to parametric uncertainty in the model as well as high frequency neglected dynamic effects or resonances in the grid due to the presence of other converters operating nearby.

In Fig. 5, the  $\mu$ -factor over the investigated frequency range related to the defined set of plant uncertainties is shown. The blue curve indicates the results when the converter operates alone, whereas the red curve corresponds to the parallel operation. The results clearly show how the highest peak of  $\mu$  of approximately 0.61 at a frequency of 277 rad/s is shifted to a value of  $\mu \approx 0.65$  at the same frequency for parallel operation, corresponding to a reduction of the stability margin.

In Fig. 6(a) and (b), the  $\mu$ -factor for a sweep of the grid SCR from 20 to 2 is shown for both examined cases. When the synchronverter operates alone, the increase of the impedance between the converter and the grid enhances the robust stability of the converter against high frequency uncertainties in the plant. This has been justified in [20] by the fact that a synchronverter reproduces the behavior of a real synchronous machine (SM) and basically behaves as a voltage source behind an impedance. The increase of the impedance between the voltage source and the grid produces the effects of a stability margin improvement [6].

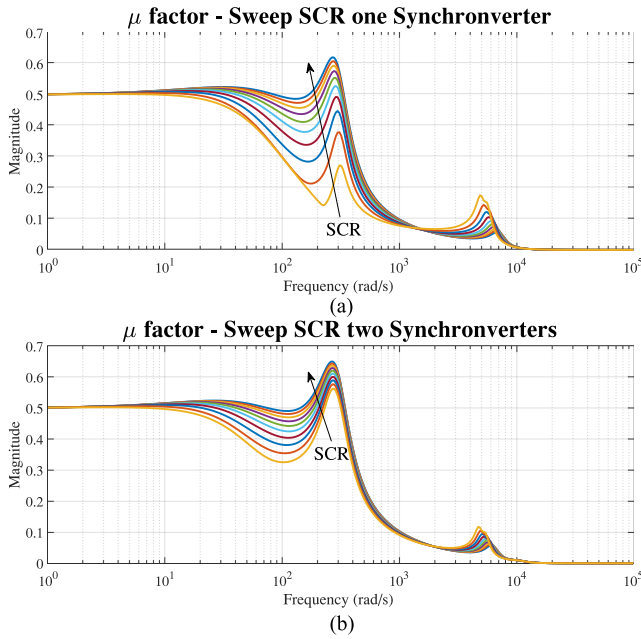


Fig. 6.  $\mu$  factor, sweep SCR from 20 to 2. (a) One synchronverter. (b) Two synchronverters.

Fig. 6(b) clearly shows how the increase of the grid impedance does not produce the same effects when the synchronverter is operating in parallel with a second synchronverter. In fact, the other synchronverter nearby also behaves as a grid forming unit [28], reducing the benefits provided by the higher impedance between the converter and the grid.

### C. Possible Countermeasure

The analysis performed in the previous section has highlighted that the stability margin of a synchronverter is reduced when operating in parallel to a converter of the same type. The issue related to parallel operation is not only confined to synchronverters, but this is a general challenge for power-electronics-based converters. Interactions among current controls and filters have been extensively investigated in the literature [15], [26], [27], along with the negative effects of synchronization units on the converter stability, which has been addressed in several works [7], [8], [16]. Since synchronverters do not require a dedicated unit for their synchronization to the grid, the issues related to interactions among synchronization units of converters operating in parallel observed in [9] and [29] can be avoided. Although the control loops of a synchronverter are different from those of standard grid-connected converters using dedicated synchronization units, the results of the analysis performed in this paper show that the parallel operation among these types of converters has also effects on their stability. According to the considerations reported in the previous sections and in [20], the impedance between the synchronverter and any other equivalent voltage source operating nearby has to be increased in order to enhance its robust stability. This can be interpreted as the need for an increase of the electrical distance between the two voltage sources. It is however important to point out that the synchronverter is not the only control structure

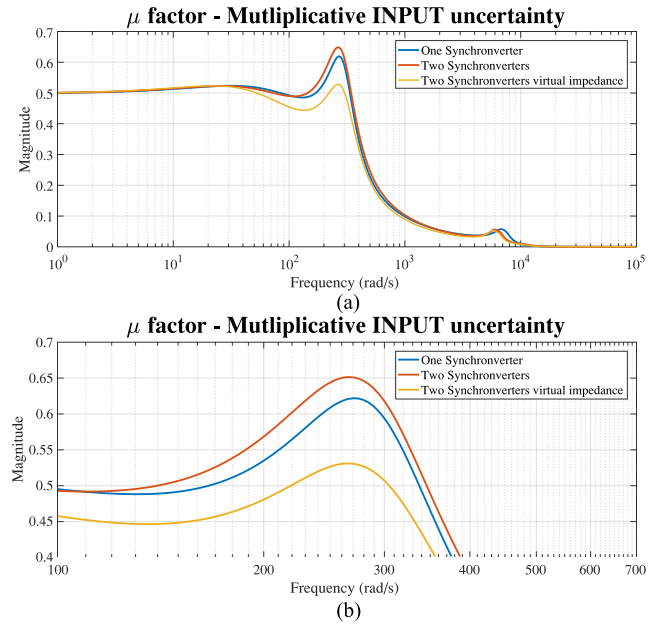


Fig. 7.  $\mu$  factor. (a) Whole frequency range. (b) Zoom.

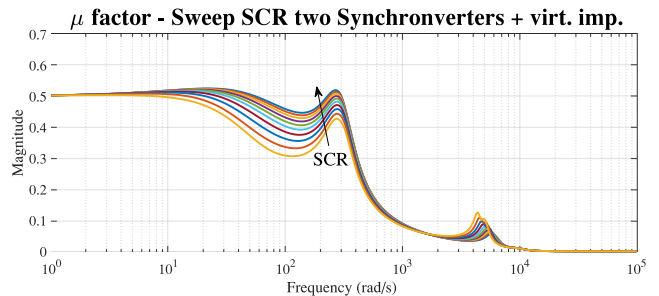


Fig. 8.  $\mu$  factor, sweep SCR from 20 to 2. (a) Two synchronverters. (b) Two synchronverters and increased filter impedance by 20%.

proposed in the literature based on the power synchronization mechanism of an SM. In [30], a generalized formulation for power-synchronization-based control algorithms has been presented and the results shown in this paper can be extended to such generalized structure.

Typical values of stator impedance of real SMs are generally much higher than the output impedance of standard converter filters. Normally, synchronous reactances of SMs are in the range of 1.5–2 p.u. [25], whereas the output impedance of a converter filter is generally significantly lower (in the range of 0.1–0.2 p.u.) [6]. Hence, the effects of an increase of filter parameters on the  $\mu$ -factor are reported in Fig. 7. The blue and red curves already shown in Fig. 5 are compared to the yellow curve accounting for the results of the  $\mu$ -analysis obtained when the converter filter impedances of both synchronverters have been increased by 20%. The results show clearly that the maximum peak of the  $\mu$ -factor is reduced to a value of  $\mu \approx 0.53$ , corresponding to an increase of the stability margin. Fig. 8 shows the variation of the  $\mu$ -factor for a sweep of the grid SCR from 20 to 2 when the two converters are connected in parallel and whose filter parameters, namely  $L_{f1}$ ,  $L_{f2}$ ,  $R_{f1}$ , and  $R_{f2}$ , have been

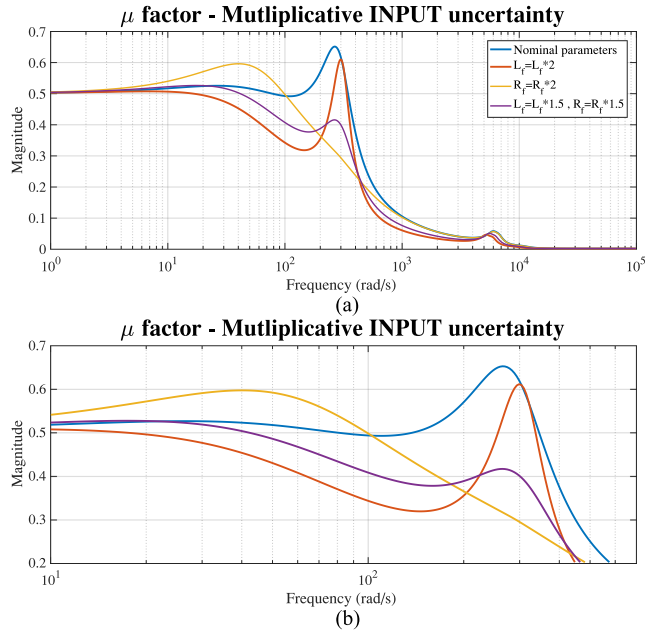


Fig. 9.  $\mu$  factor. (a) Whole frequency range. (b) Zoom. (Blue) Nominal filter parameters, (red) increase of filter inductance  $L_f$  by 100%, (yellow) increase of filter resistance  $R_f$  by 100%, and (violet) increase of  $L_f$  and  $R_f$  by 50%.

increased by 20%. Effects of increased filter impedance on the dynamic performance of the control are shown in the section dedicated to the experimental results.

Due to the fact that converter filters are typically designed so as to optimize the tradeoff between power quality and size of the filter components, a simple and efficient way for increasing the output impedance of the converter without necessarily oversizing the hardware components is represented by the emulation of a virtual impedance through the control. This represents a well-established technique in microgrid applications and several possible solutions can be found in the literature [31]–[35]. Different techniques have been proposed for a variety of purposes, e.g., proper sharing of active and reactive power among units [32], current limitations [33], or harmonics suppression [34], [35]. Assuming that the filter impedance can be arbitrarily modified, for example, by means of a virtual impedance implementation technique, in the following section, an indication about optimal parameter choice for design purposes is provided investigating the effects of converter output filter parameter variations on the  $\mu$ -factor.

#### D. Tuning of the Virtual Impedance

The curves shown in Fig. 9 have been obtained varying the magnitude of the resistive and inductive components of the filter parameters shown in Table I. The blue line represents the  $\mu$ -factor for the case when the two synchronverters are simply connected in parallel and their respective filters have the nominal values, as shown in Table I, which coincides with the red curve of Figs. 5 and 7. Indicating with  $L_f$ , the sum of  $L_{f1}$  and  $L_{f2}$ , the red curve of Fig. 9 shows the  $\mu$ -factor for an increase of  $L_f$  by 100%. Although this parameter choice causes a reduction of the amplitude of the curve in almost the whole investigated

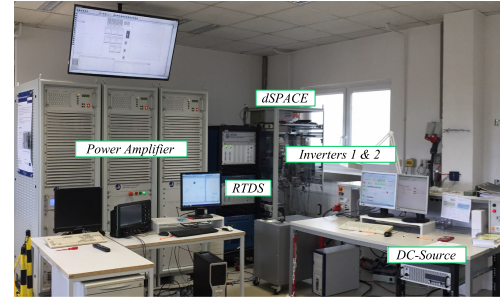
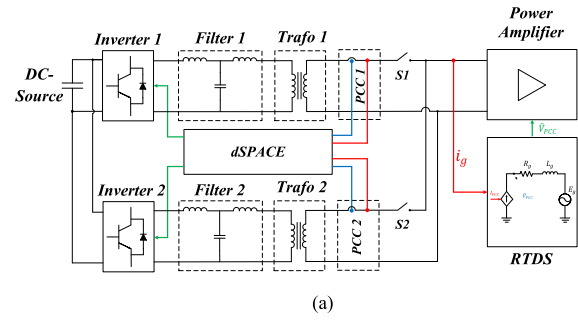


Fig. 10. Experimental set-up. (a) Scheme. (b) Picture.

TABLE II  
PARAMETERS EXPERIMENTAL SET-UP

Description	Symbol	Value
Inverter rated power	$S_{n1}=S_{n2}$	4 kVA
Line-to-line voltage	$V_{LL}$	380 V (rms)
Rated grid frequency	$f_g$	50 Hz
Inverter switching frequency	$f_{sw}$	10 kHz
Grid inductance	$L_g$	3 mH
Inverter-side filter inductance	$L_{f1I}=L_{f1II}$	5.2 mH
Grid-side filter inductance	$L_{f2I}=L_{f2II}$	0.5 mH
Transformer inductance	$L_{T1}=L_{T2}$	1.5 mH
Grid resistance	$R_g$	1 $\Omega$
Inverter-side filter resistance	$R_{f1I}=R_{f1II}$	2 $\Omega$
Grid-side filter resistance	$R_{f2I}=R_{f2II}$	1 $\Omega$
Capacitor damping resistance	$R_{cI}=R_{cII}$	5 $\Omega$
Filter capacitor	$C_{fI}=C_{fII}$	1.5 $\mu$ F
Virtual inertia	$J_1=J_2$	4e-4
Q-loop inverse integrator gain	$K_1=K_2$	800
P-Droop coefficient	$D_{p1}=D_{p2}$	0.8
Q-Droop coefficient	$D_{q1}=D_{q2}$	245

frequency range, a resonance peak around the fundamental frequency can be observed. On the contrary, the same magnitude of variation applied to the filter resistance  $R_f = R_{f1} + R_{f2}$  provides significant damping to the aforementioned resonance peak, whereas it produces an increase of the  $\mu$ -factor in the range  $\omega = [10; 100]$  rad/s, as indicated by the yellow line in the figure. Finally, the violet curve represents the case when the filter parameters  $L_{f1}$ ,  $L_{f2}$ ,  $R_{f1}$ , and  $R_{f2}$  are increased by 50%. Compared to the blue curve, this parameter choice ensures a reduction of the  $\mu$ -factor in the whole investigated frequency range and provides satisfactory damping around the fundamental frequency. As it will be shown in the next section, the increase of the filter impedance causes a detriment of the dynamic response of

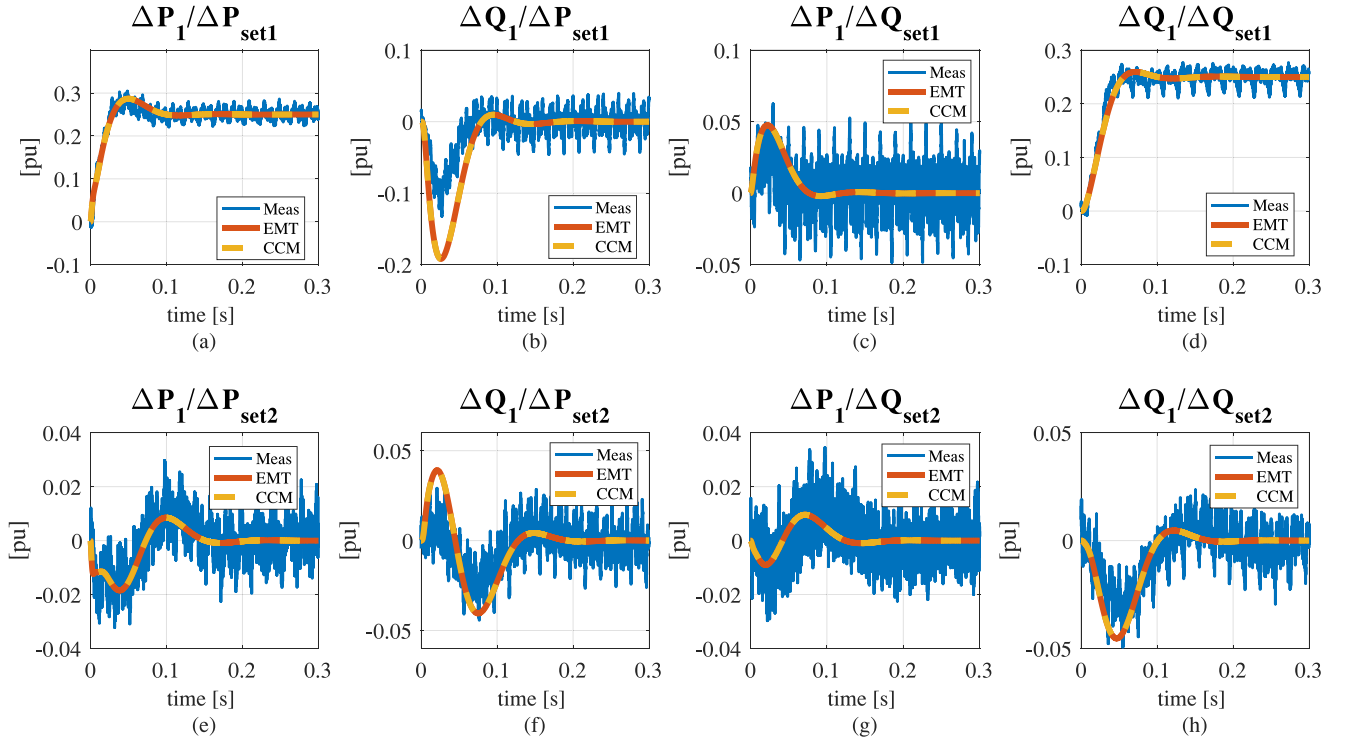


Fig. 11. Comparison experimental results (blue), EMT simulations (red), and linearized model (yellow). (a)  $\frac{\Delta P_1}{\Delta P_{set1}}$ . (b)  $\frac{\Delta Q_1}{\Delta P_{set1}}$ . (c)  $\frac{\Delta P_1}{\Delta Q_{set1}}$ . (d)  $\frac{\Delta Q_1}{\Delta Q_{set1}}$ . (e)  $\frac{\Delta P_1}{\Delta P_{set2}}$ . (f)  $\frac{\Delta Q_1}{\Delta P_{set2}}$ . (g)  $\frac{\Delta P_1}{\Delta Q_{set2}}$ . (h)  $\frac{\Delta Q_1}{\Delta Q_{set2}}$ .

the system. Fig. 9 shows how the increase of the filter impedance enhances significantly the robust stability of the synchronverter for parallel operation with other converters of the same type. According to these considerations, it is recommended to choose the parameters of the virtual impedance such as the corresponding total filter resistance  $R_f = [0.07; 0.15]$ p.u. and the total filter inductance  $L_f = [0.15; 0.25]$ p.u., in order to obtain a good compromise between robustness and dynamic performances.

#### IV. EXPERIMENTAL RESULTS

Experimental tests in a laboratory environment have been carried out so as to validate the results of the robust stability analysis performed in this paper. The laboratory set-up used for the tests is shown in Fig. 10(a) and (b), where a schematic drawing and a picture of the laboratory environment are shown, respectively. Two converters Danfoss Series FC-302, 4 kW rated power, operating with a switching frequency of 10 kHz, and equipped with output filters have been used. Each converter is additionally equipped with a transformer in order to provide galvanic isolation. The two converters are connected to a four-quadrant linear power amplifier PAS 15000 from Spitzenberger-Spies (single-phase rated power 15 kVA, total three-phase rated power 45 kVA). The control algorithms of the two converters are implemented in a dSPACE control Desk DS1202 MicroLabBox, whereas a grid model is simulated in real time by means of an real-time digital simulator simulator. The set-up enables reproducing the simulated voltages at the

output terminals of the power amplifier instantaneously for easy testing of the converters under different grid conditions.

The developed linearized model of the two synchronverters connected in parallel is validated against electromagnetic transient (EMT) time-domain simulations in MATLAB/Simulink/PLECS (modeling the converter as a voltage source) and against measurements performed with the described test set-up. Set-up parameters along with control parameters of the two synchronverters are reported in Table II. Results are shown in Fig. 11, where steps of 0.25 p.u. of active and reactive power setpoints of the synchronverter 1 indicated as  $P_{set1}$  and  $Q_{set1}$ , respectively, are performed and the dynamic behavior of active and reactive power output of the two converters  $P_1$ ,  $P_2$ ,  $Q_1$ , and  $Q_2$  are observed. The results show a good match between simulations and measurements for quantities within the same converter, namely:  $\frac{\Delta P_1}{\Delta P_{set1}}$ ,  $\frac{\Delta Q_1}{\Delta Q_{set1}}$  as well as for the cross-coupling effects between active and reactive power:  $\frac{\Delta Q_1}{\Delta P_{set1}}$ ,  $\frac{\Delta P_1}{\Delta Q_{set1}}$ . A satisfactory match between measurements and simulations can be also observed for the cross-coupling effects between the two converters:  $\frac{\Delta P_1}{\Delta P_{set2}}$ ,  $\frac{\Delta P_1}{\Delta Q_{set2}}$ ,  $\frac{\Delta Q_1}{\Delta P_{set2}}$ ,  $\frac{\Delta Q_1}{\Delta Q_{set2}}$ .

##### A. Virtual Impedance Implementation

In order to prove the results of the analysis performed in this paper, experimental tests are reported in the following, showing the effects of the virtual impedance implementation on the robust stability of the converter. The virtual impedance implementation proposed in [6] is adopted in this paper, whose principle is

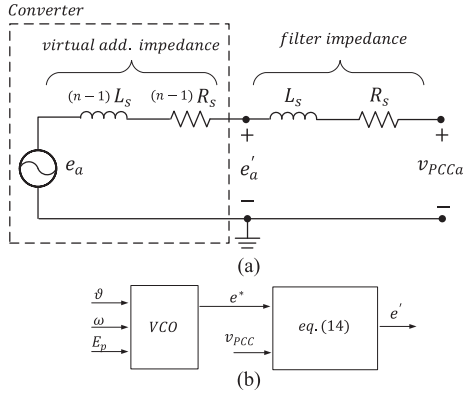


Fig. 12. (a) Equivalent electrical scheme. (b) Reference voltage generation block diagram.

briefly explained in the following. It represents an intuitive and efficient solution for virtually increasing the filter impedance of the converter by a factor  $n$ . According to Fig. 12(a), which shows the equivalent single-phase electrical circuit of the VSM, one could imagine splitting the total impedance into a physical part, represented by the filter impedance, and a virtual additional component to be implemented in the control, whose amplitude is  $(n - 1)$  times the magnitude of the filter impedance. The quantity  $e'_a$  from the equivalent scheme shown in Fig. 12(a) can be calculated as

$$e'_a = \frac{(n-1)v_{PCCa} + e_a}{n} \quad (14)$$

where  $e_a$  represents the phase  $a$  component of the synchronverter virtual back EMF voltage, calculated as follows [2]:

$$e^* = \begin{bmatrix} e_a \\ e_b \\ e_c \end{bmatrix} = E_p \omega \begin{bmatrix} \sin(\theta) \\ \sin(\theta - \frac{2}{3}\pi) \\ \sin(\theta - \frac{4}{3}\pi) \end{bmatrix}. \quad (15)$$

The effects of the additional impedance are reproduced by imposing the calculated  $e'_a$  for the generation of the converter pulses in Fig. 1. In Fig. 13, measurements results are compared to EMT simulations. According to the considerations about the optimal filter tuning presented in Section III-D, the approach previously described has been implemented in the synchronverter control in order to increase the filter impedance of twice its nominal value. Measurements are compared to simulations, showing the reference behavior of the converter when the filter impedance is physically increased by the corresponding factor. The step responses  $\frac{\Delta P_1}{\Delta P_{set1}}$ ,  $\frac{\Delta Q_1}{\Delta P_{set1}}$ ,  $\frac{\Delta P_2}{\Delta P_{set1}}$ , and  $\frac{\Delta Q_2}{\Delta P_{set1}}$  are depicted, showing a good match between measurements and the reference dynamic behavior.

In [10] and [20], the effects of the parameter  $K$  on the dynamic performance and on the stability of the synchronverter have been shown. Although higher values of  $K$  generally increase the damping of the reactive power loop, the robust stability analysis performed in [20] shows that an increase of this control parameter over a certain limit simply worsens, the dynamic performance without improving robustness. When the synchronverter is operating alone, the lowest value of  $K$  causing

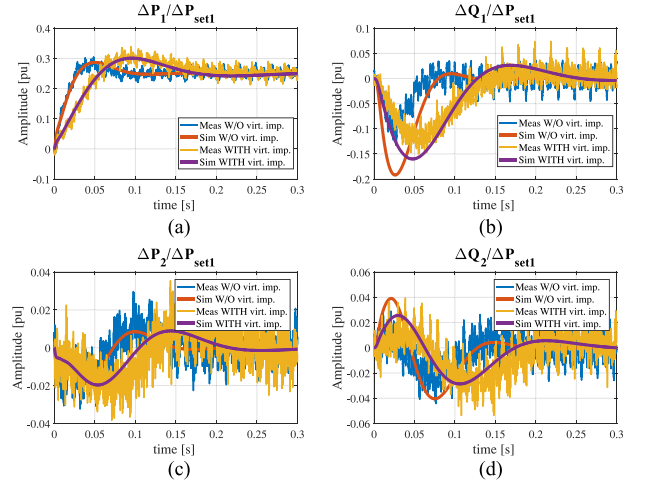


Fig. 13. Comparison experimental results versus EMT simulations. (Blue) Measurements without virtual impedance implementation, (red) simulations without virtual impedance implementation, (yellow) measurements with virtual impedance implementation, and (violet) simulations with virtual impedance implementation. (a)  $\frac{\Delta P_1}{\Delta P_{set1}}$ . (b)  $\frac{\Delta Q_1}{\Delta P_{set1}}$ . (c)  $\frac{\Delta P_2}{\Delta P_{set1}}$ . (d)  $\frac{\Delta Q_2}{\Delta P_{set1}}$ .

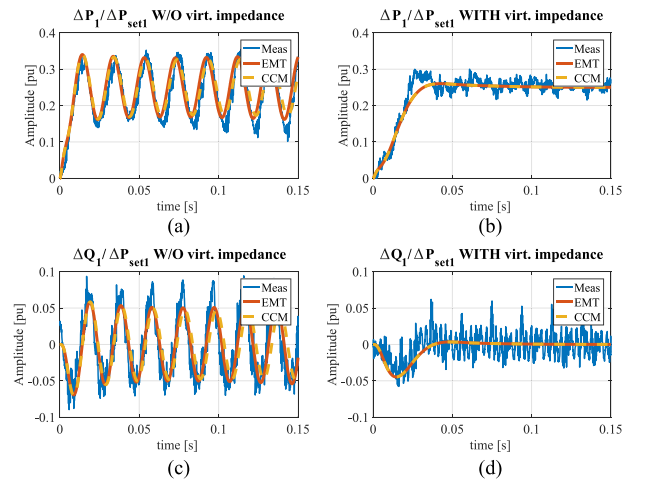


Fig. 14. Comparison experimental results (blue), EMT simulations (red), and linearized model (yellow). (a)  $\frac{\Delta P_1}{\Delta P_{set1}}$  without virtual impedance. (b)  $\frac{\Delta P_1}{\Delta P_{set1}}$  with virtual impedance. (c)  $\frac{\Delta Q_1}{\Delta P_{set1}}$  without virtual impedance. (d)  $\frac{\Delta Q_1}{\Delta P_{set1}}$  with virtual impedance.

the instability of the converter is  $K_{lim} \approx 50$ . However, when the second synchronverter is connected in parallel, the critical  $K$  causing instability increases to  $K_{lim} \approx 60$ . Fig. 14(a) and (c) shows the step responses of  $\frac{\Delta P_1}{\Delta P_{set1}}$  and  $\frac{\Delta Q_1}{\Delta P_{set1}}$  of the synchronverter 1 when operating in parallel to synchronverter 2, both having  $K_1 = K_2 = 55$  and when the virtual impedance is not implemented in the control. Fig. 14(c) and (d) shows the behavior of the converter when operating in parallel to the second converter, both having  $K_1 = K_2 = 55$  and when the virtual impedance is instead implemented in the control of synchronverter 1. This demonstrates the benefit of the virtual impedance implementation on the converter stability, since the new critical  $K$  causing the instability of both synchronverters for parallel operation is reduced to  $K_{lim} \approx 20$  when it is implemented in both controls.



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