

Hybrid and Reconfigurable IPT Systems With High-Misalignment Tolerance for Constant-Current and Constant-Voltage Battery Charging

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Abstract—Inductive power transfer (IPT) for battery charging applications has significant advantages over the traditional plug-in system. Since misalignment between the primary and secondary windings is inevitable, it is of significance to improve the misalignment tolerance of IPT systems with constant-current (CC) and constant-voltage (CV) outputs for battery charging. In this paper, the load-independent output characteristic of the hybrid topology and the function switching between CC and CV of the reconfigurable topology are analyzed. Besides, a hybrid and reconfigurable IPT system with 3-D misalignment tolerance for CC and CV outputs is proposed, simplifying or even canceling control schemes. Moreover, a novel parametric design method is given for the IPT system, which can suppress the fluctuation of the output voltage/current within a certain range of misalignment. In order to validate the performance of the proposed topology, a 1-kW prototype is built, and the corresponding experiments are carried out. In the CC/CV mode, the system can operate with the longitudinal misalignment to 50% when the load varies from 36 to 480 Ω , and the fluctuation of the output current/voltage is within 5%. Similarly, the misalignment in Y- and Z-axis is 12.5% and 33.3%, respectively.

Index Terms—Battery charging, constant current (CC), constant voltage (CV), high-misalignment tolerance, hybrid and reconfigurable inductive power transfer (IPT) systems.

I. INTRODUCTION

INDUCTIVE power transfer (IPT) technology [1]–[3], based on magnetic coupling, has been adopted in many applications such as consumer electronics [4], biomedical implant

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devices [5], underwater power supplies [6], electric vehicles (EVs) [7]–[9], train power supplies [10], etc. Compared to conventional plug-in systems, IPT systems are more convenient, flexible, safer, and weatherproof. Some companies, such as ZET, Mgeek, Evatran, WiTricity, etc., have developed many IPT-based products for battery charging. Due to the exploding of EVs in people's daily life, the EV wireless charging may be one of the best solutions to free people from the tiresome plug-in action. However, more research work is still required to reduce control scheme complexity and to increase misalignment tolerance.

Lithium-ion batteries are widely adopted in smartphones and EVs. Typically, the profile of batteries is with the constant-current (CC) mode at first and then the constant-voltage (CV) mode, which aims at prolonging the battery lifetime and recycle time. During the whole charging process, the battery equivalent resistance R_B ($R_B = v_B/i_B$, where v_B and i_B are the charging voltage and charging current, respectively) varies from several ohms up to hundreds of ohms. There are mainly two categories of control schemes for IPT systems operating with CC or CV outputs against variable loads. 1) The control schemes for a high-frequency inverter (HFI): The IPT systems with phase shift control schemes for the HFI can adjust the charging current or charging voltage against various loads [11]. Besides, the systems with load-independent currents/voltages against variable loads can operate at split frequencies [12], [13], which may cause a stability issue due to the occurrence of frequency bifurcation phenomenon. 2) The control schemes for dc–dc converters placed on the primary side or the secondary side: The duty cycle of the dc–dc converters can be modulated to achieve desired output currents or output voltages [14], [15]. The IPT systems with aforementioned control schemes can regulate the output currents or voltages with load and coupling variations, but they are at the expense of a wide range of input modulation index and/or complicated control circuitry [16].

Some scholars pay attention to the compensation topologies for CC or CV output replacing the complex control methods. A series of individual passive resonant networks and some higher order compensations for IPT systems CC or CV output are systematically analyzed in [16] and [17]. Besides, some new compensation topologies such as inductor–capacitor–series and series–capacitor–inductor–capacitor topologies for CC and CV

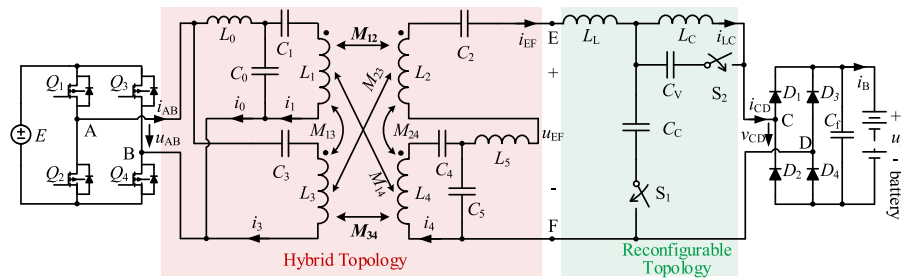


Fig. 1. Hybrid and reconfigurable topology.

outputs are introduced in [18] and [19]. In addition, many efforts are spent in [20]–[22] to combine topologies with CC and CV outputs for battery charging. Four basic topologies including series–series (SS), series–parallel (SP), parallel–parallel, and parallel–series (PS) compensations are discussed in detail, and then, two hybrid topologies are proposed for battery charging [20]. A dual topology switching between SS and SP topologies is given in [21] for CC and CV mode charging. Moreover, a hybrid topology switching between SS and series–inductor–capacitor–capacitor (S-LCC) topologies is proposed in work [22], which can charge a fleet of electric bicycles (EBs) with a single inverter. However, all the compensation topologies mentioned above required precise placement of magnetic couplers. Or else, the misalignment leads to system degraded dramatically.

It is essential to improve the misalignment tolerance of IPT systems because the variation of the relative position between the primary and secondary windings is inevitable. The pad misalignments will cause some critical problems such as low power transfer, high power losses, and instability of the systems. Thus, considerable efforts have been made, revolving around coil designs and compensation topologies, to increase the misalignment tolerance of IPT systems. Some authors of the literature [23]–[25] try to optimize the structures of the magnetic couplers to obtain a relatively uniform magnetic field distribution. Additionally, a series–parallel–series topology, which combines the characteristics of SS and PS configurations, is proposed in [26]. The new compensation topology can transfer rated power with high misalignment up to 25% over a 1-D lateral misalignment. In [27], the optimization for compensating capacitor in four-coil coupled wireless charging system can extend the tolerant lateral misalignment up to 44.3%. Multiphase couplers are utilized to increase the vertical and horizontal misalignment tolerances for relatively constant power [28]. In [29], a hybrid bidirectional wireless EV charging system is proposed with 17% vertical misalignment and 41% longitudinal misalignment, maintaining 95% of its nominal power level. Moreover, a dual-side detuned SS compensation topology is used in the IPT system, of which the power transfer varies from 80% to 100% of its nominal power level [30]. All the efforts above do not involve the CC–CV charging research with high misalignment.

A hybrid and reconfigurable IPT system is proposed in this paper to overcome the drawback of misalignment and to provide the characteristics of CC or CV outputs. The hybrid and reconfigurable topology consists of a hybrid topology and a reconfigurable topology, as shown in Fig. 1. The hybrid topology

is formed by an inductor–capacitor–capacitor–series (LCC–S) topology and an S-LCC topology, where the LCC represents the inductor–capacitor–capacitor compensation network, and S stands for the capacitor in series with the primary or secondary coil. The two topologies connected in parallel at the primary side aim at decreasing the number of HFIs. The two topologies can also be driven by two HFIs, separately. Since both topologies hold the characteristic of load-independent output voltage, they are chosen to connect in series at the secondary side instead of in parallel. The reconfigurable topology consists of an inductor–capacitor–inductor (LCL) topology, two ac switches (ACSs), and an additional capacitor. An ACS in series with the capacitor of LCL and the extra capacitor in series with another ACS together connect with the last inductor of LCL in parallel.

The contributions of the paper are listed as follows.

- 1) This paper systematically analyzes the load-independent voltage output, misalignment tolerance characteristics of a hybrid topology formed by LCC–S and S-LCC topologies, and the function switching between CC and CV of the reconfigurable topology. Then, an innovative hybrid and reconfigurable topology is proposed for CC and CV outputs with the ability of suppressing 3-D misalignments, simplifying the control schemes.
- 2) The misalignment tolerance analysis of double-D quadrature pad (DDQP) is presented in this paper. Then, a novel parametric design method for the hybrid and reconfigurable topology is proposed, so that the fluctuation of the output voltage can be limited within a certain range of misalignment tolerances.

The proposed method has a high misalignment in the X-axis but cannot have a high misalignment in the Y-axis, so the applications are limited. As for EV or buses, the y-misalignment (front-to-rear) may be readily adjusted because the direction of the EV is parallel to the Y-axis. In this case, a sensor that informs the current y-misalignment to the user is additionally required or can easily be neglected with the help of the user. Additionally, the method can be applied for trams for the reason that the y-misalignment hardly exists due to the railway track.

The rest of this paper is organized as follows. The analysis of the hybrid and reconfigurable topology is presented, and the basic characteristics are described in Section II. The misalignment tolerance of two pads and the parametric design method are analyzed in Section III. The hybrid and reconfigurable topology for battery charging is implemented for validation in Section IV. Finally, the conclusion is drawn in Section V.

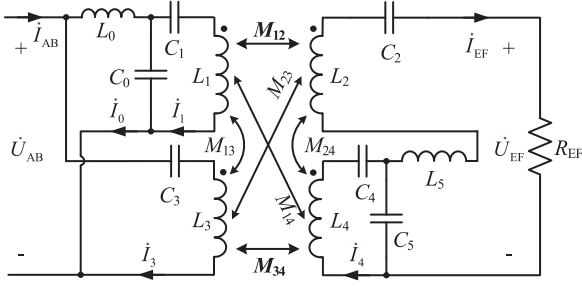


Fig. 2. Hybrid topology driven by a voltage source.

II. THEORETICAL ANALYSIS

The proposed hybrid and reconfigurable topology is shown in Fig. 1. The HFI has a full bridge consisting of four power MOSFETs (Q_1 – Q_4). L_0 , C_0 , and C_1 (L_5 , C_5 , and C_4) constitute the LCC compensation network, while C_3 (C_2) is part of the S compensation network, together forming LCC - S (S - LCC) topology. L_L , C_C , and L_C constitute the LCL topology, combining C_V and ACSs S_1 and S_2 , together forming the reconfigurable topology. The mutual inductances between the four coils are M_{12} , M_{13} , M_{14} , M_{23} , M_{24} , and M_{34} . The rectifier comprises four diodes (D_1 – D_4) and a filter capacitor C_f . Besides, the resonant tank is assumed to be lossless (equivalent series resistance is zero), and semiconductor switches are ideal. The fundamental harmonic approximation method is used to analyze the system.

A. Analysis of the Hybrid Topology

The hybrid and reconfigurable topology is separated intentionally for analyzing, and the hybrid topology is illustrated in Fig. 2. The circuit is driven by a sinusoidal voltage source with angular frequency ω . The topology can be represented by the following equations:

$$\begin{cases} j\omega L_1 + 1/(j\omega C_1) = j\omega L_0 = -1/(j\omega C_0) \\ j\omega L_2 = -1/(j\omega C_2) \\ j\omega L_4 + 1/(j\omega C_4) = j\omega L_5 = -1/(j\omega C_5) \\ j\omega L_3 = -1/(j\omega C_3). \end{cases} \quad (1)$$

According to Kirchhoff's laws, the hybrid circuit can be expressed by

$$\begin{bmatrix} \dot{U}_{AB} \\ 0 \\ 0 \\ \dot{U}_{AB} \\ 0 \end{bmatrix} = \begin{bmatrix} Z_{00} & Z_{01} & Z_{02} & Z_{03} & Z_{04} \\ Z_{10} & Z_{11} & Z_{12} & Z_{13} & Z_{14} \\ Z_{20} & Z_{21} & Z_{22} & Z_{23} & Z_{24} \\ Z_{30} & Z_{31} & Z_{32} & Z_{33} & Z_{34} \\ Z_{40} & Z_{41} & Z_{42} & Z_{43} & Z_{44} \end{bmatrix} \begin{bmatrix} \dot{I}_0 \\ \dot{I}_1 \\ \dot{I}_{EF} \\ \dot{I}_3 \\ \dot{I}_4 \end{bmatrix} \quad (2)$$

where

$$\begin{aligned} Z_{00} &= j\omega L_0 + 1/(j\omega C_0), & Z_{01} &= Z_{10} = -1/(j\omega C_0) \\ Z_{02} &= Z_{20} = 0, & Z_{03} &= Z_{30} = 0, & Z_{04} &= Z_{40} = 0 \\ Z_{11} &= 1/(j\omega C_0) + 1/(j\omega C_1) + j\omega L_1 \end{aligned}$$

$$Z_{12} = Z_{21} = -j\omega M_{12}$$

$$Z_{13} = Z_{31} = j\omega M_{13}, \quad Z_{14} = Z_{41} = -j\omega M_{14}$$

$$Z_{22} = j\omega L_2 + 1/(j\omega C_2) + j\omega L_5 + 1/(j\omega C_5) + R_{EF}$$

$$Z_{23} = Z_{32} = -j\omega M_{23}, \quad Z_{24} = Z_{42} = j\omega M_{24} - 1/(j\omega C_5)$$

$$Z_{33} = j\omega L_3 + 1/(j\omega C_3), \quad Z_{34} = Z_{43} = -j\omega M_{34}$$

$$Z_{44} = j\omega L_4 + 1/(j\omega C_4) + 1/(j\omega C_5). \quad (3)$$

By choosing a proper magnetic coupler, the cross couplings (M_{13} , M_{14} , M_{23} , and M_{24}) have much less effects on the output of the system than that of the main couplings (M_{12} and M_{34}) with designed coil structures, which will be discussed in Section III. Therefore, the effects of cross couplings (M_{13} , M_{14} , M_{23} , and M_{24}) are neglected. By solving (2), we can obtain

$$\begin{cases} \dot{I}_0 = \frac{(M_{12}M_{34} + L_0L_5)M_{12}\dot{U}_{AB}}{M_{34}R_{EF}L_0^2} \\ \dot{I}_{EF} = \frac{(M_{12}M_{34} + L_0L_5)\dot{U}_{AB}}{M_{34}R_{EF}L_0} \\ \dot{I}_3 = \frac{(M_{12}M_{34} + L_0L_5)\omega L_5\dot{U}_{AB}}{\omega R_{EF}L_0M_{34}^2}. \end{cases} \quad (4)$$

Then, the voltage gain G_{VVh} of the hybrid topology is given as

$$G_{VVh} = \frac{\dot{I}_{EF}R_{EF}}{\dot{U}_{AB}} = \frac{M_{12}}{L_0} + \frac{L_5}{M_{34}}. \quad (5)$$

It shows that the output voltage of the hybrid topology is load independent by neglecting the cross couplings, which take the advantages of the characteristics of LCC - S and S - LCC configurations. The role of the LCC - S topology is to increase the output voltage with the decrease of mutual inductance. On the contrary, the role of the S - LCC topology is to decrease the output voltage with the decrease of mutual inductance. When the pad misalignment takes place, the main coupling M_{12} and M_{34} will decrease, which causes the decrease of M_{12}/L_0 and the increase of L_5/M_{34} . If the parameters are chosen properly, the voltage gain G_{VVh} can maintain relatively constant in a certain range by canceling out the impact of the increase or decrease of M_{12} and M_{34} . Equation (5) is derived under the assumption of no parasitic resistance in the system. It is very intuitive to show the basic output voltage characteristic of the hybrid topology. As we know, the S compensation on the primary side cannot operate without load (mutual inductance is very small, or even close to zero), or the system will break down because of over-current on the primary side. Thus, the hybrid topology cannot work without load.

The input current \dot{I}_{AB} is given by

$$\dot{I}_{AB} = \dot{I}_0 + \dot{I}_3 = \frac{\dot{U}_{AB}(M_{12}M_{34} + L_0L_5)^2}{R_{EF}L_0^2M_{34}^2}. \quad (6)$$

The input equivalent impedance Z_{ABh} can be derived by

$$Z_{ABh} = \frac{\dot{U}_{AB}}{\dot{I}_{AB}} = \frac{R_{EF}L_0^2M_{34}^2}{(M_{12}M_{34} + L_0L_5)^2} \quad (7)$$

which is pure resistant indicating that current i_{AB} and voltage v_{AB} can realize zero phase angle (ZPA).

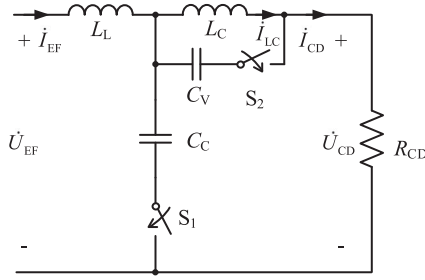


Fig. 3. Reconfigurable topology driven by a voltage source.

B. Analysis of the Reconfigurable Topology

The reconfigurable topology is shown in Fig. 3. When the ACS S_1 is ON and S_2 is OFF, the circuit operating in the CC mode can be described by

$$\begin{bmatrix} \dot{U}_{EF} \\ 0 \end{bmatrix} = \begin{bmatrix} j\omega L_L + \frac{1}{j\omega C_C} & \frac{-1}{j\omega C_C} \\ \frac{-1}{j\omega C_C} & \frac{1}{j\omega C_C} + j\omega L_C + R_{CD} \end{bmatrix} \begin{bmatrix} \dot{I}_{EF} \\ \dot{I}_{CD} \end{bmatrix}. \quad (8)$$

The relationships between inductors L_L and L_C and capacitor C_C are given as follows:

$$\begin{cases} j\omega L_L + \frac{1}{j\omega C_C} = 0 \\ j\omega L_C + \frac{1}{j\omega C_C} = 0. \end{cases} \quad (9)$$

Then, by solving (8), the input and output currents \dot{I}_{EF} and \dot{I}_{CD} are shown as

$$\begin{cases} \dot{I}_{EF} = \dot{U}_{EF} R_{CD} \omega^2 C_C^2 \\ \dot{I}_{CD} = -j\omega C_C \dot{U}_{EF}. \end{cases} \quad (10)$$

Therefore, the current gain $G_{VI,CC}$ of the reconfigurable topology in the CC mode is

$$G_{VI,CC} = \frac{\dot{I}_{CD}}{\dot{I}_{EF}} = -j\omega C_C. \quad (11)$$

The input equivalent impedance $Z_{EFr,CC}$ can be derived by

$$Z_{EFr,CC} = \frac{\dot{U}_{EF}}{\dot{I}_{EF}} = \frac{1}{R_{CD} \omega^2 C_C^2}. \quad (12)$$

When the ACS S_1 is OFF and S_2 is ON, the circuit operates in the CV mode. The relationship of inductors L_L and L_C and capacitor C_V is given by

$$j\omega L_L + \frac{1}{j\omega C_V + 1/(j\omega L_C)} = 0 \quad (13)$$

which indicates that inductors L_L and L_C and capacitor C_V form the series resonance tank. The voltage gain $G_{VVr,CV}$ of the reconfigurable topology in the CV mode is

$$G_{VVr,CV} = 1. \quad (14)$$

The input equivalent impedance $Z_{EFr,CV}$ can be expressed as

$$Z_{EFr,CV} = R_{CD}. \quad (15)$$

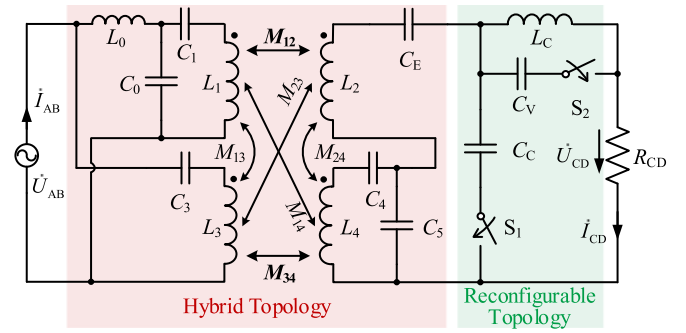


Fig. 4. Combination of the hybrid topology and the reconfigurable topology.

C. Combination of the Hybrid Topology and the Reconfigurable Topology

The hybrid and reconfigurable system is formed by the combination of the hybrid topology and the reconfigurable topology as shown in Fig. 4. Comparing Fig. 1 with Fig. 4, the equivalent impedance of a component noted as C_E (or L_E) is used to replace the equivalent impedance of inductors L_L and L_5 and capacitor C_2 . The component C_E (or L_E) can be an inductor L_E or a capacitor C_E , i.e.,

$$j\omega L_L + j\omega L_5 + \frac{1}{j\omega C_2} = j\omega L_E \quad (16)$$

or

$$j\omega L_L + j\omega L_5 + \frac{1}{j\omega C_2} = \frac{1}{j\omega C_E}. \quad (17)$$

The component C_E (or L_E) is treated as a capacitor C_E for simplifying analysis in this section.

CC mode: According to (5) and (11), the current gain G_{VI} of the hybrid and reconfigurable topology is expressed by

$$G_{VI} = G_{VVh} \cdot G_{VIr,CC} = -j\omega C_C \left(\frac{M_{12}}{L_0} + \frac{L_5}{M_{34}} \right). \quad (18)$$

The corresponding input impedance $Z_{AB,CC}$ is

$$Z_{AB,CC} = \frac{L_0^2 M_{34}^2}{R_{CD} \omega^2 C_C^2 (M_{12} M_{34} + L_0 L_5)^2}. \quad (19)$$

CV mode: According to (5) and (14), the voltage gain G_{VV} of the hybrid and reconfigurable topology is obtained

$$G_{VV} = G_{VVh} \cdot G_{VVr,CV} = \frac{M_{12}}{L_0} + \frac{L_5}{M_{34}}. \quad (20)$$

The corresponding input impedance $Z_{AB,CV}$ is

$$Z_{AB,CV} = \frac{R_{CD} L_0^2 M_{34}^2}{(M_{12} M_{34} + L_0 L_5)^2}. \quad (21)$$

It indicates that the input impedance of the hybrid and reconfigurable system in CC and CV modes are both pure resistance, which shows that ZPA can be achieved.

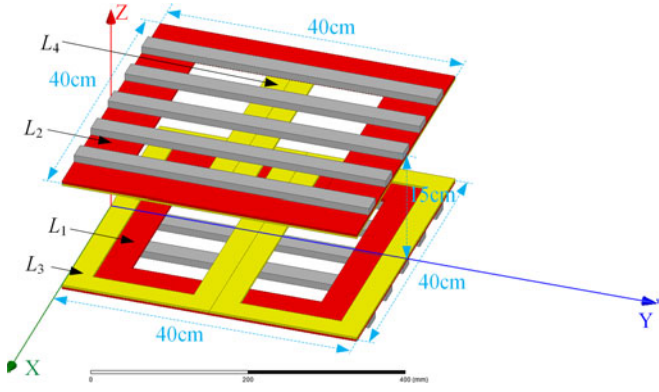


Fig. 5. Structure of DDQP.

III. PARAMETRIC DESIGN OF THE HYBRID AND CONFIGURABLE SYSTEM

A. Misalignment Tolerance Analysis of DDQP

The magnetic coupler, shown in Fig. 5, is DDQP [31]. The detailed design method of the magnetic coupler is provided in [32]. The misalignment between the pads can be longitudinal (X), lateral (Y), and vertical (Z), which cannot be practically avoided in the charging system. The mutual inductances of a prototype hybrid and reconfigurable IPT system are measured by moving secondary pads longitudinally, laterally, and vertically, separately. The nominal operating point (NOP) of the secondary pads are centered 150 mm directly above the primary pads, where the parameters are designed for high-misalignment tolerance.

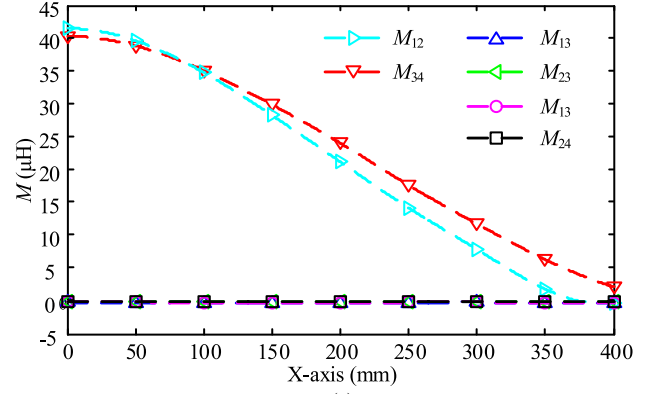
The variation in mutual inductances due to the misalignment of secondary pads in X -, Y -, and Z -axis is drawn in Fig. 6. The results indicate that main couplings (M_{12} and M_{34}) have a significant change in X - and Z -axis, while the cross couplings (M_{13} , M_{14} , M_{23} , and M_{24}) are so small that they can be neglected [32]. To make those cross couplings small like that, the quadrature pad and the double-D pad on the primary or secondary side must be placed in the center of the magnetic coupler in different layers, which means that they are symmetrical. Besides, the DDQPs on the primary side and the secondary side must be symmetrical too. The cross couplings M_{23} and M_{14} are significantly affected by misalignments in the Y -axis, so the system cannot have high misalignment in the Y -axis with the proposed method.

B. Parametric Design of the Hybrid and Reconfigurable System

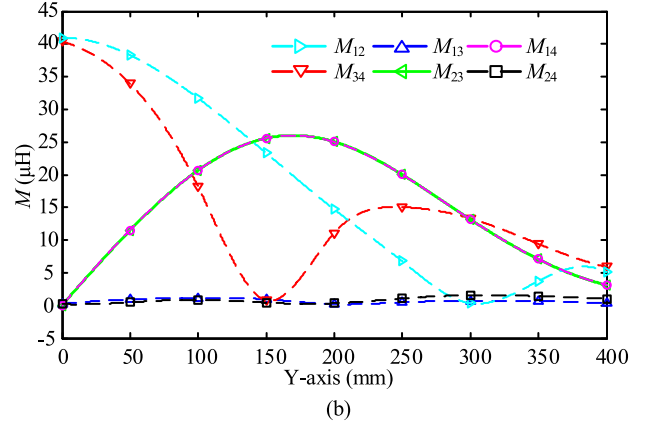
Before the parametric design, the values of some parameters should be given first, such as the input dc voltage E , the output dc voltage/current U_B/I_B , the mutual inductance $M_{34,nop}$ of the NOP for pads, and the output dc voltage deviation Δ .

The relationship between the input dc voltage E and the output ac voltage U_{AB} of the full-bridge HFI can be expressed by [33]

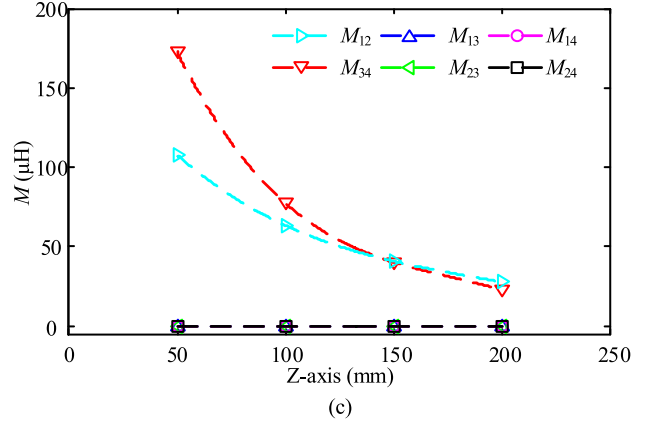
$$U_{AB} = \frac{2\sqrt{2}E}{\pi}. \quad (22)$$



(a)



(b)



(c)

Fig. 6. Variation of mutual inductances due to misalignment of secondary pads in (a) X -, (b) Y -, and (c) Z -axis.

Similarly, the relationship between the input ac voltage/current U_{CD}/I_{CD} and the output dc voltage/current U_B/I_B of the full-bridge diode rectifier can be expressed by [33]

$$\begin{cases} U_B = \frac{\pi\sqrt{2}}{4}U_{CD} \\ I_B = \frac{2\sqrt{2}}{\pi}I_{CD}. \end{cases} \quad (23)$$

As a result, the nominal voltage gain G_{VVN} and the nominal current gain G_{VIN} can be given as

$$\begin{cases} G_{VVN} = \frac{U_{CD}}{U_{AB}} = \frac{U_B}{E} \\ G_{VIN} = \frac{I_{CD}}{U_{AB}} = \frac{\pi^2 I_B}{8E}. \end{cases} \quad (24)$$

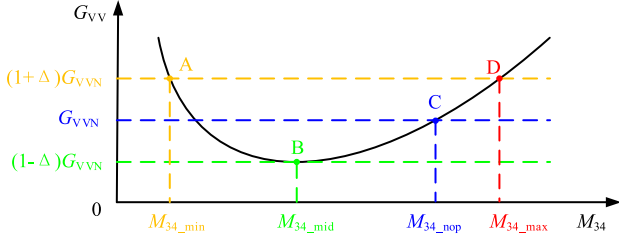


Fig. 7. Relationship between G_{VV} and M_{34} .

The variations of the main couplings M_{12} and M_{34} can be expressed by a function $M_{12} = f(M_{34})$ with the help of MATLAB in a certain range of misalignment. According to Fig. 6(a), the relationship of M_{12} and M_{34} can be approximately a linear model, i.e.,

$$M_{12} = aM_{34} + b. \quad (25)$$

It is not necessary to keep the variations of M_{12} and M_{34} similar. We are concerned about the relationship between M_{12} and M_{34} . In other words, what we need to do is to keep the output voltage $((M_{12}/L_0 + L_5/M_{34})U_{AB})$ varying as small as possible with a certain range of misalignment.

Substituting (25) into (20), we can obtain

$$G_{VV} = \frac{aM_{34} + b}{L_0} + \frac{L_5}{M_{34}}. \quad (26)$$

The relationship between G_{VV} and M_{34} is drawn in Fig. 7, where Δ is the deviation of the output voltage. M_{34_nop} and M_{34_max} are the value at the normal operating point and the maximum value of the misalignment, respectively, and G_{VVN} is the corresponding voltage gain. When the misalignment occurs, M_{34} decreases to M_{34_mid} and the voltage gain decreases to the minimum value $(1 - \Delta)G_{VVN}$. Then, M_{34} continues declining until to the minimum value M_{34_min} , and the voltage gain reaches the maximum value $(1 + \Delta)G_{VVN}$.

The value of M_{34_mid} can be derived by setting the derivative of G_{VV} to 0, i.e.,

$$\frac{dG_{VV}}{dM_{34}} = 0. \quad (27)$$

By solving (27), we can obtain

$$M_{34_mid} = \sqrt{\frac{L_0 L_5}{a}}. \quad (28)$$

Substituting point B (M_{34_mid} , $(1 - \Delta)G_{VVN}$) and C (M_{34_nop} , G_{VVN}) into (26), we have

$$\begin{cases} G_{VV} |_{M_{34_mid}} = \frac{aM_{34_mid} + b}{L_0} + \frac{L_5}{M_{34_mid}} = (1 - \Delta)G_{VVN} \\ G_{VV} |_{M_{34_nop}} = \frac{aM_{34_nop} + b}{L_0} + \frac{L_5}{M_{34_nop}} = G_{VVN}. \end{cases} \quad (29)$$

The values of L_0 and L_5 can be obtained by solving (29)

$$\begin{cases} L_0 = \frac{A(b+aB)}{G_{VVN}(A+M_{34_mid}\Delta)} \\ L_5 = \frac{M_{34_nop}M_{34_mid}G_{VVN}(aA-b\Delta-M_{34_nop}a\Delta)}{A(b+aB)} \end{cases} \quad (30)$$

where $A = M_{34_nop} - M_{34_mid}$ and $B = M_{34_nop} + M_{34_mid}$.

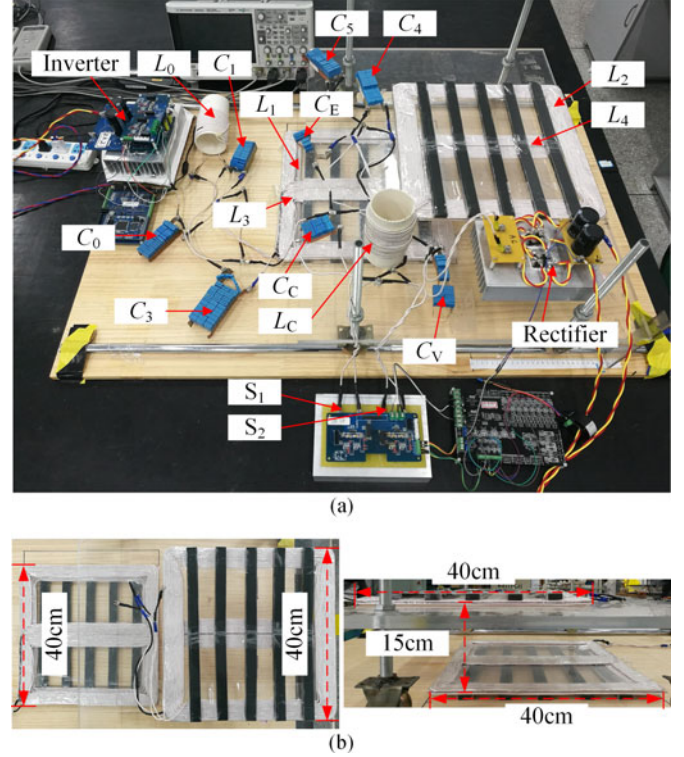


Fig. 8. (a) Setup of the hybrid and reconfigurable IPT system. (b) Structure of coil pads.

According to (18) and (25), we can obtain the value of C_C , i.e.,

$$C_C = G_{VIN} / \left[\omega \left(\frac{aM_{34_nop} + b}{L_0} + \frac{L_5}{M_{34_nop}} \right) \right]. \quad (31)$$

Then, the values of C_0 , C_1 , C_2 , C_3 , C_4 , C_5 , L_C , L_L , C_V , and C_E can be calculated according to (1), (9), (13), and (16)/(17).

IV. EXPERIMENTAL RESULTS

In order to demonstrate the applicability of the proposed topology and the above analysis, an experimental prototype of the hybrid and reconfigurable IPT system was designed and built, illustrated in Fig. 8. The switching devices are $Q_1-Q_4 = C2M0080120D$ and S_1 and $S_2 = APT56F50L$. The system parameters are given in Table I. To simplify the test, an electronic load is used to replace the equivalent load of the batteries in different charging processes.

A. Performance of the IPT System at the NOP

1) *Results of Charging an Electronic Load:* In the CC mode, the electronic load is changed from 36 to 48 Ω , while in the CV mode, it varies from 48 to 480 Ω . Fig. 9 shows the steady-state waveforms of the output voltage u_{AB} , the output current i_{AB} of the HFI, and the output voltage u_B and output current i_B at $R_B = 36 \Omega$ in the CC mode and at $R_B = 50 \Omega$ in the CV mode, which indicates that ZPA is achieved, and illustrates that reactive power is eliminated. The transient waveforms of the charging mode from CC to CV are given in Fig. 10. There are

TABLE I
SYSTEM SPECIFICATION AND PARAMETER VALUES

Parameter	Value	Parameter	Value
V_B	220 V	I_B	4.6 A
f	85 kHz	Δ	5%
a	1.188	b	-5.88×10^{-6}
E	110 V	M_{34_nop}	40.1 μ H
L_0	32.2 μ H	C_0	108.4 nF
C_1	19.1 nF	L_1	217.1 μ H
L_2	217.7 μ H	C_E	31.6 nF
C_3	15.1 nF	L_3	233.6 μ H
L_4	233.7 μ H	C_4	17.5 nF
C_5	110.8 nF	C_C	48.7 nF
L_C	71.4 μ H	C_V	98.1 nF

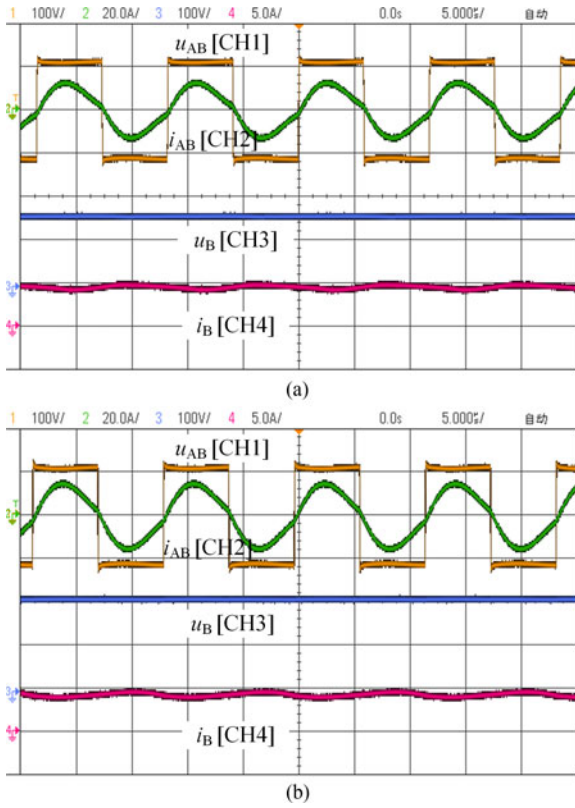


Fig. 9. Experimental waveforms of u_{AB} , i_{AB} , u_B , i_B . (a) $R_B = 36 \Omega$ in the CC mode. (b) $R_B = 50 \Omega$ in the CV mode at the NOP.

some oscillations during the transient switching mode, and it takes around 2 ms to return to the steady state. The output u_B and i_B nearly remains the same values after mode switching, and the output current i_{AB} of the HFI also maintains relatively invariable. The whole charging profile with the charging current i_B and charging voltage u_B is illustrated in Fig. 11. In the CC mode, the fluctuation of the charging current i_B is less than 5%. In the CV mode, the fluctuation of the charging voltage u_{AB} is within 5%.

A digital power analyzer, PW6001 of HIOKI, is used to measure the input and output dc power, and the efficiency of the IPT system can be obtained. The efficiencies of the hybrid and reconfigurable IPT system are measured as shown in Fig. 12. The

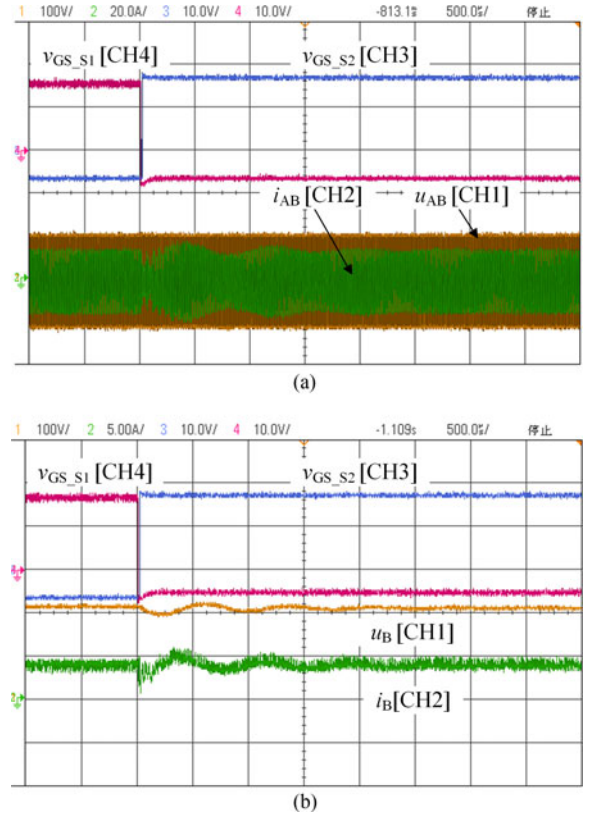


Fig. 10. Transient waveforms from the CC mode to the CV mode of the IPT system. (a) Waveforms of gate drive signals $v_{GS,S1}$ and $v_{GS,S2}$ of ACSs S_1 and S_2 , waveforms of i_{AB} and u_{AB} . (b) Waveforms of i_B and u_B .

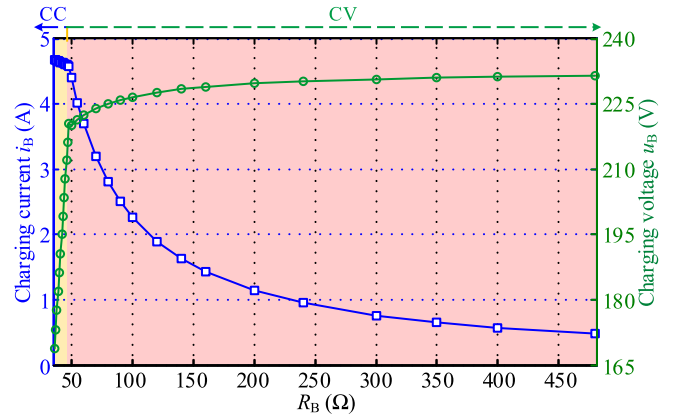


Fig. 11. Measured charging profiles of the hybrid and reconfigurable IPT system versus R_B .

efficiency of the IPT system decreases from 92.4% to 91.7% in the CC mode, while in the CV mode, it first climbs from 92.7% to 93.9% and then decreases to 85.7% at the end of the CV mode.

2) *Results of Charging a Lead Accumulator:* To prove that the simplification of using an electric load is feasible, a lead acid battery is charged in the experiment. Since there is a lead accumulator ($4 \times 6\text{-DZM-12}$) with 48-V/12-Ah rating in our laboratory, we regulate the dc input voltage from 110 to 24 V ($110\text{--}220 \text{ V}/4.6 \text{ A} \rightarrow 24\text{--}48 \text{ V}/1 \text{ A}$) to charge the battery. The initial charging voltage of the battery is 44 V, and the charging voltage gradually increases with the charge current of 1 A in the

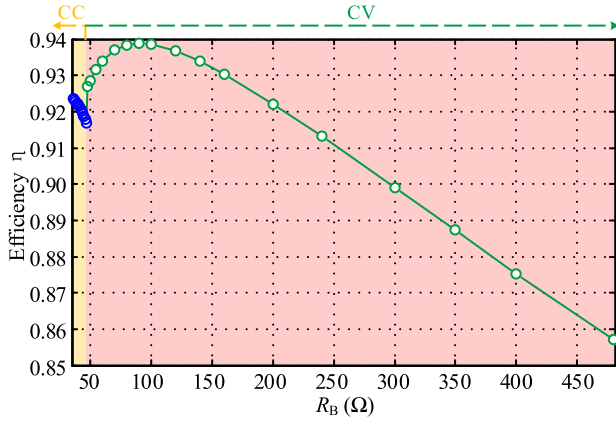


Fig. 12. Measured efficiencies of the hybrid and reconfigurable IPT system in CC and CV modes.

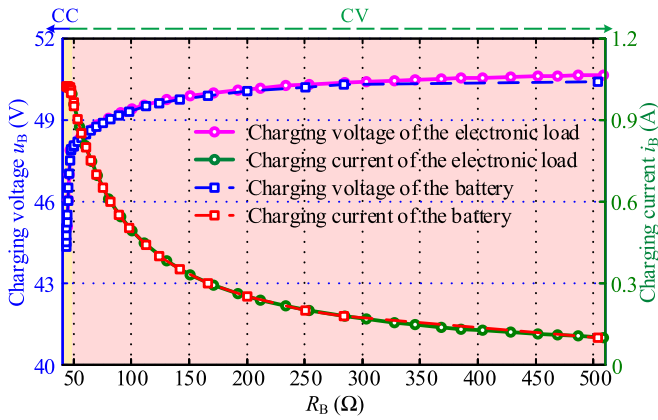


Fig. 13. Measured charging profiles of charging electronic load and battery.

CC mode. When the charging voltage reach 48 V, the charging mode switches from the CC mode to the CV mode. In the CV mode, the charging current decays until it hits the end of charging condition (we set it as 0.1 A in this paper). Both electronic load and lead accumulator are utilized to be charged by the hybrid and reconfigurable IPT system. The measured charging profiles of electronic load and lead accumulator are shown in Fig. 13. The performances indicate that the charging profile of the battery is coincident with that of the electronic load, which means that it is feasible to adopt electric load instead of a battery. Furthermore, the charging current and voltage match the charging profile closely, demonstrating that the proposed method can be used to charge batteries.

The measured efficiencies of charging the battery are illustrated in Fig. 14. The results demonstrate that the efficiencies of the electronic load match that of the lead acid battery very well. During the whole charging process, the efficiency climbs from 91.2% to 92.5% and then decreases to 85.0% with an output power of 5.1 W.

3) *Power Loss of ACSs S_1 and S_2* : S_1 and S_2 are ACSs, consisting of two antiseres-connected MOSFETs, which is shown in Fig. 15 [22].

ACS S_1 is ON in the circuit for the CC mode, while ACS S_2 is ON for the CV mode. The ACSs only turn ON or turn OFF

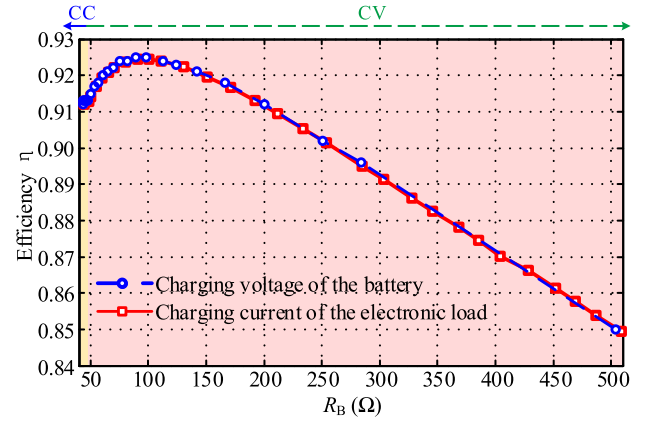


Fig. 14. Measured efficiencies of charging electronic load and battery.

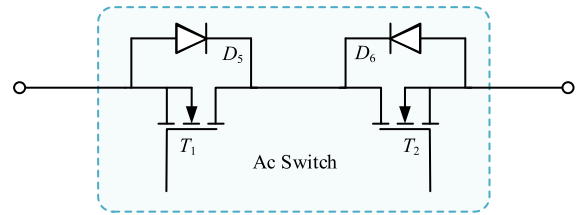


Fig. 15. ACS with two antiseres-connected MOSFETs.

for once. Thus, the switch conduction loss is inevitable, and switching loss can be ignored. The switch conduction loss is determined by the static ON-resistance and the drain current of MOSFET. The semiconductor component's conduction loss P_C is given by [34], [35].

$$P_C = R_{DSon} \times I_{D_{rms}}^2 \quad (32)$$

where R_{DSon} is the drain–source ON-state resistance of MOSFET and $I_{D_{rms}}$ is the RMS value of the MOSFET ON-state current.

In this paper, the ACS is implemented with APT56F50L manufactured by Microsemi. A preliminary assessment of the nominal losses in the ACS has been performed using the information found in the datasheets of the power switches [36]. According to the datasheet of APT56F50L, R_{DSon} is 0.085 Ω . When the IPT system changes the charging mode, the equivalent variable resistance R_B is 48 Ω . At this moment, the current flowing through ACS S_2/S_1 is the maximum. In the CC mode, ACS S_1 is ON and the RMS value of the MOSFET ON-state current $I_{D_{rms,S1}}$ is 7.4 A, and the conduction losses of the two MOSFETs are

$$\begin{aligned} P_{C,S1} &= 2 \times R_{DSon} \times I_{D_{rms,S1}}^2 \\ &= 2 \times 0.085 \times 7.4^2 = 9.3 \text{ W}. \end{aligned} \quad (33)$$

In the CV mode, ACS S_2 is ON and the rms value of the MOSFET ON-state current $I_{D_{rms,S2}}$ is 10.2 A, and the conduction loss of the two MOSFETs are

$$\begin{aligned} P_{C,S2} &= 2 \times R_{DSon} \times I_{D_{rms,S2}}^2 \\ &= 2 \times 0.085 \times 10.2^2 = 17.7 \text{ W}. \end{aligned} \quad (34)$$

The maximum output power of the IPT system is 1 kW; according to (33) and (34), the switch conduction loss in the CC

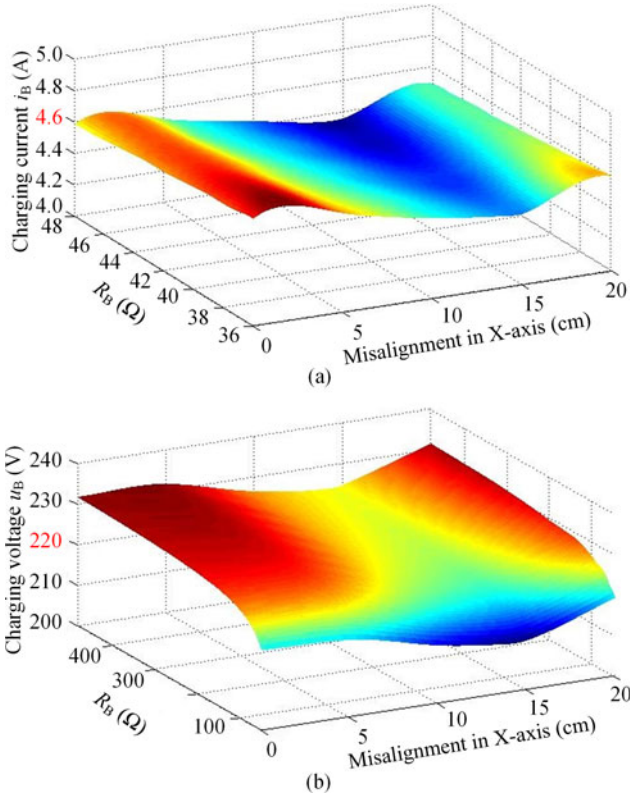


Fig. 16. (a) CC mode: charging current i_B against variable loads with misalignment in the X-axis. (b) CV mode: charging voltage u_B against variable loads with misalignment in the X-axis.

mode accounted for 0.9% of the output power, and the switch conduction loss in the CV mode accounted for 1.8% of the output power.

B. Performance of the IPT System With Misalignment

The output current i_B and the output voltage u_B of the IPT system against variable loads with misalignment are shown in Fig. 16. The results demonstrate that the fluctuations of the current i_B and voltage u_B are both within the presupposed deviation Δ , that is 5%. The maximum misalignment in the X-axis can achieve 50% with the range of the load from 36 to 480 Ω in CC and CV modes. The efficiencies of the IPT system in CC and CV modes with longitudinal misalignment are shown in Fig. 17. The maximum efficiency is 93.9% at the NOP, and the minimum one is 75.1% with the longitudinal misalignment of 50% at $R_B = 480 \Omega$. When the misalignment is 20 cm in the X-axis, the experimental waveforms of u_{AB} , i_{AB} , u_B , and i_B at $R_B = 36 \Omega$ in the CC mode and at $R_B = 50 \Omega$ in the CV mode are given in Fig. 18. There is a small phase angle of i_B lagging u_B , which realizes zero-voltage switching of the HFI switches.

Similar experiments with misalignment in Y- and Z-axis are also observed. As for the misalignment in the Y-axis, the designed IPT system only can tolerate the lateral misalignment to 5 cm, i.e., 12.5%. The reason for the smaller misalignment than that in the X-axis is that the cross couplings increase much with the misalignment changes. In the Z-axis, the IPT system

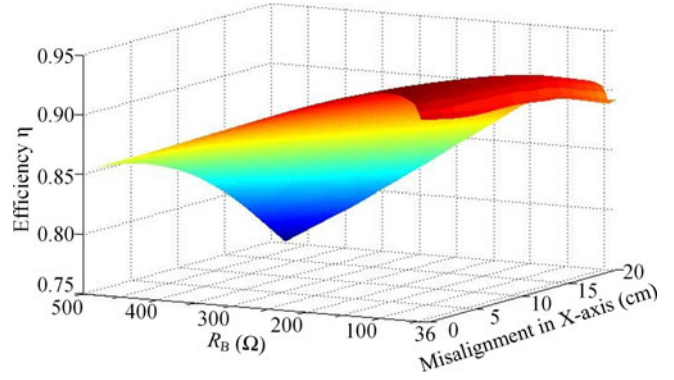


Fig. 17. Measured efficiencies of the IPT system in CC and CV modes with misalignment in the X-axis.

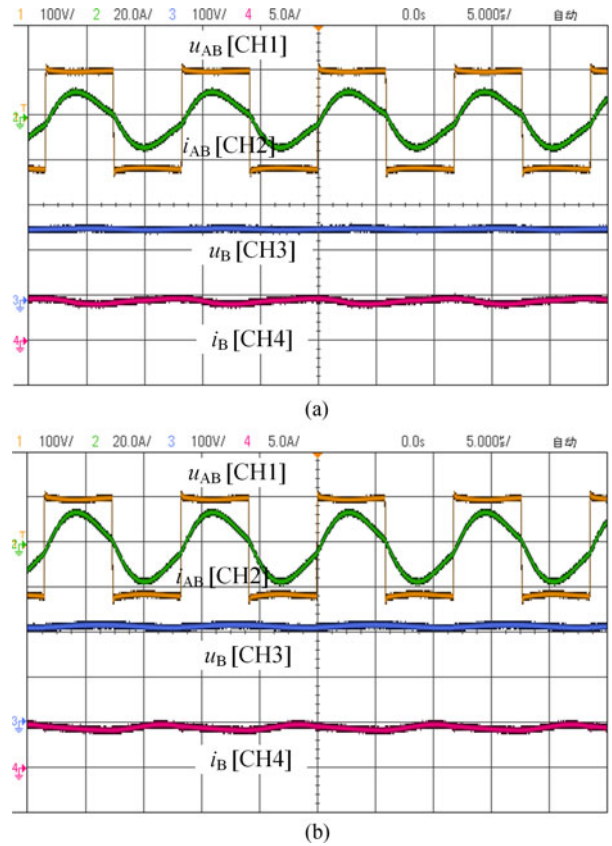


Fig. 18. Experimental waveforms of u_{AB} , i_{AB} , u_B , and i_B . (a) $R_B = 36 \Omega$ in the CC mode. (b) $R_B = 50 \Omega$ in the CV mode with the misalignment in the X-axis of 20 cm.

can have the misalignment of $\pm 33.3\%$, which means that the vertical height of the secondary pads can increase to 200 mm or decrease to 100 mm. The main couplings are both nearly 20 μH (M_{34_min}) with the height of 200 mm, and they are more than 60 μH (M_{34_max}) of 100 mm. In this circumstance, the values of the main couplings are not suitable for the proposed parametric design method anymore.

In addition, we measured the currents and voltages of the devices on the secondary side under different charging

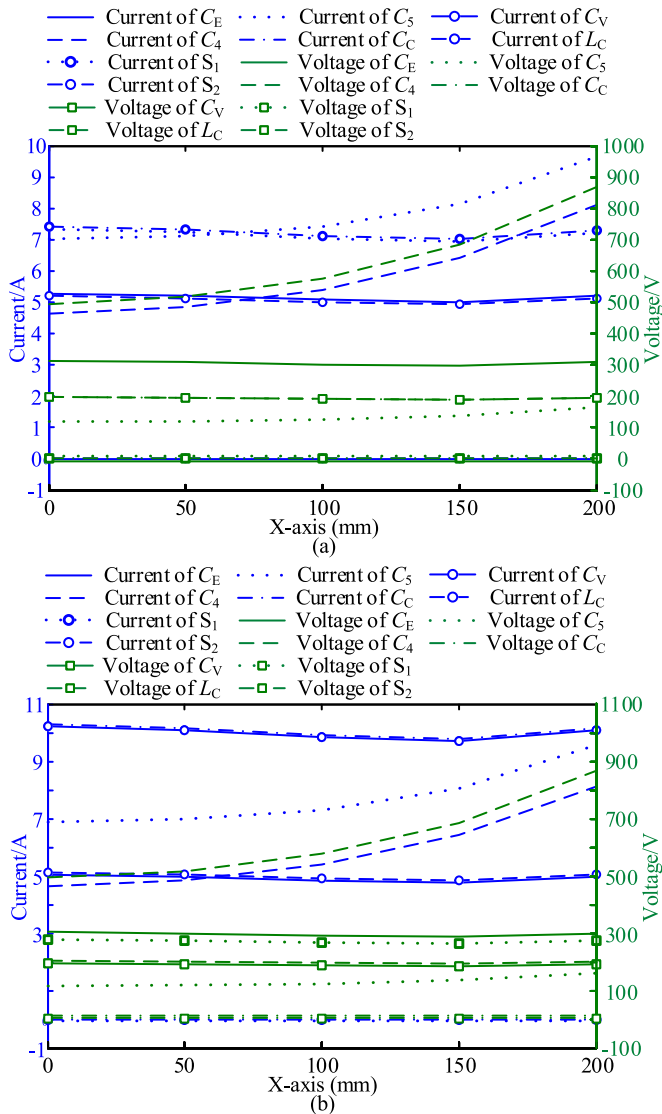


Fig. 19. Measured device stresses on the secondary devices in (a) CC mode and (b) CV mode.

condition (CC and CV modes) and various x -misalignments. The measured results are given in Fig. 19. The increase of the misalignment results in the increase of secondary coil current. It indicates that when misalignment happens, the output power of the LCC - S topology decreases, while the output power of the S - LCC topology increases to keep the output voltage maintaining relatively constant. The performances verify the feasibility of the proposed scheme.

V. CONCLUSION

An innovative approach for CC and CV outputs with the ability of 3-D misalignments is proposed in this paper. The load-independent output characteristic of the hybrid topology and the function switching between CC and CV of the reconfigurable topology are studied, and the hybrid and reconfigurable topology is proposed, which combines the characteristics of LCC - S and S - LCC configurations. Moreover, a novel

parametric design method for the hybrid and reconfigurable topology is proposed, which can limit the fluctuation of the output voltage/current within a certain range of misalignment tolerances. A 1-kW laboratory-scale prototype having the hybrid and reconfigurable topology has been developed. In the CC/CV mode, the system can operate with the longitudinal misalignment to 50% when the load varies from 36 to 480 Ω , and the fluctuation of the output current/voltage is within 5%. Similarly, the misalignment in Y - and Z -axis is 12.5% and 33.3%, respectively. The results validate the theoretical analysis and demonstrate the good behavior of the proposed method.

Our future work is about 1) the coupling pads, of which the main couplings vary relatively similar with misalignment in X - and Y -axis, and 2) the optimization of the variations of the main mutual inductances to increase the misalignment tolerance for the hybrid and reconfigurable IPT system.

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