

Suppression of Real Power Back Flow of Nonregenerative Cascaded H-Bridge Inverters Operating Under Faulty Conditions

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Abstract—When the faulty cells of a nonregenerative cascaded H-bridge inverter are bypassed, it is necessary to inject a zero-sequence voltage into the inverter phase voltages so as to achieve the maximum balanced line-to-line voltage. However, the injected zero-sequence voltage may lead to a back flow of real power (BFRP) in at least one phase so that the dc voltage will rise to an intolerable level. To solve the problem, this paper proposes a new method of generating zero-sequence voltages. This method can not only maximize the available output voltage but also suppress the BFRP effectively. The zero-sequence voltage is always limited to an appropriate range to achieve the maximum output voltage through linear modulation. Closed-loop control is used to minimize the fundamental component of the zero-sequence voltage so that the power is less likely to flow back. Compared with the conventional methods, the proposed method allows the inverter to drive a load with a lower power factor not inducing BFRP or degrading the output voltage capability. As a result, the probability of overvoltage on the dc side is reduced. The experimental results show that the proposed method is effective and feasible.

Index Terms—Cascaded H-bridge (CHB) inverter, fault tolerant, modulation, real power back flow, zero sequence.

I. INTRODUCTION

MULTILEVEL inverters have attracted much attention in recent years [1], [2]. The topologies of multilevel inverters mainly include diode-clamped inverters [3], flying-capacitor inverters [4], cascaded H-bridge (CHB) inverters [5], and modular multilevel converters [6]. Among them, CHB inverters have been widely used for high-voltage motor drives due to their good power quality [7]. Fig. 1 shows a typical CHB inverter. Owing to the large number of power cells, its failure probability is comparatively high. Therefore, the reliability of CHB inverters has been the focus of a great deal of research [8]–[10]. On

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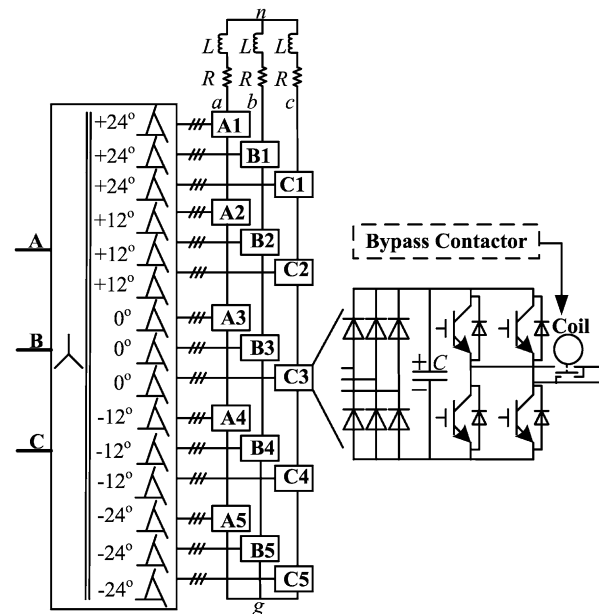


Fig. 1. CHB inverter.

the other hand, when one or several cells of a CHB inverter fail, these faulty cells can be short-circuited by bidirectional thyristors [11] or bypass contactors [12], and the inverter can continue its operation. Such fault-tolerant operation needs both accurate fault diagnosis and appropriate control methods under fault conditions [13]. Much work has been done on the former [11], [14], [15], while this paper focuses on the latter.

After the faulty cells are isolated, there may be a difference in the number of the remaining normal cells in three phases, so the maximum available output voltages in three phases also may be different. If the modulating signals are not adjusted accordingly, the output line-to-line voltages may be unbalanced. To solve the problem, several fault-tolerant methods have been proposed, which include using redundant cells, adjusting dc voltages, bypassing the cells at the same level, using redundant switching states, and injecting a zero-sequence voltage.

Using redundant cells is a hardware method, which means that an extra cell must be added in each phase besides the cells required for normal operation [16]. With redundant cells, the inverter can tolerate at least one faulty cell with no loss of capacity. However, the redundant cells inevitably increase cost. The method based on the adjustment of dc voltages is to raise the dc voltages of the remaining cells in the faulty phases [17].

This method enables the inverter to keep its normal output voltage capability under fault conditions, but it can be used only in the applications in which dc voltages are controllable, such as static var compensators (STATCOM) and CHB inverters with front-end PWM rectifiers providing dc voltages. Moreover, the switching devices are required to endure a voltage higher than the normal level, which also increases cost. The method based on bypassing the cells at the same level means bypassing an equal number of cells in all three phases, even if some of these bypassed cells have not failed [13]. This method bypasses too many normal cells, so the output voltage capability will drop severely, especially when the majority of the faulty cells are concentrated in one phase. Redundant switching states are generally used in the space vector pulse width modulation [18]. Because a CHB inverter has many switching states, the complexity of the methods based on redundant switching states increases significantly with increasing number of cells.

Injecting a zero-sequence voltage may be the most practical fault-tolerant method. Among all, the methods based on zero-sequence voltage injection, the neutral point shift (NPS) method may be the earliest one [19]. This method bypasses only the faulty cells and balances the line-to-line voltages by adjusting the amplitude and phase of the inverter phase voltages. The essence of the NPS method is to inject a fundamental zero-sequence voltage. The maximum available output voltage achieved by the NPS method is higher than that achieved by bypassing the cells at the same level. However, the NPS method requires that the ratio of the amplitude values of the three-phase inverter voltages should be the same as the ratio of the numbers of the remaining normal cells in three phases. Limited by this, the output voltage capability of the CHB inverter cannot be fully raised. In [20], the NPS method is improved by forcing the phase angle between the two inverter phase voltages with the lowest amplitude values to be 180° . However, this method can extend the output voltage capability only under some special fault conditions. In [21], the amplitude and phase of the inverter phase voltages are not directly changed; instead, a zero-sequence voltage generated by the closed-loop control is injected. This method can fully raise the output voltage capability under all fault conditions apart from the case when all cells in one phase are bypassed. In [22] and [23], open-loop methods are used to generate the zero-sequence voltage. Among all, these methods based on zero-sequence voltage injection, it can be verified that, under most fault conditions, the methods proposed in [21] and [22] are capable of achieving higher output voltage through linear modulation than the other methods.

However, these methods have two common problems. One problem is that the input power quality and the output power quality may be degraded when several cells are bypassed [21]. In spite of this, this is often acceptable compared with a complete shutdown, as pointed out in [21]. The other problem is that the injected zero-sequence voltage may interact with the output currents and produce negative real power in at least one phase. This problem is more serious. If the negative power from the zero-sequence voltage is greater than the positive power from the positive-sequence voltage, the back flow of real power (BFRP) will occur. For those CHB inverters which are capable

of regeneration, the real power can be returned to the grid [24]. Unfortunately, many CHB inverters have a topology as shown in Fig. 1, where diode rectifiers are used to provide dc voltages. These inverters cannot return the real power to the grid. Thus, when the real power flows back to the dc side, the dc voltages will rise to an intolerable level, and, finally, damage the inverters. Moreover, the lower the load power factor (PF), the more likely the real power is to flow back [21].

To solve this problem, Hammond [21] suggests reducing the motor voltage to keep the load PF high enough at light load. This will weaken the flux of the motor. Although weakening flux helps to save energy at light load, it may degrade the maximum output torque and the ability to resist disturbances [25]. Therefore, the flux is kept at a high level in most applications. Moreover, energy saving effects of weakening the flux is not significant if the light-load operation time is not long. In addition, it is difficult to know exactly how high the load PF should be to avoid BFRP. Instead of changing load PF, Lezana [20] improves the modulation method under fault conditions to enable the inverter to tolerate lower load PF. However, as mentioned previously, this method can improve the NPS method only in some special fault conditions. Except [20] and [21], little literature is available on the issue of BFRP under fault conditions.

Since BFRP results from the injected zero-sequence voltage, we may avoid it by controlling the zero-sequence voltage. Injecting zero-sequence voltage to balance three-phase power is not a new idea. In [26], it is used to solve the power unbalance problem caused by negative-sequence currents. It is also adopted in [27] for balancing dc voltages in CHB STATCOMs, and in [28] and [29] for balancing SOC in CHB battery energy storage systems. However, these methods do not consider the requirement for the zero-sequence voltage to maximize output voltage capability without overmodulation. When CHB inverters operate under fault conditions, this requirement is important.

This paper proposes a new method of generating the zero-sequence voltage. The method can effectively suppress BFRP by reducing the fundamental component of the zero-sequence voltage. Moreover, the generated zero-sequence voltage is always limited to an appropriate range so that the amplitude of the output voltage can be raised to the maximum level without overmodulation. As a result, the proposed method can not only successfully maximize the output voltage capability but also extend the allowable range of the load PF under fault conditions. The organization of this paper is as follows. Section II investigates the requirements for the zero-sequence voltage in terms of maximizing available output voltage and suppressing BFRP. In Section III, the principle and the implementation of the proposed method are explained in detail. In Section IV, experimental results are provided to verify the validity of the proposed method. Finally, the paper is concluded in Section V.

II. REQUIREMENTS FOR THE ZERO-SEQUENCE VOLTAGES

Fig. 2 shows the basic block diagram of the modulation method based on zero-sequence voltage injection. In Fig. 2, u_{an} , u_{bn} , and u_{cn} are the reference voltages; u_0 is the zero-sequence voltage; u_{ag} , u_{bg} , and u_{cg} are the modulating voltages; u_{ag}^* , u_{bg}^* ,

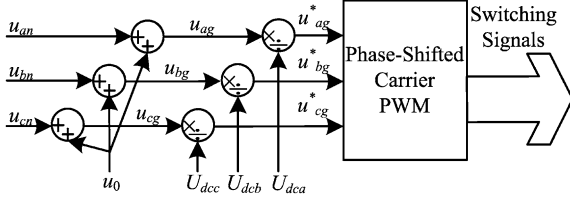


Fig. 2. Basic block diagram of the modulation method based on zero-sequence voltage injection.

and u_{cg}^* are the normalized modulating signals; and U_{dca} , U_{dcb} , and U_{dcc} are the sums of the dc voltages of the normal cells in each phase. In this paper, U_{dca} , U_{dcb} , and U_{dcc} are called available dc voltages in phases a , b , and c , respectively. Suppose that the dc voltage of each cell is $V_{dc,cell}$ and the numbers of the normal cells in phases a , b , and c are N_a , N_b , and N_c , respectively. Then, we have $(U_{dca}, U_{dcb}, U_{dcc}) = (N_a, N_b, N_c) V_{dc,cell}$. Different sets of (N_a, N_b, N_c) represent different fault conditions.

In this paper, high-frequency harmonics caused by PWM are ignored. Thus, within the linear modulation region, u_{an} , u_{bn} , and u_{cn} are also the load phase voltages between the output terminals a , b , or c and the load neutral point n in Fig. 1; u_{ag} , u_{bg} , and u_{cg} are also the inverter phase voltages between the output terminals a , b , or c and the inverter neutral point g in Fig. 1.

The requirements for u_0 include two aspects: not degrading the output voltage capability and not leading to BFRP.

A. Requirement for the Zero-Sequence Voltage to Maximize the Output Voltage Capability

In this paper, subscript k represents the phase and can be a , b , or c . Suppose that the reference voltage in phase k is

$$u_{kn} = U_{m,phase} \sin(\omega t + \varphi_k), k \in \{a, b, c\} \quad (1)$$

where φ_k equals 0, $-2\pi/3$, or $2\pi/3$ when k is a , b , or c , respectively; $U_{m,phase}$ is the amplitude of the reference voltage. Overmodulation may occur if $U_{m,phase}$ is large enough. The maximum value of $U_{m,phase}$ not leading to overmodulation depends on not only the dc voltages but also the zero-sequence voltage as well.

Carnielutti *et al.* [22] have derived the constraint on u_0 to avoid overmodulation. According to [22], u_0 should meet the following inequation to ensure $u_{kg}^* \in [-1, 1]$:

$$u_{\min}(t) \leq u_0(t) \leq u_{\max}(t) \quad (2)$$

where

$$\begin{aligned} u_{\min}(t) &= \max_{k=a,b,c} (c'_k), c'_k \\ &= -U_{dck} - u_{kn}(t), k \in \{a, b, c\} \end{aligned} \quad (3)$$

$$\begin{aligned} u_{\max}(t) &= \min_{k=a,b,c} (c_k), c_k \\ &= U_{dck} - u_{kn}(t), k \in \{a, b, c\}. \end{aligned} \quad (4)$$

Note that whether (2) is valid depends on not only u_0 but also u_{kn} and U_{dck} . If $u_{\min} > u_{\max}$, no matter how u_0 is generated,

(2) cannot be satisfied and overmodulation will inevitably occur. Therefore, we should first ensure that $u_{\min} \leq u_{\max}$ to avoid overmodulation. That means the following inequation must be valid:

$$-U_{dci} - u_{in}(t) \leq U_{dcj} - u_{jn}(t), \text{ for } \forall i, j \in \{a, b, c\}. \quad (5)$$

Equation (5) can also be expressed as

$$u_{ji} \leq U_{dci} + U_{dcj}, \text{ for } \forall i, j \in \{a, b, c\} \quad (6)$$

where $u_{ji} = u_{jn} - u_{in}$. Considering the symmetry of the waveforms of u_{ji} , (6) is equivalent to

$$\begin{cases} -U_{dca} - U_{dcb} \leq u_{ab} \leq U_{dca} + U_{dcb} \\ -U_{dcb} - U_{dcc} \leq u_{bc} \leq U_{dcb} + U_{dcc} \\ -U_{dcc} - U_{dca} \leq u_{ca} \leq U_{dcc} + U_{dca}. \end{cases} \quad (7)$$

Considering that the line-to-line voltages must be balanced, the following inequation is valid for $\forall i, j \in \{a, b, c\}$:

$$-(U_{dc,\min} + U_{dc,\text{mid}}) \leq u_{ij} \leq U_{dc,\min} + U_{dc,\text{mid}} \quad (8)$$

where $U_{dc,\min}$, $U_{dc,\text{mid}}$, and $U_{dc,\max}$ are the maximum, the median, and the minimum among U_{dca} , U_{dcb} , and U_{dcc} , respectively. Thus, we have

$$U_{m,phase} = U_{m,l-l}/\sqrt{3} \leq (U_{dc,\min} + U_{dc,\text{mid}})/\sqrt{3} \quad (9)$$

where $U_{m,l-l}$ is the amplitude of the line-to-line voltages. On the other hand, if (9) is right, (5)–(8) can be derived sequentially. Then, we can further obtain $u_{\min} \leq u_{\max}$. Thus, we have

$$U_{m,phase} \leq (U_{dc,\min} + U_{dc,\text{mid}})/\sqrt{3} \Leftrightarrow u_{\min} \leq u_{\max}. \quad (10)$$

It can be concluded that: first, to avoid overmodulation, we should ensure that (9) is valid and that u_0 satisfies (2); second, the maximum amplitude of the load phase voltages achieved by linear modulation is

$$U_{MAX} = (U_{dc,\min} + U_{dc,\text{mid}})/\sqrt{3}. \quad (11)$$

As long as u_0 is constrained within $[u_{\min}, u_{\max}]$, overmodulation can be avoided for $\forall U_{m,phase} \in [0, U_{MAX}]$, and, thus, the maximum available output voltage can be achieved. According to this principle, Carnielutti *et al.* [22] gives a very simple formula for calculating u_0

$$u_0 = (u_{\max} + u_{\min})/2. \quad (12)$$

For $\forall U_{m,phase} \in [0, U_{MAX}]$, u_0 calculated from (12) obviously satisfies (2). Among the conventional methods based on injecting zero-sequence voltages mentioned in Section I, only the method in [22] can raise $U_{m,phase}$ to U_{MAX} through linear modulation under all fault conditions, even when all cells in one phase are bypassed. The method in [21] can also do this in all cases except when all the cells in one phase are bypassed. The other methods can match the two methods only under some of the possible fault conditions, but in other cases they cannot always constrain u_0 within $[u_{\min}, u_{\max}]$, for $\forall U_{m,phase} \in [0, U_{MAX}]$.

The method of calculating u_0 is not unique. The impacts of u_0 on the distribution of real power should also be considered.

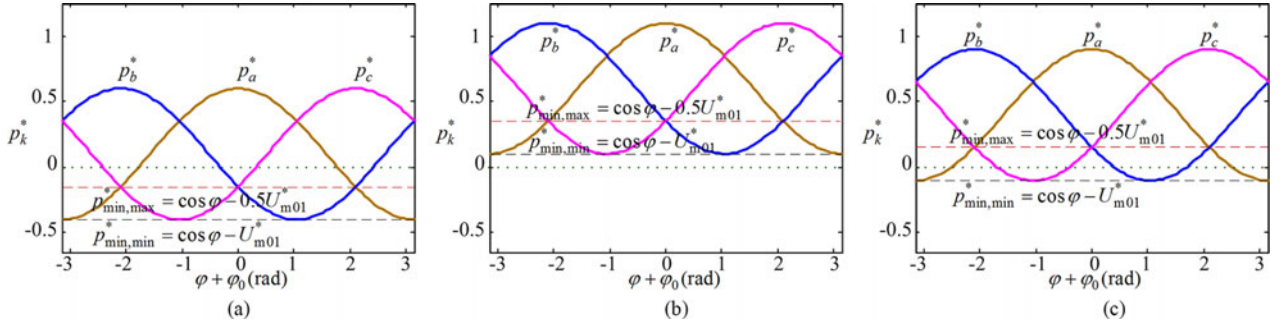


Fig. 3. Relationship curves between p_k^* and $\varphi_0 + \varphi$. (a) $U_{m01}^* > 2\cos\varphi$. (b) $U_{m01}^* < \cos\varphi$. (c) $2\cos\varphi > U_{m01}^* > \cos\varphi$.

B. Requirement for the Zero-Sequence Voltage to Suppress BFRP

Most of the loads drove by CHB inverters are approximately linear and balanced. Thus, in this paper, we assume that the inverter drives a three-phase linear and balanced load. Generally, the load currents are approximately sinusoidal. Therefore, the output current in phase k is assumed to be

$$i_k = I_m \sin(\omega t + \varphi_k - \varphi), k \in \{a, b, c\} \quad (13)$$

where φ is the load PF angle and I_m is the amplitude of the load current. Suppose that the fundamental component of the zero-sequence voltage is

$$u_{01} = U_{m01} \sin(\omega t + \varphi_0) \quad (14)$$

where U_{m01} and φ_0 are the amplitude and the initial phase angle, respectively. Then, the fundamental component of the inverter phase voltage is expressed as follows:

$$u_{kg1} = U_{m,\text{phase}} \sin(\omega t + \varphi_k) + U_{m01} \sin(\omega t + \varphi_0), k \in \{a, b, c\}. \quad (15)$$

According to the definition of average power, the average power in phase k can be calculated from

$$p_k = \left(\int_0^{2\pi} u_{kg1} i_k d\omega t \right) / (2\pi) = U_{m,\text{phase}} I_m \cos(\varphi) + U_{m01} I_m \cos(\varphi + \varphi_0 - \varphi_k). \quad (16)$$

We define the normalized zero-sequence fundamental amplitude as

$$U_{m01}^* = U_{m01} / U_{m,\text{phase}} \quad (17)$$

and define the normalized average power in phase k as

$$p_k^*(U_{m01}^*, \varphi, \varphi_0) = p_k / (U_{m,\text{phase}} I_m) = \cos(\varphi) + U_{m01}^* \cos(\varphi + \varphi_0 - \varphi_k). \quad (18)$$

To avoid BFRP, we require that the real power in all three phases should be positive. That means

$$p_{\min}^* = \min_{k \in \{a, b, c\}} (p_k^*) \geq 0. \quad (19)$$

If U_{m01}^* and φ are constants, p_k^* can be viewed as a function of $\varphi_0 + \varphi$. Fig. 3 shows the relationship between p_k^* and $\varphi_0 + \varphi$ in the cases when $U_{m01}^* > 2\cos\varphi$, $U_{m01}^* < \cos\varphi$, and $2\cos\varphi \geq U_{m01}^* \geq \cos\varphi$.

According to (18), (19) and Fig. 3, it can be found that p_{\min}^* curve is the lower envelope line of p_a^* , p_b^* , and p_c^* curves. When $U_{m01}^* > 2\cos\varphi$, p_{\min}^* is always negative and BFRP will inevitably occur. When $U_{m01}^* < \cos\varphi$, p_{\min}^* is always positive and BFRP will never occur. When $2\cos\varphi \geq U_{m01}^* \geq \cos\varphi$, following inequation can be derived from $p_{\min}^* \geq 0$:

$$(2N - 1)\pi/3 + \arccos(\cos\varphi / U_{m01}^*) - \varphi_0 \leq \varphi \leq (2N + 1)\pi/3 - \arccos(\cos\varphi / U_{m01}^*) - \varphi_0, N \in \mathbb{Z}. \quad (20)$$

According to the above analysis, the following conclusions can be drawn.

First, either of the following two conditions must be satisfied to avoid BFRP: first, $U_{m01}^* < \cos\varphi$; second, $2\cos\varphi \geq U_{m01}^* \geq \cos\varphi$ and (20) is valid.

Second, if φ and φ_0 are constants, the real power is more likely to flow back as U_{m01}^* becomes larger. Suppose that $\exists A, B \in [0, 1]$ and $A > B$. If $p_{\min}^* < 0$ when $U_{m01}^* = A$, we also have $p_{\min}^* < 0$ when $U_{m01}^* = B$.

Third, if U_{m01}^* and φ are constants, p_{\min}^* is a periodical function of φ_0 , and the period is $2\pi/3$.

C. Definition of the Conservative Range of the PF Angle (CRPA)

The first conclusion above implies that it is impossible to avoid BFRP, for $\forall \varphi \in [-\pi/2, \pi/2]$ as long as u_0 has a fundamental component. However, u_0 must be limited within $[u_{\min}, u_{\max}]$ to avoid overmodulation. In Section III, it will be seen that the region between the waveforms of u_{\min} and u_{\max} under fault conditions may be so unregular that the fundamental component of u_0 is difficult to eliminate completely. Therefore, the range of φ that the inverter can tolerate without degrading output voltage capability or inducing BFRP cannot be easily extended to $[-\pi/2, \pi/2]$. Nevertheless, the range is expected to be as wide as possible. The wider the range, the lower the possibility of BFRP is. Thus, the range can be used to evaluate the ability of a method based on zero-sequence voltage injection to suppress BFRP. Considering that this range is also affected by U_{dck} and $U_{m,\text{phase}}$, the range is expressed mathematically by the concept of CRPA defined as follows.

Suppose that a certain method based on zero-sequence voltage injection is used. Given a set of $(U_{dca}, U_{dcb}, U_{dcc})$, if $[\varphi_{\min}, \varphi_{\max}]$ meets both of the following conditions: first, for $\forall \varphi \in [\varphi_{\min}, \varphi_{\max}]$ and $\forall U_{m,\text{phase}} \in [0, U_{\text{MAX}}]$, $p_{\min}^* \geq$

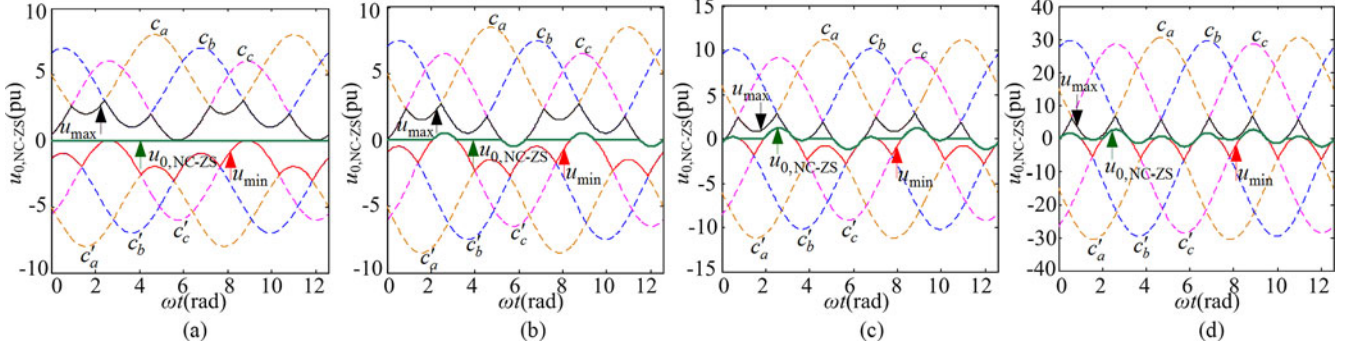


Fig. 4. NC-ZS signals. (a) 0-type. (b) I-type. (c) II-type. (d) III-type.

0; second, for $\forall \varphi \in [-\pi/2, \varphi_{\min}) \cup (\varphi_{\max}, \pi/2]$, $\exists U_{m,\text{phase}} \in [0, U_{\text{MAX}}]$ such that $p_{\min}^* < 0$, then the interval $[\varphi_{\min}, \varphi_{\max}]$ is referred to as the CRPA of the method under the condition of $(U_{\text{dca}}, U_{\text{dcb}}, U_{\text{dcc}})$.

This concept is defined only for those methods that can raise $U_{m,\text{phase}}$ to U_{MAX} , such as the methods proposed in [21] and [22]. As for the methods that cannot raise $U_{m,\text{phase}}$ to U_{MAX} , their CRPA will be not discussed in this paper, since those methods have a disadvantage in terms of output voltage capability.

III. GENERATION OF THE ZERO-SEQUENCE VOLTAGE

To make the CRPA as wide as possible, we should first ensure that u_0 is always constrained within $[u_{\min}, u_{\max}]$, for $\forall U_{m,\text{phase}} \in [0, U_{\text{MAX}}]$. Then, according to the second conclusion mentioned in Section II-B, the smaller U_{m01}^* , the less likely the real power is to flow back. Therefore, the possibility of BFRP can be effectively reduced if U_{m01}^* is made as small as possible.

The process of confining u_0 within $[u_{\min}, u_{\max}]$ can be achieved by a saturation link, the lower and upper limits of which are u_{\min} and u_{\max} , respectively. The output of the link can be used as u_0 . Now, the problem is how to find an appropriate input signal to make the fundamental component of the output as small as possible. It is a challenge because the limits of the saturation link are dynamic and unregular.

In this section, the problem is solved step by step. First, we temporarily set the input signal of the saturation link to 0. The output signal obtained can be viewed as an embryonic form of u_0 . The output signal is called a naturally clipped zero-sequence (NC-ZS) signal, which is denoted by $u_{0,\text{NC-ZS}}$. Then, the saturation limits are regularized. Finally, we select a more appropriate signal as the input so that the fundamental component in the output can be effectively reduced.

A. NC-ZS Signals

There are four types of NC-ZS signals.

0-type: When $U_{m,\text{phase}} \leq U_{\text{dc},\text{min}}$, $u_{0,\text{SC-ZS}} \equiv 0$. Fig. 4(a) shows an example, in which $(U_{\text{dca}}, U_{\text{dcb}}, U_{\text{dcc}}) = (5, 4, 3)$ and $U_{m,\text{phase}} = 3$.

I-type: When $U_{\text{dc},\text{min}} < U_{m,\text{phase}} \leq U_{\text{dc},\text{mid}}$, the zero signal is clipped only once per half cycle of reference voltages on average. Fig. 4(b) shows an example, in which $(U_{\text{dca}}, U_{\text{dcb}}, U_{\text{dcc}}) = (5, 4, 3)$ and $U_{m,\text{phase}} = 3.5$.

II-type: When $U_{\text{dc},\text{mid}} < U_{m,\text{phase}} \leq \min(U_{\text{MAX}}, U_{\text{dc},\text{max}})$, the zero signal is clipped twice per half cycle of reference voltages on average. Fig. 4(c) shows an example, in which $(U_{\text{dca}}, U_{\text{dcb}}, U_{\text{dcc}}) = (6, 5, 4)$, $U_{m,\text{phase}} = 5.196$.

III-type: When $U_{\text{dc},\text{max}} < U_{m,\text{phase}} \leq U_{\text{MAX}}$, the zero signal is clipped three times per half cycle of reference voltages on average. Fig. 4(d) shows an example, in which $(U_{\text{dca}}, U_{\text{dcb}}, U_{\text{dcc}}) = (15, 14, 13)$, $U_{m,\text{phase}} = 15.588$.

Obviously, apart from 0-type NC-ZS signals, the other three types of NC-ZS signals inevitably have fundamental components. It can be found that the waveform of $u_{0,\text{NC-ZS}}$ can be reshaped within the region between the waveforms of u_{\min} and u_{\max} so that its fundamental component can be further reduced. Nevertheless, the irregularity of the waveforms of u_{\min} and u_{\max} makes it difficult to reshape $u_{0,\text{NC-ZS}}$. Therefore, it is necessary to regularize the waveforms of u_{\min} and u_{\max} . Furthermore, the revision of u_{\min} and u_{\max} must follow two principles: not degrading the available output voltage and not increasing the fundamental component in the output of the saturation link.

B. Revision of the Saturation Limits and Symmetrically Clipped Zero-Sequence (SC-ZS) Signals

According to (11), U_{MAX} is affected only by $U_{\text{dc},\text{min}}$ and $U_{\text{dc},\text{mid}}$. Therefore, if we replace $U_{\text{dc},\text{max}}$ with $U_{\text{dc},\text{mid}}$ when calculating u_{\min} and u_{\max} , we still have $u_{\max} \geq u_{\min}$ for $\forall U_{m,\text{phase}} \in [0, U_{\text{MAX}}]$. Thus, the maximum available output voltage will not change. The new available dc voltage in phase k can be defined as

$$U_{\text{dck},s} = \begin{cases} U_{\text{dc},\text{mid}}, & \text{when } U_{\text{dck}} = U_{\text{dc},\text{max}} \\ U_{\text{dck}}, & \text{when } U_{\text{dck}} \neq U_{\text{dc},\text{max}} \end{cases} \quad (21)$$

Replacing U_{dck} in (3), (4) with $U_{\text{dck},s}$ leads to new c_k, c'_k, u_{\min} , and u_{\max} , which are denoted by $c_{k,s}, c'_{k,s}, u_{\min,s}$, and $u_{\max,s}$, respectively. If the input of the saturation link is still 0, the new output signal is referred to as an SC-ZS signal, which is denoted by $u_{0,\text{SC-ZS}}$. There are only three types of SC-ZS signals.

0-type: When $U_{m,\text{phase}} \leq U_{\text{dc},\text{min}}$, $u_{0,\text{SC-ZS}} \equiv 0$. Fig. 5(a) shows an example, where U_{dck} and $U_{m,\text{phase}}$ are the same as those in Fig. 4(a).

I-type: When $U_{\text{dc},\text{min}} < U_{m,\text{phase}} \leq U_{\text{dc},\text{mid}}$, the zero signal is clipped only once per half cycle of the reference voltages on

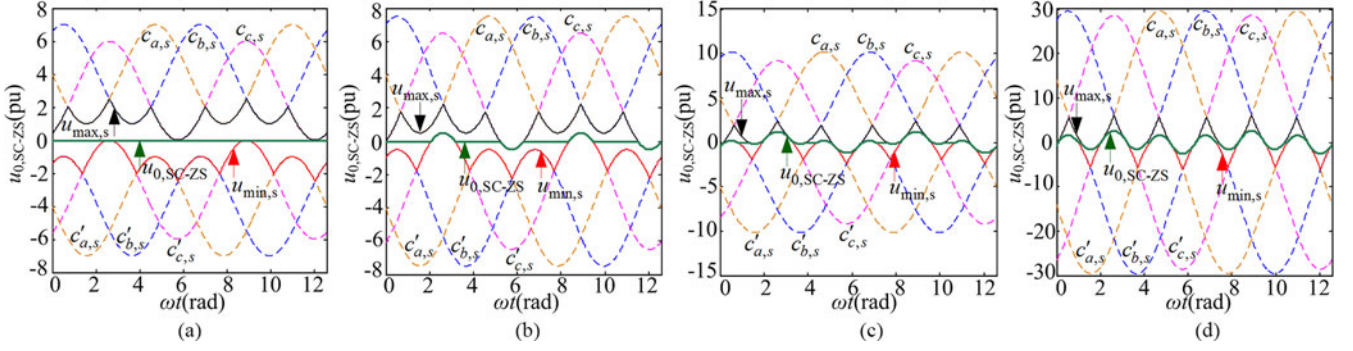


Fig. 5. SC-ZS signals. (a) 0-type. (b) I-type. (c) III-type, $(U_{dca}, U_{dcb}, U_{dcc}) = (6, 5, 4)$, $U_{m,phase} = 5.196$. (d) III-type, $(U_{dca}, U_{dcb}, U_{dcc}) = (15, 14, 13)$, $U_{m,phase} = 15.588$.

average. Fig. 5(b) shows an example, where U_{dck} and $U_{m,phase}$ are the same as those in Fig. 4(b).

III-type: When $U_{dc,mid} < U_{m,phase} \leq U_{MAX}$, the zero signal is clipped three times per half cycle of the reference voltages on average. Fig. 5(c) shows an example, where U_{dck} and $U_{m,phase}$ are the same as those in Fig. 4(c); Fig. 5(d) shows another example, where U_{dck} and $U_{m,phase}$ are the same as those in Fig. 4(d).

It can be seen from Fig. 5 that the waveforms of u_{min} and u_{max} both have reflection symmetry, which will help to further reshape the waveform of SC-ZS signals.

As mentioned at the end of Section II-A, the revision of u_{min} and u_{max} should not increase the fundamental component in the output of the saturation link. After the revision, NC-ZS signals are changed into SC-ZS signals. Based on Fourier analysis, their fundamental amplitudes can be compared. Although there are different types of NC-ZS signals and SC-ZS signals, they can be written in a uniform way

$$u_{0,NC-ZS} = u_{Ia} + u_{Ib} + u_{Ic} \quad (22)$$

$$u_{0,SC-ZS} = u_{Ia,s} + u_{Ib,s} + u_{Ic,s} \quad (23)$$

where

$$u_{Ik} = \begin{cases} c_k, & \text{when } c_k < 0 \\ 0, & \text{when } c'_k < 0 < c_k, k \in \{a, b, c\} \\ c'_k, & \text{when } c'_k > 0 \end{cases} \quad (24)$$

$$u_{Ik,s} = \begin{cases} c_{k,s}, & \text{when } c_{k,s} < 0 \\ 0, & \text{when } c'_{k,s} < 0 < c_{k,s}, k \in \{a, b, c\}. \\ c'_{k,s}, & \text{when } c'_{k,s} > 0 \end{cases} \quad (25)$$

We call u_{Ik} the I-type component of an NC-ZS signal and call $u_{Ik,s}$ the I-type component of an SC-ZS signal. Fig. 6 shows a typical waveform of u_{Ik} or $u_{Ik,s}$ in one period. From Fourier decomposition, the fundamental component of u_{Ik} can be derived, which is

$$u_{Ik1}(t) = U_{Im}(U_{dck})\sin(\omega t + \varphi_k + \pi), k \in \{a, b, c\} \quad (26)$$

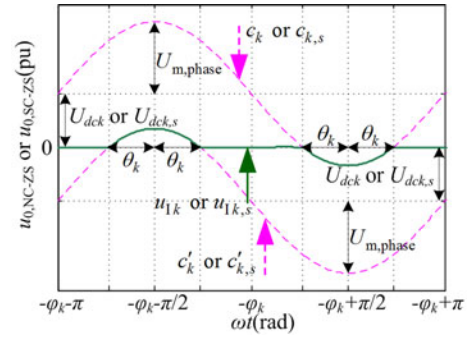


Fig. 6. Typical waveform of I-type component of an NC-ZS or SC-ZS signal in one period.

where U_{Im} is a function of U_{dck}

$$U_{Im}(U_{dck}) = U_{m,phase} [2\theta_k(U_{dck}) - \sin(2\theta_k(U_{dck}))] / \pi \quad (27)$$

$$\theta_k(U_{dck}) = \begin{cases} \arccos(U_{dck}/U_{m,phase}), & U_{dck} < U_{m,phase} \\ 0, & U_{dck} \geq U_{m,phase}. \end{cases} \quad (28)$$

Replacing the U_{dck} with $U_{dck,s}$ in (27), (28), we obtain the fundamental component of $u_{Ik,s}$

$$u_{Ik1,s}(t) = U_{Im}(U_{dck,s})\sin(\omega t + \varphi_k + \pi), k \in \{a, b, c\}. \quad (29)$$

Suppose that the fundamental components of $u_{0,NC-ZS}$ and $u_{0,SC-ZS}$ are $u_{01,NC-ZS}$ and $u_{01,SC-ZS}$, respectively. Obviously, we have $u_{01,NC-ZS} = u_{Ia1} + u_{Ib1} + u_{Ic1}$ and $u_{01,SC-ZS} = u_{Ia1,s} + u_{Ib1,s} + u_{Ic1,s}$. Without loss of generality, we assume that $U_{dca} = U_{dc,max}$, $U_{dcb} = U_{dc,mid}$, and $U_{dcc} = U_{dc,min}$. In this paper, if $x = X_m \sin(\omega t + \varphi_x)$, the phasor of x is expressed as $\mathbf{X} = X_m \varphi_x$. Thus, we have $\mathbf{U}_{Ia1} = U_{Im}(U_{dc,max})\pi$; $\mathbf{U}_{Ib1} = U_{Im}(U_{dc,mid}) \angle \pi/3$; $\mathbf{U}_{Ic1} = U_{Im}(U_{dc,min}) \angle 5\pi/3$; $\mathbf{U}_{Ia1,s} = U_{Im}(U_{dc,mid}) \angle \pi$; $\mathbf{U}_{Ib1,s} = U_{Im}(U_{dc,mid})\pi/3$; and $\mathbf{U}_{Ic1,s} = U_{Im}(U_{dc,mid}) \angle 5\pi/3$. Then, the phasors of $u_{01,NC-ZS}$ and $u_{01,SC-ZS}$ can be calculated from

$$\mathbf{U}_{01,NC-ZS} = \mathbf{U}_{Ia1} + \mathbf{U}_{Ib1} + \mathbf{U}_{Ic1} = U_{m01,NC-ZS} \angle \varphi_{NC-ZS} \quad (30)$$

$$\begin{aligned} \mathbf{U}_{01,SC-ZS} &= \mathbf{U}_{Ia1,s} + \mathbf{U}_{Ib1,s} + \mathbf{U}_{Ic1,s} \\ &= U_{m01,SC-ZS} \angle \varphi_{SC-ZS} \end{aligned} \quad (31)$$

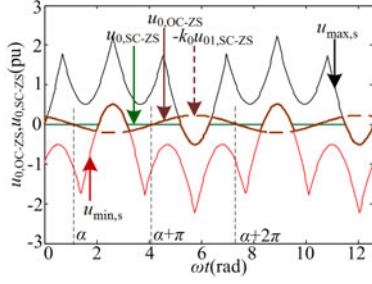


Fig. 7. Waveform of an OC-ZS signal.

where

$$U_{m01,NC-ZS} = \{ [U_{Im}(U_{dc,max}) - U_{Im}(U_{dc,mid})/2 - U_{Im}(U_{dc,min})/2]^2 + 0.75[U_{Im}(U_{dc,mid}) - U_{Im}(U_{dc,min})]^2 \}^{1/2} \quad (32)$$

$$\varphi_{NC-ZS} = \arctan \left[\frac{(\sqrt{3}U_{Im}(U_{dc,mid}) - \sqrt{3}U_{Im}(U_{dc,min}))}{(U_{Im}(U_{dc,mid}) + U_{Im}(U_{dc,min}) - 2U_{Im}(U_{dc,max}))} \right] + 2N\pi \quad (33)$$

$$U_{m01,SC-ZS} = U_{Im}(U_{dc,min}) - U_{Im}(U_{dc,mid}) \quad (34)$$

$$\varphi_{SC-ZS} = \varphi_c + \pi + 2N\pi, N \in \mathbb{Z}. \quad (35)$$

Because the derivative of U_{Im} with respect to U_{dck} is always nonpositive, we have $U_{Im}(U_{dc,max}) \leq U_{Im}(U_{dc,mid}) \leq U_{Im}(U_{dc,min})$. As a result

$$U_{m01,NC-ZS}^2 - U_{m01,SC-ZS}^2 = [U_{Im}(U_{dc,max}) - U_{Im}(U_{dc,mid})] \cdot [U_{Im}(U_{dc,max}) - U_{Im}(U_{dc,min})] \geq 0. \quad (36)$$

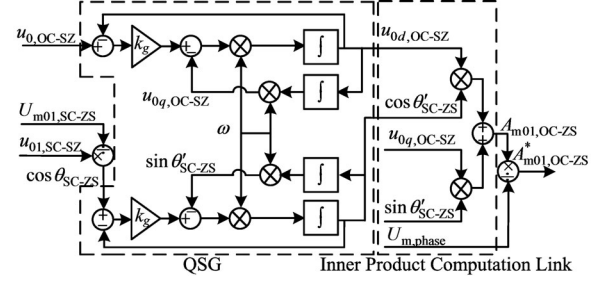
Therefore, the fundamental amplitude of $u_{0,SC-ZS}$ is not larger than that of $u_{0,NC-ZS}$. The revision of u_{min} and u_{max} does not increase the fundamental component in the output signal of the saturation link.

Moreover, if $u_{xn,dcmin}$ denotes the reference voltage in the phase with the smallest available dc voltage, it can be seen from (35) that the phase difference between $u_{01,SC-ZS}$ and $u_{xn,dcmin}$ is π . Therefore, $u_{01,SC-ZS}$ can be expressed as

$$u_{01,SC-ZS} = -U_{m01,SC-ZS} u_{xn,dcmin} / U_{m,phase}. \quad (37)$$

C. Oppositely Clipped Zero-Sequence (OC-ZS) Control Method

Now, the waveforms of the saturation limits have reflection symmetry, which makes it easy to select a more appropriate signal as the input signal of the saturation link. Obviously, the signal $-k_0 u_{01,SC-ZS}$ is a good choice. Here, k_0 is a nonnegative coefficient. If $-k_0 u_{01,SC-ZS}$ instead of zero is used as the input signal of the saturation link, a new output signal can be obtained. It is referred to as an OC-ZS signal and denoted by $u_{0,OC-ZS}$. Its fundamental component is denoted by $u_{01,OC-ZS}$. Fig. 7 shows the waveform of $u_{0,OC-ZS}$. When $k_0 = 0$, we obviously have $u_{0,OC-ZS} = u_{0,SC-ZS}$ and $u_{01,OC-ZS} = u_{01,SC-ZS}$. With k_0 increasing from 0, the waveform of $u_{0,OC-ZS}$ will have

Fig. 8. Detection of $A_{m01,OC-ZS}^*$.

negative parts over the interval $[\alpha, \alpha + \pi]$ and positive parts over the interval $[\alpha + \pi, \alpha + 2\pi]$, as shown in Fig. 7. According to the Fourier analysis and the symmetry of the waveform of $u_{0,OC-ZS}$, the amplitude of $u_{01,OC-ZS}$ will drop from the amplitude of $u_{01,SC-ZS}$ down to 0 as k_0 rises from 0.

In some cases, the region between the waveforms of $u_{max,s}$ and $u_{min,s}$ is so narrow that the amplitude of $u_{01,OC-ZS}$ cannot be reduced to 0 even if k_0 increases to $+\infty$. In some other cases, the region is wide enough so that the amplitude of $u_{01,OC-ZS}$ is reduced to 0 when k_0 reaches a certain value. However, if k_0 continues increasing, the amplitude of $u_{01,OC-ZS}$ will increase. Therefore, k_0 must be set to an appropriate value.

Feedback control is used to find an appropriate k_0 in this paper. Here, the algebraic amplitude of $u_{01,OC-ZS}$ is defined as a quantity that has the same absolute value as the amplitude of $u_{01,OC-ZS}$ and has the same sign as the product of $u_{01,OC-ZS}$ and $u_{01,SC-ZS}$. The quantity is denoted by $A_{m01,OC-ZS}$. We also define the normalized value of $A_{m01,OC-ZS}$ as $A_{m01,OC-ZS}^* = A_{m01,OC-ZS} / U_{m,phase}$. The value of k_0 is adjusted according to the value of $A_{m01,OC-ZS}^*$. When $A_{m01,OC-ZS}^* > 0$, we increase k_0 . When $A_{m01,OC-ZS}^* < 0$, we reduce k_0 . Finally, the fundamental component of $u_{0,OC-ZS}$ will be as small as possible. The control method described above is referred to as OC-ZS control.

In the control process, the adjustment of k_0 can be implemented by a PI controller. $A_{m01,OC-ZS}^*$ can be detected by the method illustrated in Fig. 8, in which there are two quadrature signal generators based on second-order general integrator [30] and one inner product computation link. According to [30], the gain k_g should be 1.414 to acquire an optimal damping factor. The whole process of OC-ZS control is shown in Fig. 9.

In Fig. 9, $u_{an}, u_{bn}, u_{cn}, U_{m,phase}$, and ω depend on other control objectives; U_{dca}, U_{dcb} , and U_{dcc} are determined by dc voltages of the cells and the number of normal cells in each phase. The PI controller automatically adjusts k_0 according to the value of $A_{m01,OC-ZS}^*$. The lower output limit of the PI is set to 0 so that $k_0 \geq 0$. Thus, the amplitude of $u_{01,OC-ZS}$ will be always smaller than that of $u_{01,SC-ZS}$. The final output signal u_0 , namely $u_{0,OC-ZS}$ will be used in the modulation process depicted by Fig. 2.

D. Ability of OC-ZS Control to Suppress BFRP

In Section II-C, the concept of CRPA is defined to evaluate the ability of a method based on zero-sequence voltage injection

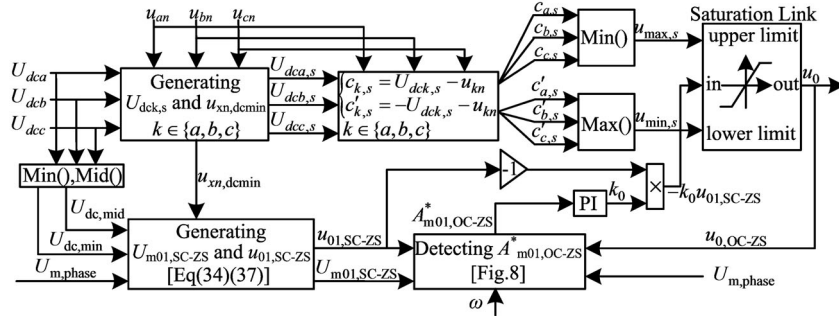


Fig. 9. Diagram of an OC-ZS control.

to suppress BFRP. The CRPA of OC-ZS control is difficult to determine exactly because it is affected by the parameters of the PI controller in Fig. 9. However, we have known that the amplitude of $u_{01,OC-ZS}$ is always smaller than that of $u_{01,SC-ZS}$. Thus, if the method that SC-ZS signals are used as u_0 is referred to as SC-ZS method, the CRPA of OC-ZS control is at least wider than that of the SC-ZS method. Therefore, the CRPA of the SC-ZS method can be viewed as a conservative evaluation of the CRPA of an OC-ZS control.

According to (18), (19), p_{\min}^* is a function of U_{m01}^* , φ , and φ_0 . When $u_0 = u_{0,SC-ZS}$, U_{m01}^* can be expressed according to (17), (34) by (38) as shown at the bottom of the page, where

$$\theta_{\min} = \arccos(U_{dc,\min}/U_{m,\text{phase}}) \quad (39)$$

$$\theta_{\text{mid}} = \arccos(U_{dc,\text{mid}}/U_{m,\text{phase}}). \quad (40)$$

Given a set of $(U_{dca}, U_{dcb}, U_{dcc})$, U_{m01}^* is a function of $U_{m,\text{phase}}$. The function is represented by $U_{m01}^*(U_{m,\text{phase}})$. Therefore, p_{\min}^* is also a function of $U_{m,\text{phase}}$, φ , and φ_0 , and can be expressed as $p_{\min}^* = p_{\min}^*(U_{m01}^*(U_{m,\text{phase}}), \varphi, \varphi_0)$. When $u_0 = u_{0,SC-ZS}$, φ_0 defined in (14) may be π or $\pm\pi/3$ according to (37). However, according to the third conclusion in Section II-B, p_{\min}^* is a periodical function of φ_0 and the period is $2\pi/3$. Thus, it makes no difference whether φ_0 equals π or $\pm\pi/3$, so φ_0 can be set to π . Now, p_{\min}^* is affected only by $U_{m,\text{phase}}$ and φ . For each value of $U_{m,\text{phase}}$ that belongs to $[0, U_{\text{MAX}}]$, we can derive a range of φ satisfying $p_{\min}^* \geq 0$. When $u_0 = u_{0,SC-ZS}$, the CRPA is the intersection of all such ranges. There are infinite values in $[0, U_{\text{MAX}}]$, so it is impossible to obtain all the ranges. However, according to the second conclusion in Section II-B, the real power is most likely to flow back when U_{0m1}^* reaches its maximum value. Therefore, when U_{0m1}^* equals its maximum value, the range of φ satisfying $p_{\min}^* \geq 0$ is the narrowest one, namely the CRPA of the SC-ZS method.

It is easy to prove that U_{m01}^* is a continuous function of $U_{m,\text{phase}}$ when $U_{m,\text{phase}} \in [0, U_{\text{MAX}}]$ and that the function is piecewise differentiable. When $U_{dc,\min} < U_{m,\text{phase}} < U_{dc,\text{mid}}$,

we have $dU_{m01}^*/d\theta_{\min} = 2(1 - \cos 2\theta_{\min})/\pi > 0$. According to (39), θ_{\min} is an increasing function of $U_{m,\text{phase}}$. Therefore, U_{m01}^* is an increasing function of $U_{m,\text{phase}}$ when $U_{dc,\min} < U_{m,\text{phase}} < U_{dc,\text{mid}}$. When $U_{m,\text{phase}} > U_{dc,\text{mid}}$, we have

$$\frac{dU_{m01}^*}{dU_{m,\text{phase}}} = \frac{4 \cos(\theta_{\min} + \theta_{\text{mid}}) \sin(\theta_{\min} - \theta_{\text{mid}})}{\pi U_{m,\text{phase}}}. \quad (41)$$

According to (39), (40), $\pi/2 > \theta_{\min} > \theta_{\text{mid}} > 0$, so $\sin(\theta_{\min} - \theta_{\text{mid}}) > 0$. If $\theta_{\min} + \theta_{\text{mid}} = \pi/2$, $U_{m,\text{phase}}$ equals $\sqrt{U_{\text{mid}}^2 + U_{\text{min}}^2}$. It is easy to prove that the following inequation is always valid:

$$\begin{aligned} \sqrt{U_{dc,\text{mid}}^2 + U_{dc,\min}^2} &> \frac{U_{dc,\text{mid}} + U_{dc,\min}}{\sqrt{3}} \\ &= U_{\text{MAX}} \geq U_{m,\text{phase}}. \end{aligned} \quad (42)$$

Furthermore, $\theta_{\min} + \theta_{\text{mid}}$ is obviously an increasing function of $U_{m,\text{phase}}$. As a result, we have $\theta_{\min} + \theta_{\text{mid}} < \pi/2$ and $\cos(\theta_{\min} + \theta_{\text{mid}}) > 0$. Thus, $dU_{m01}^*/dU_{m,\text{phase}} > 0$ and U_{m01}^* is still an increasing function of $U_{m,\text{phase}}$ when $U_{m,\text{phase}} \in (U_{dc,\text{mid}}, U_{\text{MAX}}]$. According to the above analysis, U_{m01}^* is an increasing function of $U_{m,\text{phase}}$ when $U_{m,\text{phase}} \in [0, U_{\text{MAX}}]$, so the maximum of U_{m01}^* is $U_{m01}^*(U_{\text{MAX}})$. The boundary values of the CRPA, namely φ_{\min} and φ_{\max} , are the solutions for φ in the following equation:

$$p_{\min}^* = \min_{k=a,b,c} (p_k^*(U_{m01}^*(U_{\text{MAX}}), \varphi, \pi)) = 0, \varphi \in \left[-\frac{\pi}{2}, \frac{\pi}{2}\right]. \quad (43)$$

This is a nonlinear equation which can be solved by numerical methods. For example, suppose that every phase has five cells and $V_{dc,\text{cell}}$ is 1; N_{max} , N_{mid} , and N_{min} denote the maximum, the median, and the minimum among N_a , N_b , and N_c , respectively; $N_{\text{max}} = x \geq 4$, $N_{\text{mid}} = 4$ and $N_{\text{min}} = 1$. In this case, $(U_{dc,\text{max}}, U_{dc,\text{mid}}, U_{dc,\text{min}}) = (x, 4, 1)$. Thus, U_{MAX} equals 2.8868. On substituting this value into (43) and solving for φ by fsolve function in MATLAB, we have $\varphi = \pm 1.2050$,

$$U_{m01}^* = U_{m01,SC-ZS}/U_{m,\text{phase}} = \begin{cases} 0, & U_{m,\text{phase}} \leq U_{dc,\min} \\ (2\theta_{\min} - \sin 2\theta_{\min})/\pi, & U_{dc,\min} < U_{m,\text{phase}} \leq U_{dc,\text{mid}} \\ \frac{(2\theta_{\min} - \sin 2\theta_{\min}) - (2\theta_{\text{mid}} - \sin 2\theta_{\text{mid}})}{\pi}, & U_{dc,\text{mid}} < U_{m,\text{phase}} \end{cases} \quad (38)$$

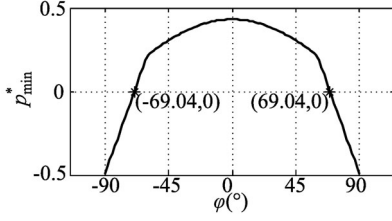


Fig. 10. $p_{\min}^* - \varphi$ curve when $(U_{dc,max}, U_{dc,mid}, U_{dc,min}) = (x, 4, 1)$.

TABLE I
CRPA AND THE MAXIMUM AMPLITUDES OF THE LINE-TO-LINE VOLTAGES
WHEN SC-ZS METHOD IS USED

$(N_{max}, N_{mid}, N_{min})$	$[\varphi_{min}, \varphi_{max}]^\circ$	$U_{l-l,max}/p.u.$
(5, 5, 5)	[-90, 90]	10
(5, 5, 4)	[-84.43, 84.43]	9
(5, 5, 3)	[-79.65, 79.65]	8
(5, 5, 2)	[-74, 74]	7
(5, 5, 1)	[-67, 67]	6
(5, 5, 0)	[-60, 60]	5
(x, 4, 4)	[-90, 90]	8
(x, 4, 3)	[-83.13, 83.13]	7
(x, 4, 2)	[-76.98, 76.98]	6
(x, 4, 1)	[-69.04, 69.04]	5
(x, 4, 0)	[-60, 60]	4
(x, 3, 3)	[-90, 90]	6
(x, 3, 2)	[-81.27, 81.27]	5
(x, 3, 1)	[-71.86, 71.86]	4
(x, 3, 0)	[-60, 60]	3
(x, 2, 2)	[-90, 90]	4
(x, 2, 1)	[-76.98, 76.98]	3
(x, 2, 0)	[-60, 60]	2
(x, 1, 1)	[-90, 90]	2
(x, 1, 0)	[-60, 60]	1

namely $\pm 69.04^\circ$. Therefore, the CPRA in this case is $[-69.04^\circ, 69.04^\circ]$, and the $p_{\min}^* - \varphi$ curve is plotted, as shown in Fig. 10.

Similar methods can be used to obtain the CRPA of SC-ZS method under any set of $(N_{max}, N_{mid}, N_{min})$. Table I lists the results as well as the maximum achievable amplitudes of the line-to-line voltages, which can be calculated from (9).

If $U_{m,phase}$ is set to U_{MAX} and φ is set to φ_{min} or φ_{max} listed in Table I, it can be verified through simulations or experiments that the method in [22] will lead to BFRP in all fault conditions except when $N_{min} = 0$. When $N_{min} = 0$, according to (3), (4), we have $u_{max} = u_{min} = -u_{xn,dcmin}$. Thus, u_0 can only be $-u_{xn,dcmin}$. As a result, the CRPA of either the method in [22] or SC-ZS method is $[-60^\circ, 60^\circ]$. Therefore, the CRPA of the method in [22] is narrower than that of the SC-ZS method when $N_{min} \neq 0$ and is the same as that of the SC-ZS method when $N_{min} = 0$. This means that the SC-ZS method is preferable to the method in [22] in terms of suppressing BFRP. Therefore, OC-ZS control is also better than the method in [22] in terms of suppressing BFRP.

IV. EXPERIMENTAL RESULTS

The experimental circuit was similar to the circuit in Fig. 1, but the difference was that there were no bypass contactors in the experimental circuit. A cell could be bypassed by keeping

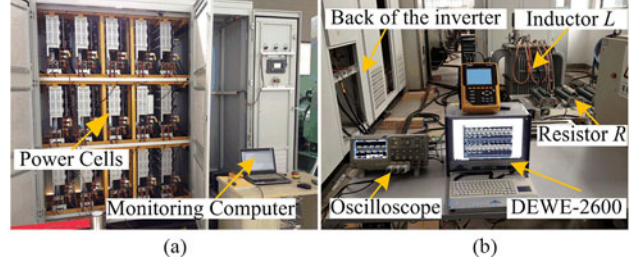


Fig. 11. Pictures of the experimental platform. (a) CHB inverter. (b) Load and the measuring equipment.

TABLE II
MAIN EXPERIMENTAL PARAMETERS

Parameters	VALUES
Normal cells in phase A, B, and C: (N_a, N_b, N_c)	(5, 3, 2)
Angular frequency of reference voltages: ω	$50 \cdot 2\pi$ rad/s
RMS value of primary line-to-line voltages	690 ± 69 V
The voltage ratio of the rectifier transformer	6000 V/705 V
DC voltage of each power cell without loads: V_{dc1}	109.6 V
DC voltage of each power cell with a load: V_{dc2}	107.8 V
Switching frequency: f_s	500 Hz
Load resistor: R	1.8014 Ω
Load induction: L	37.3 mH
Proportional coefficient of PI: K_P	100
Integration coefficient of P: K_I	0.1

its two upper IGBTs on and its two lower IGBTs off. The experimental platform is shown in Fig. 11. The whole inverter was controlled by a main controller, which was responsible for calculating the normalized modulating signals (u_{kg}^*) and transferring these signals to 15 cell controllers. The cell controllers sampled the received signals at a rate of 5000 Hz, and, then, compared the sampled signals with the triangular carrier waves with a frequency of 500 Hz to generate switching signals. The triangular carrier waves of different cells in phase k were displaced from each other by π/N_k . In this way, phase-shifted carrier PWM was fulfilled [31]. DEWE-2600 data acquisition instrument was used to record the waveforms of the inverter phase voltages (u_{ag}, u_{bg} , and u_{cg}); the dc voltages of the cells A5, B5 and C5; and the load currents (i_a, i_b , and i_c). The sampling frequency was 1 MHz. A low-pass filter with a cut-off frequency of 300 kHz was used in the DEWE-2600. Important parameters are listed in Table II.

The method in [22], namely generating u_0 by (12), was used as the representative of conventional methods. This method was compared with the proposed OC-ZS control method. The experiments included two parts: Experiment A and Experiment B. Experiment A was performed without any load, so the dc voltage was not affected by load currents. Thus, the output voltages produced by the two methods could be compared under the same condition of U_{dck} . In Experiment A, we evaluated the ability of the two methods in terms of suppressing BFRP according to the fundamental component of the zero-sequence voltage. Experiment B was performed with the inverter driving a load, as shown in Fig. 1. In this experiment, we compared the

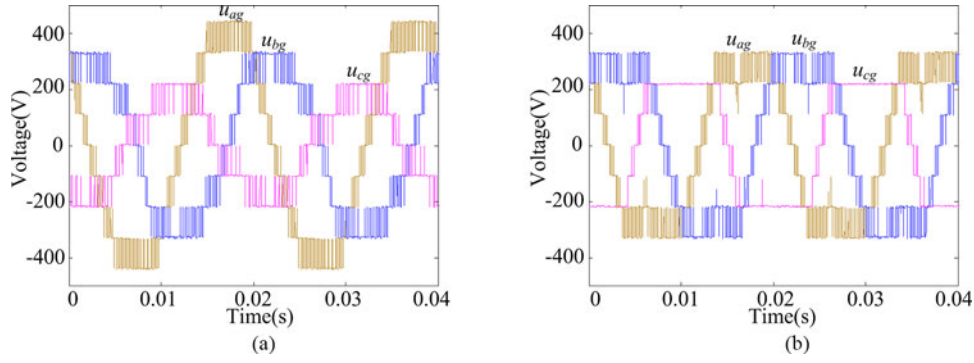


Fig. 12. Inverter phase voltages in Experiment A. (a) Conventional method. (b) Proposed method.

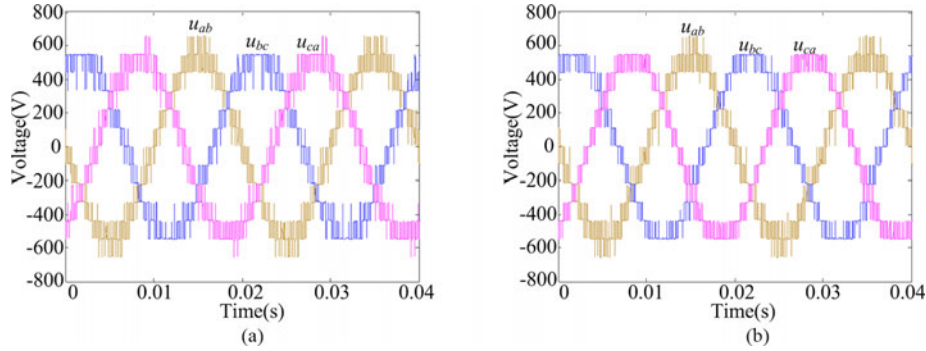


Fig. 13. Line-to-line voltages in Experiment A. (a) Conventional method. (b) Proposed method.

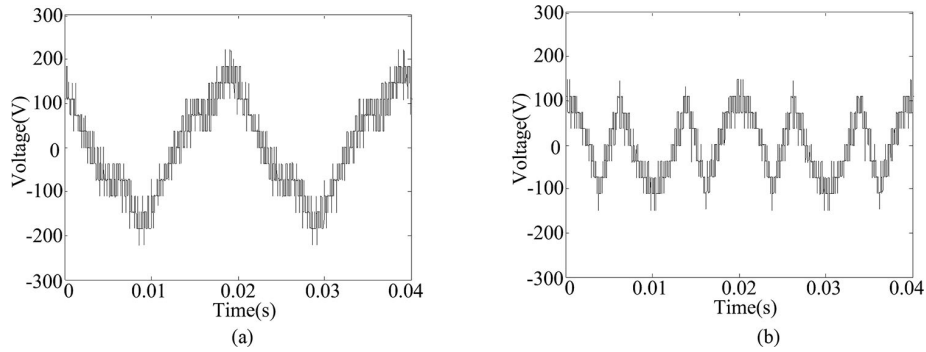


Fig. 14. Zero-sequence voltages in Experiment A. (a) Conventional method. (b) Proposed method.

practical effects of the two methods on suppressing BFRP by observing the rise of the dc voltages.

In the experiments, $(N_a, N_b, N_c) = (5, 3, 2)$. In phase a , no cells were bypassed; in phase b , cells B1 and B2 were bypassed; in phase c , cells C1, C2, and C3 were bypassed.

A. Experiment A

In this experiment, there were no loads and the dc voltage of each power cell was 109.6 V. Therefore, $U_{dc,min} = N_c \times 109.6V = 219.2V$ and $U_{dc,mid} = N_b \times 109.6V = 328.8V$. According to (11), $U_{MAX} = 316.4V$. Thus, we set $U_{m,phase}$ to 316.4 V to see if the two methods can achieve the maximum available output voltage.

At the beginning of Experiment A, the conventional method was used to generate u_0 . Then, the proposed method was used. In the process, the inverter phase voltages were recorded by the DEWE-2600. The recorded data were imported into MATLAB. The line-to-line voltages were the differences between the inverter phase voltages, and the zero-sequence voltages could be calculated from $u_0 = (u_{ag} + u_{bg} + u_{cg})/3$. Figs. 12–14 show their waveforms.

Through FFT analysis of the waveforms, the amplitude and phase values of the fundamental components in these waveforms were obtained. According to these values, two phasor diagrams were plotted in Fig. 15. It can be seen that both methods achieved line-to-line voltages with an amplitude near $\sqrt{3}U_{MAX} = 548$ V, namely the maximum amplitude of the line-to-line voltage that

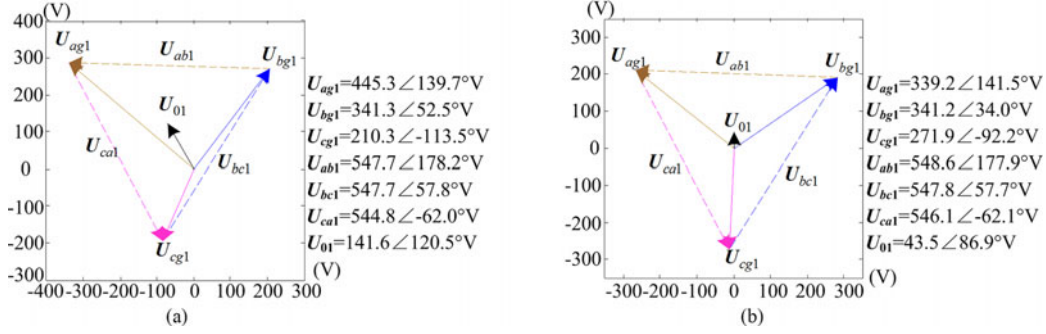


Fig. 15. Phasor diagrams of fundamental components of inverter phase voltages, line-to-line voltages, and zero-sequence voltages in Experiment A. (a) Conventional method. (b) Proposed method.

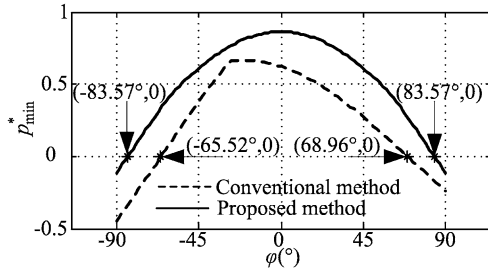


Fig. 16. $p_{\min}^* - \varphi$ curves for the two methods in Experiment A.

can be achieved by linear modulation. This indicates that there are no great differences between the two methods in terms of the output voltage capability.

According to (1), (14), and the data listed in Fig. 15, φ_0 can be calculated from

$$\varphi_0 = \varphi_{uab} - 30^\circ - \varphi_{u0} \quad (44)$$

where φ_{uab} is the phase angle of U_{ab1} ; φ_{u0} is the phase angle of U_{01} . Substituting the values of φ_{uab} and φ_{u0} in Fig. 15 into (44), we have $\varphi_0 = 27.7^\circ$ for the conventional method and $\varphi_0 = 60^\circ$ for the proposed method. Substituting the amplitudes of U_{01} in Fig. 15 and $U_{m,\text{phase}} = 316.4$ into (17), we have $U_{m01}^* = 0.4475$ for the conventional method and $U_{m01}^* = 0.1375$ for the proposed method. Finally, according to (18), (19), $p_{\min}^* - \varphi$ curves for the conventional method and the proposed method are plotted in Fig. 16.

For the proposed method, the range of φ satisfying $p_{\min}^* \geq 0$ is $[-83.57^\circ, 83.57^\circ]$; for the conventional method, the range is $[-65.52^\circ, 68.96^\circ]$. Obviously, the allowable range of φ for the proposed method is wider than that for the conventional method.

B. Experiment B

In this experiment, a load was connected with the output terminals of the inverter, as shown in Fig. 1. The values of R and L are listed in Table II. The load PF angle was 81.27° . Under such conditions, according to the conclusions in Experiment A, the conventional method will lead to BFRP but the proposed method will not. The dc voltage of each power cell was 107.8 V . Therefore, $U_{\text{dc},\text{min}} = N_c \times 107.8 \text{ V} = 215.6 \text{ V}$

and $U_{\text{dc},\text{mid}} = N_b \times 107.8 \text{ V} = 323.4 \text{ V}$. According to (11), $U_{\text{MAX}} = 311.2 \text{ V}$. Thus, we set $U_{m,\text{phase}}$ to 311.2 V .

First, the proposed method was used to generate u_0 . Then at time = 1.8 s , the proposed method was dismissed and the conventional method was used. At time = 2.0 s , the conventional method was dismissed and the proposed method was used again. In the process, the inverter phase voltages, the load currents, and the dc voltages of A5, B5, and C5 were recorded by the DEWE-2600. The recorded data were imported into MATLAB and the line-to-line voltages were calculated according to the inverter phase voltages. Fig. 17 shows their waveforms.

It can be seen from Fig. 17 that, once the conventional method was used, the dc voltage of B5 rose drastically. This indicates that the real power flowed back. After the proposed method was used, the dc voltage of B5 recovered gradually. This indicates that the BFRP was suppressed by the proposed method.

Figs. 18–21 show the enlarged waveforms when the conventional method was used and when the proposed method was used. Through FFT analysis of these waveforms, the amplitude and phase values of the fundamental components in these waveforms were obtained. According to these values, two phasor diagrams were plotted in Fig. 22. It can be seen that, when the conventional method was used, the angle between the phasor of the b -phase inverter voltage (denoted by U_{bg1}) and the phasor of the b -phase load current (denoted by I_{bg1}) was 103.9° , which was greater than 90° ; and that when the proposed method was used, the angles between the phasors of the inverter phase voltages and the phasors of the currents in corresponding phases were all less than 90° .

The experimental results suggest that, when $U_{m,\text{phase}} = U_{\text{MAX}}$, the allowable range of φ for the proposed method is larger than $[-81.27^\circ, 81.27^\circ]$, which is listed in Table I. On the other hand, the results also show that the allowable range of φ for the conventional method is less than $[-81.27^\circ, 81.27^\circ]$ at least when $U_{m,\text{phase}} = U_{\text{MAX}}$. When $U_{m,\text{phase}} < U_{\text{MAX}}$, the region between the waveforms of $u_{\text{max},s}$ and $u_{\text{min},s}$ will be wider, so OC-ZS control can suppress the fundamental component of $u_{0,\text{OC-ZS}}$ more effectively. The allowable range of φ for the proposed method will also be wider. Therefore, the CRPA of the proposed method is wider than that of the conventional method.

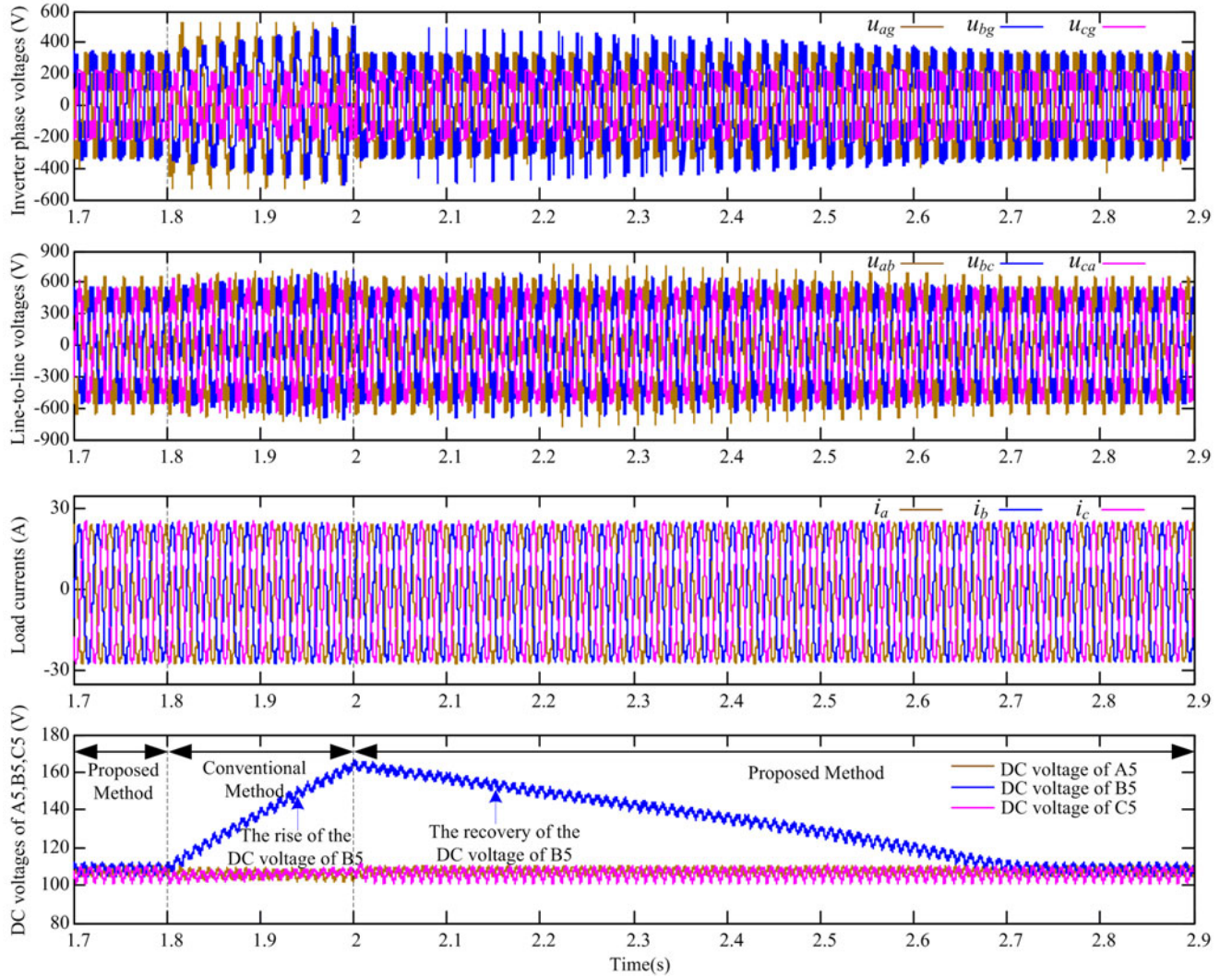


Fig. 17. Global waveforms of inverter phase voltages, line-to-line voltages, load currents, and the dc voltages of A5, B5, and C5 in Experiment B.

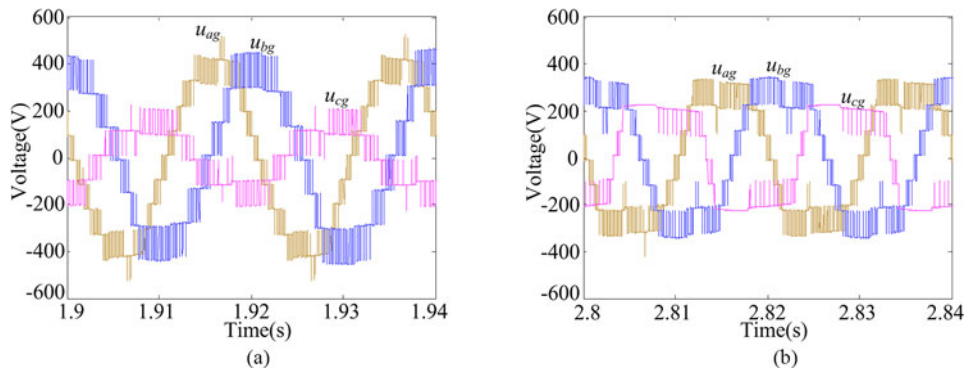


Fig. 18. Enlarged waveforms of the inverter phase voltages in Experiment B. (a) Conventional method. (b) Proposed method.

The two experiments above were performed when $(N_{max}, N_{mid}, N_{min}) = (5, 3, 2)$. However, in the other cases listed in Table I (apart from the cases when $N_{min} = 0$), it can also be verified by similar experiments or simulations that the CRPA of the proposed method is wider than that of the

conventional method. When $N_{min} = 0$, the CRPA of either the proposed method or the conventional method is $[-60^\circ, 60^\circ]$. Therefore, in general, the proposed method has an obvious advantage over the conventional method in terms of suppressing BFRP.

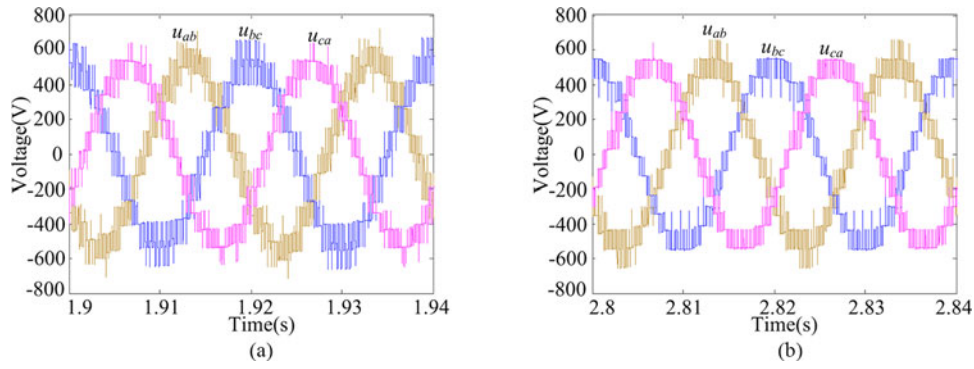


Fig. 19. Enlarged waveforms of the line-to-line voltages in Experiment B. (a) Conventional method. (b) Proposed method.

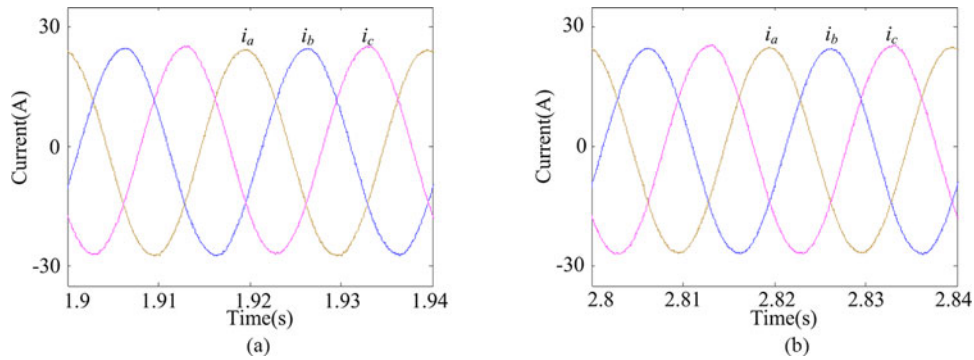


Fig. 20. Enlarged waveforms of the load currents in Experiment B. (a) Conventional method. (b) Proposed method.

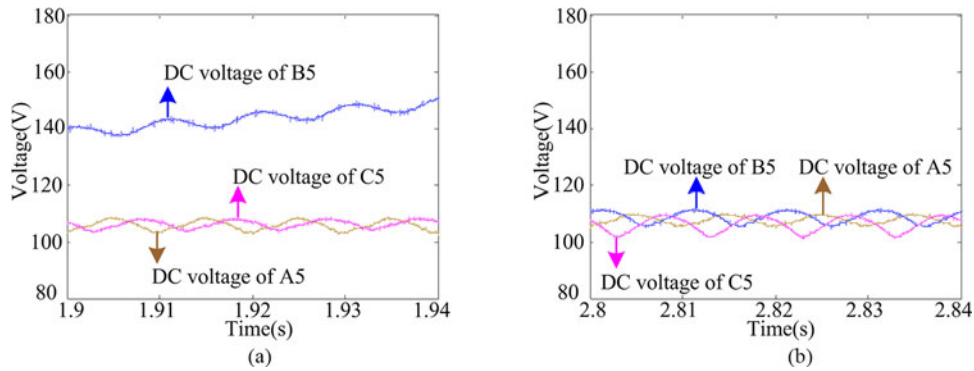


Fig. 21. Enlarged waveforms of the dc voltages of A5, B5, and C5 in Experiment B. (a) Conventional method. (b) Proposed method.

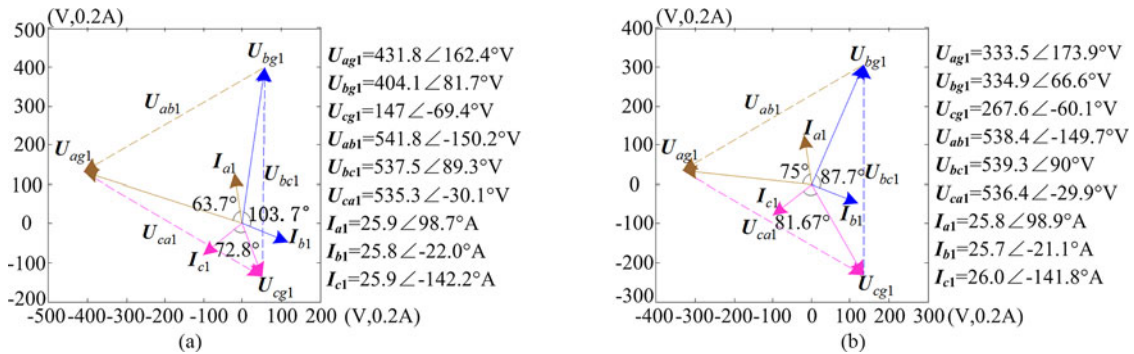


Fig. 22. Phasor diagrams of fundamental components of inverter phase voltages, line-to-line voltages, and load currents in Experiment B. (a) Conventional method. (b) Proposed method.

V. CONCLUSION

This paper has made a study of the fault-tolerant control methods based on zero-sequence voltage injection for CHB inverters. To solve the problem of BFRP, to which little attention has been paid so far, we propose a new method called OC-ZS control. The method extends the allowable range of load PF angle without degrading the output voltage capability of the inverter. Compared with the conventional methods, the proposed method enables the CHB inverter to operate in the fault-tolerant mode under a lower load PF without inducing BFRP. As a result, the dc voltage is less likely to rise to an intolerable level and the inverter becomes more reliable.

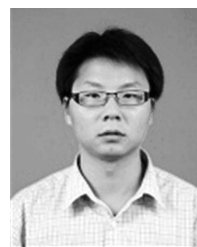
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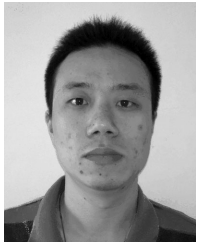
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