

# DC Microgrids—Part II: A Review of Power Architectures, Applications, and Standardization Issues

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**Abstract**—DC microgrids (MGs) have been gaining a continually increasing interest over the past couple of years both in academia and industry. The advantages of dc distribution when compared to its ac counterpart are well known. The most important ones include higher reliability and efficiency, simpler control and natural interface with renewable energy sources, and electronic loads and energy storage systems. With rapid emergence of these components in modern power systems, the importance of dc in today's society is gradually being brought to a whole new level. A broad class of traditional dc distribution applications, such as traction, telecom, vehicular, and distributed power systems can be classified under dc MG framework and ongoing development, and expansion of the field is largely influenced by concepts used over there. This paper aims first to shed light on the practical design aspects of dc MG technology concerning typical power hardware topologies and their suitability for different emerging smart grid applications. Then, an overview of the state of the art in dc MG protection and grounding is provided. Owing to the fact that there is no zero-current crossing, an arc that appears upon breaking dc current cannot be extinguished naturally, making the protection of dc MGs a challenging problem. In relation with this, a comprehensive overview of protection schemes, which discusses both design of practical protective devices and their integration into overall protection systems, is provided. Closely coupled with protection, conflicting grounding objectives, e.g., minimization of stray current and common-mode voltage, are explained and several practical solutions are presented. Also, standardization efforts for dc systems are addressed. Finally, concluding remarks and important future research directions are pointed out.

**Index Terms**—DC microgrid (MG), power architectures, protection and grounding, standardization.

## I. INTRODUCTION

**P**ROLIFERATION of renewable energy sources (RES), together with ever more electronic loads and electric vehicles (EVs) in modern power networks has prompted an idea of considering the application of low-voltage dc distribution to areas far beyond the traditional dc distributed power systems (DPSs) [1]–[10], or power supplies for telecom stations

[11]–[13], data centers [14], [15], and vehicular electric power systems [16], [17]. Accompanied with technological advancements in power electronics and improvements in computational power of real-time controllers, dc systems of today are capable of achieving much broader functions than simple voltage regulation. Hence, they are becoming an increasingly popular solution for many types of residential and industrial applications, such as dc-powered homes [18]–[22], fast EV charging stations [23]–[29], hybrid energy storage systems (ESSs) [30]–[36], and renewable energy parks [37]–[43].

With regard to traditional ac, this kind of distribution has proved to offer more efficient and reliable energy transfer (see [44] for efficiency comparison of various architectures used in data centers). Calling on to this and other benefits, such as circumvention of problems with harmonics, unbalances, synchronization, and reactive power flows, different types of dc power distribution systems are experiencing more and more widespread use. Since extensive low- and medium-voltage dc distribution network infrastructure still does not exist, individual dc systems are nowadays either operated autonomously or connected to ac utility mains. Therefore, it is convenient to classify them as dc microgrids (MGs) [19]. This research topic has recently spurred a great amount of interest in research community [20], [45]–[55]. However, although dc MG appears to be a novel technology, it is essentially stemming from a hybrid mixture of several existing and ongoing research fields; one group being already mentioned traditional dc applications and the other being the ac MGs area.

MG concept was originally proposed in 2002, as a building block of future low-voltage distribution systems which were experiencing revolutionary changes at the time due to market liberalization and introduction of distributed generation (DG) [56], [57]. It was based on the idea of aggregating number of microsources and loads into one unique entity, which could be interpreted as a single dispatchable prosumer from the overhead power system viewpoint. AC MG research has been very dynamic over the course of following decade for MGs have soon after their introduction been recognized as a key element for integrating DG and storage, while allowing power independence from utility mains at the same time. From power electronics standing point, the roots of their development stem from control of paralleled inverters in uninterruptible power supplies (UPSs) [58]–[62].

Simultaneously with the fast development in ac MG arena, Ito *et al.* reported one of the first dc MG experimental prototypes

Manuscript received March 27, 2015; revised June 14, 2015; accepted July 28, 2015. Date of publication August 4, 2015; date of current version December 10, 2015. The work of X. Lu was supported by the U.S. Department of Energy (DOE) Office of Electricity Delivery and Energy Reliability. Recommended for publication by Associate Editor T.-F. Wu.

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Digital Object Identifier 10.1109/TPEL.2015.2464277

of 10 kW in 2004, claiming for simpler control, while superior efficiency and reliability with regard to its ac counterpart [63].

Later on, several works followed up on it and demonstrated usage of flexible dc MGs for telecom systems [64], distributed renewable and storage plants [65], data centers [66], and residential households [67]. In the next years, accompanied by the rapid emergence of consumer electronics, integration of ever more RESs, and expansion of EV market with associated charging infrastructure, dc MGs have firmly established as a research field itself.

Driven by practical requirements of specific dc MG applications, a number of hardware topologies have been proposed. Principal criteria in that sense were control flexibility, robustness, and reliability. As they are often opposed to each other, different tradeoffs need to be taken into account before deciding the most suitable configuration for a given application. For its extreme robustness, direct connection of the battery stack to the common dc bus has been historically a popular structure for practical industrial applications. Some typical examples are power supplies for remote telecom stations [68] and rural-based households [69]. Due to high capacitance of the battery stack, dynamic stability of these kinds of systems is inherently assured [70]. Therefore, control design of converters that are used to interface different sources is not a critical issue from the bandwidth point of view, and thus, standardized components available in the market can be readily used. On the other hand, by interconnecting battery through power electronics converter interface, flexibility of the system is greatly increased since the voltage can then be actively regulated, rather than leaving it dependent on the battery operating conditions. Picking up on this basic structure, a number of alternatives have been proposed, including bipolar type dc bus [20], SST-enabled dc MGs [51], [71], [72], redundant dc bus structures [54], [73], and multiple dc MG clusters [19], [74].

For safe and reliable operation of dc MG, a well-functioning protection system is instrumental in any topology. Its principal objective is to minimize the propagation of disturbances by detecting and isolating faults within the minimum time frame [75], [76]. Protection of dc systems is in general a challenging task due to difficulties in extinguishing arc, which on the contrary happens naturally in ac systems. Accurate short-circuit current calculation and fault detection are the most important prerequisites for the good design of protection system [45].

Moreover, an assessment of the influence of realistic protection devices and grounding methods on the total system performance becomes critical when deploying a protection system in real-world environment. Up to this point, protection of dc MGs was designed based on technologies and strategies taken over from existing matured solutions developed for auxiliary dc systems in big power plants and traction power systems [77]. Recently, however, a number of new coordinated protection strategies have been proposed [78]–[84]. Apart from protection systems, standardization of future dc systems regarding dc voltage levels, installation design, maintenance practices, and instrumentation and control recommendations are at present moment the most important barriers to wide-scale adoption. Organiza-

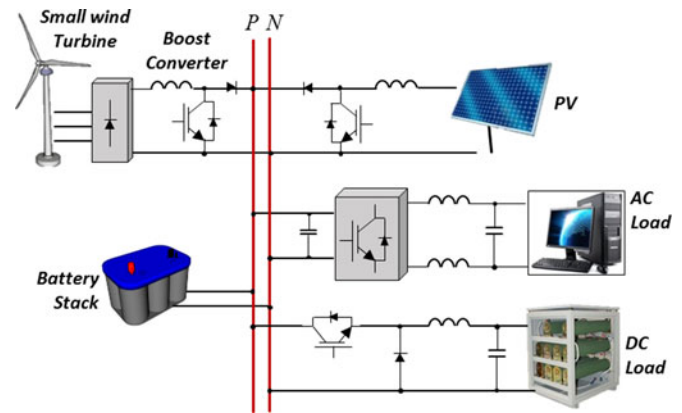


Fig. 1. Single bus dc MG with ESS directly connected to the common bus.

tions such as EMerge Alliance, IEC, and IEEE are engaged in developing voltage and performance standards.

This paper is not only motivated by the recent outburst of publications in this area, but also by an immense amount of their real-world residential and industrial deployments [69], [85]–[88]. It is intended to supplement its first part which is more focused on control issues, stability analysis, and stabilization techniques for dc MGs. The organization of this paper is as follows. In Section II, a configuration of the dc MG system is presented and various circuit topologies are shown. Section III points out four practical dc MG applications and presents critical discussion about particular performance requirements for each one of them. Applicability of different architectures described beforehand and control strategies presented in the first part of this paper are also evaluated. Section IV sheds light on the current status of protection for dc MGs. For that matter, discussion about possible types of faults is followed by a description of prominent protection devices and grounding methods, and also their integration into overall coordinated protection strategies.

Overview of available dc standards and those under development is given in Section V. Finally, Section VI presents the concluding remarks.

## II. DC MG SYSTEM OVERVIEW

Typical dc MG systems are reviewed here. There are a number of topologies that have been reported in the literature and some of their most prominent representatives are assessed in this section. In general, the topologies of dc MG can be classified into three categories, i.e., single-bus topology, multibus topology, and reconfigurable topology, respectively.

### A. Single-Bus Topology

Single-bus topology is commonly used for dc MGs, and it can be regarded as the basis of multibus systems. Fig. 1 shows an exemplary schematic diagram of the most common type of dc MG, which is frequently deployed in practical industrial applications. It is based on the singular dc bus to which ESS, typically an electrochemical battery stack, is directly connected [89]–[92]. The

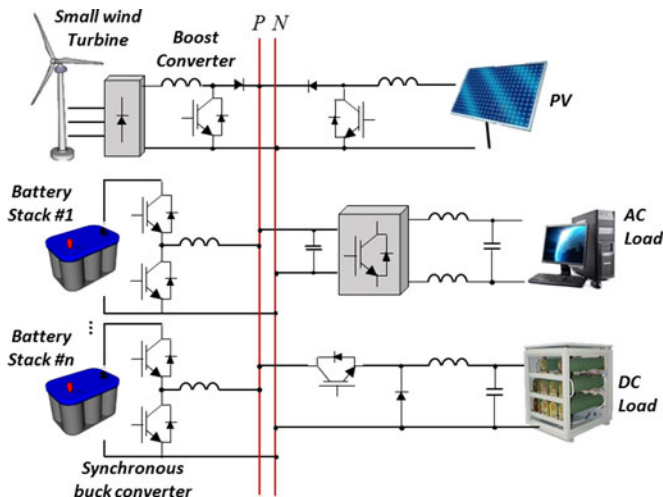


Fig. 2. Single bus dc MG with ESS connected to the common dc bus through dedicated converter.

number of series battery cells is determined depending on the voltage required by loads. Electrical power supply systems for telecom applications have been historically using this configuration operating at 48 V [93]. Many types of converters that can be seamlessly connected to the bus operating around this nominal voltage are available in the market (e.g., [94]). However, despite inherent dynamic stability of the system, an uncontrollable voltage in the common dc bus, which depends mostly on the state of charge (SoC) and current of the battery, limits its application only to dense, singular bus systems [70]. Apart from that, it suffers from the practical problem of unregulated battery charging as it needs to be performed coordinately by a number of paralleled converters that have inherent imperfections in bus voltage measurement. This causes a circulating current problem that leads to uneven loading of those converters [4], [95]–[97], and also to accelerated wear and tear of stationary batteries [70]. On the other side, connection of the ESS through dedicated converter interface to a regulated low-voltage dc bus (see Fig. 2) allows application of more flexible control and possibility of connecting multiple buses for enhancing the reliability of the system or to supply loads in a wider area.

Moreover, the reliability of this system can be increased by using multiple battery stacks. This topology, operated at low voltage, has by now been the most widely studied in the literature [15], [18], [46], [48]–[50], [55], [98]–[103]. However, its attractive features are partially counterbalanced with several associated technical problems. First, there now appears a need for careful design of circuit and control parameters due to a fact that equivalent capacitance of dc bus gets much smaller than in case of direct battery connection. Second, there exists only one dc bus, compelling the consumers to be powered from it.

As a practical remedy to the aforementioned problem, several alternative concepts have been examined. Kakigano *et al.* proposed a bipolar single regulated bus structure (see Fig. 3), that operates at  $\pm 170$  V [20]. The main benefit of this configuration is a fact that load-side dc–dc converters have an option to choose

the source voltages between 340, +170, and  $-170$  V. Therefore, the reliability of the system is increased since in case of failure of one line, it is still possible to supply power by the other two lines and an auxiliary converter.

### B. Multibus Topology

Besides the single-bus topologies, dc MGs can be extended to multibus configurations aiming at higher availability and reliability. An interesting topological modification for enhancing the reliability of the system in that sense was proposed by Balog and Krein [54]. It is based on the automated hot-swap principle between more buses using the method of auctioneering diodes. In that particular work, the case of two redundant buses is analyzed. The location of intervention is on the load side, where critical loads automatically select the bus to be supplied from based on higher voltage. The structure of this system is shown in Fig. 4. Similar strategy for selecting the most appropriate supply bus is presented in [73], but it uses a game-theoretic approach. In that case, loads are more flexible to define the way of selecting desired bus, and several objective functions are tested for that matter. Besides, simultaneous supply from multiple buses is possible in order to enhance the total efficiency of the system.

Multiple dc MG cluster configuration (see Fig. 5) is an alternative redundant solution [19], [74], [104]. In this way, every MG is able to absorb or inject power from its neighboring MGs in case of shortage or surplus of power, respectively. Additionally, depending on the configuration in which the MGs are connected, some corrupted buses can be automatically isolated from the system in case of failure. Power exchanges between multiple dc buses are regulated by imposing appropriate local voltage deviations. However, by dint of digital communication technologies, total average voltage can be regulated to a nominal value [74].

Low-voltage dc distribution systems can be interfaced to medium-voltage ac utility mains through SST, as shown in Fig. 6. With connection of a number of SSTs to medium-voltage ac network, it is envisioned that the energy management in lower voltage levels will be completely in SST domain, largely simplifying the task of system operator in the ac grid above SST [72]. However, although it is widely deemed that SSTs will bring a new revolution to future distribution systems, serving as the true enablers of dc distribution architecture, they are still in the early development stage [105]. Recognizing its potential, many academic and industrial projects around the world, such as FREEDM [106], UNIFLEX [107], and HEART [108] are currently underway, focusing on pushing this technology to the next technical readiness level. Speed of its further evolution will to a large extent determine the significance of the role of dc distribution in future power systems. It is expected that previous advances in dc MGs area will play an instrumental role in realizing the SST scenario [51].

### C. Reconfigurable Topology

Besides the aforementioned architectures of dc MG, reconfigurable topologies were also proposed for the purpose of

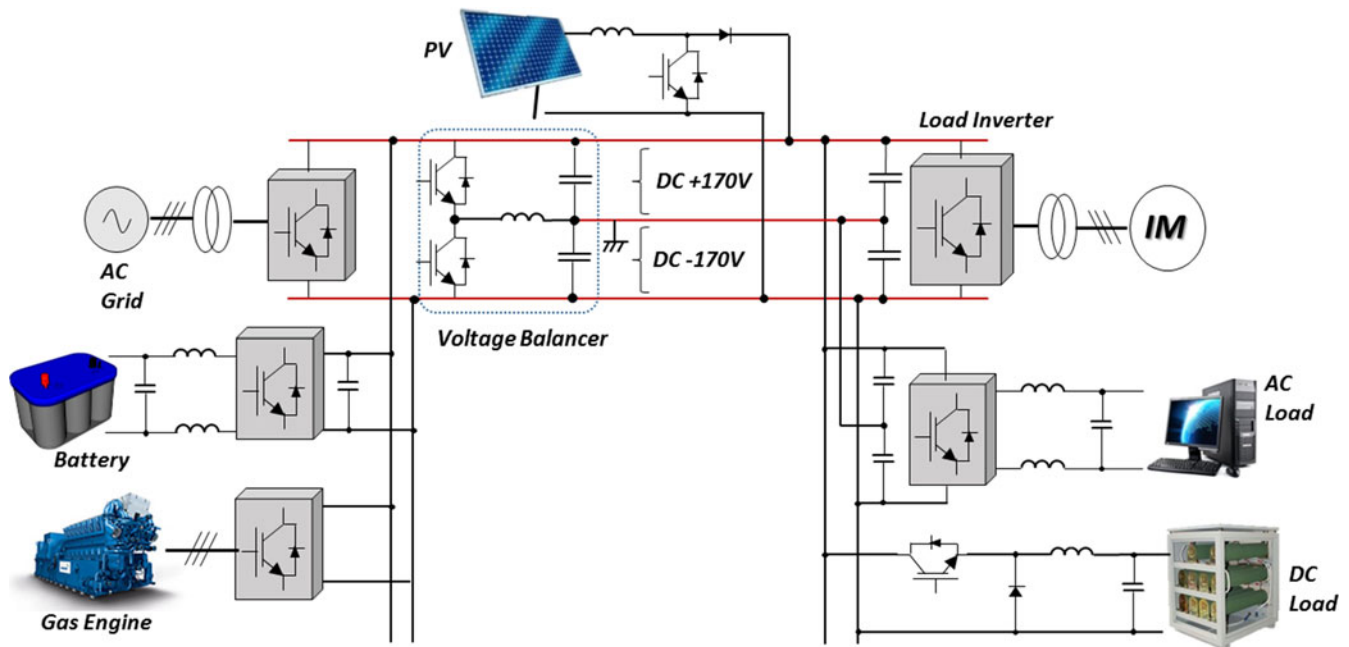


Fig. 3. Bipolar single bus dc MG.

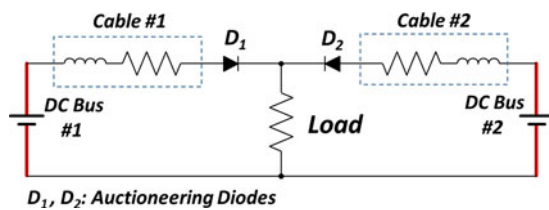


Fig. 4. Dual-bus, separately fed dc MG with a bus selector at each load.

increased flexibility during the faults or periodic equipment maintenance periods. Fig. 7 shows a dc ring bus architecture [81]. Each node and the link between neighboring nodes are controlled by intelligent electronic devices (IEDs). The main merit of this configuration is high reliability and redundant operation. Since the load connected to the common dc bus can be fed bidirectionally, alternative path is provided at the ring bus in case of fault. When encountering a fault in this dc MG, the fault is isolated by IEDs first, and then the load power turns to be supplied by the sources in the normal alternative path.

Another dc MG architecture that features high reliability is based on zonal configuration, as shown in Fig. 8 [75]. It is seen that this type of dc MG is comprised of multiple dc distribution units, with each one of them being connected in series to form a zonal architecture. Note that each dc distribution unit in the zonal configuration has two dc buses. These two buses form the redundant configuration and the required load power can be flexibly obtained by either one of them. In particular, assuming that the load power is supplied by the upper bus, in case of its failure the switches at the upper side are turned OFF and the switches at the lower side are turned ON to change the source bus and ensure the normal operation of the load. Meanwhile, since

each dc distribution unit in the zonal architecture is powered by the utility grid separately, fault can be isolated within each unit without influence on the operation of other parts of the zonal system. This topology is similar to the redundant bus system proposed in [54], but it has better flexibility due to higher number of switches.

Multiterminal dc (MTDC) system is proposed in [109]–[111], as shown in Fig. 9. It should be noted that the MTDC architecture was originally proposed for high-voltage dc (HVdc) systems, e.g., off-shore wind farms. In this configuration, the dc distribution network is fed by multiple input terminals. The system usually features mesh configuration. Compared to conventional radial configuration, power flow in mesh configuration is more complicated. However, it can be also more flexible since the system connection provides multiple paths for power transmission.

Table I sums up the hardware topologies that have been presented above and gives critical comparisons from different points of view. The voltage levels of each configuration reported so far in the literature are listed. Some of the configurations can be implemented by using standardized off-the-shelf components, such as industrial inverters. However, for the others, specialized components are required. For example, dc voltage balancers are needed for the configuration with bipolar regulated bus, while SSTs are required for SST-enabled MGs. IEDs should be used for reconfigurable dc MG architectures. Considering the impact of ESS, its type of connection to the dc MG is also summarized. In relation with this, it is shown that configurations which can incorporate directly connected ESS have inherent stability. Final two columns of the table present two performance metrics of each configuration, i.e., expandability to multiple buses and reliability.

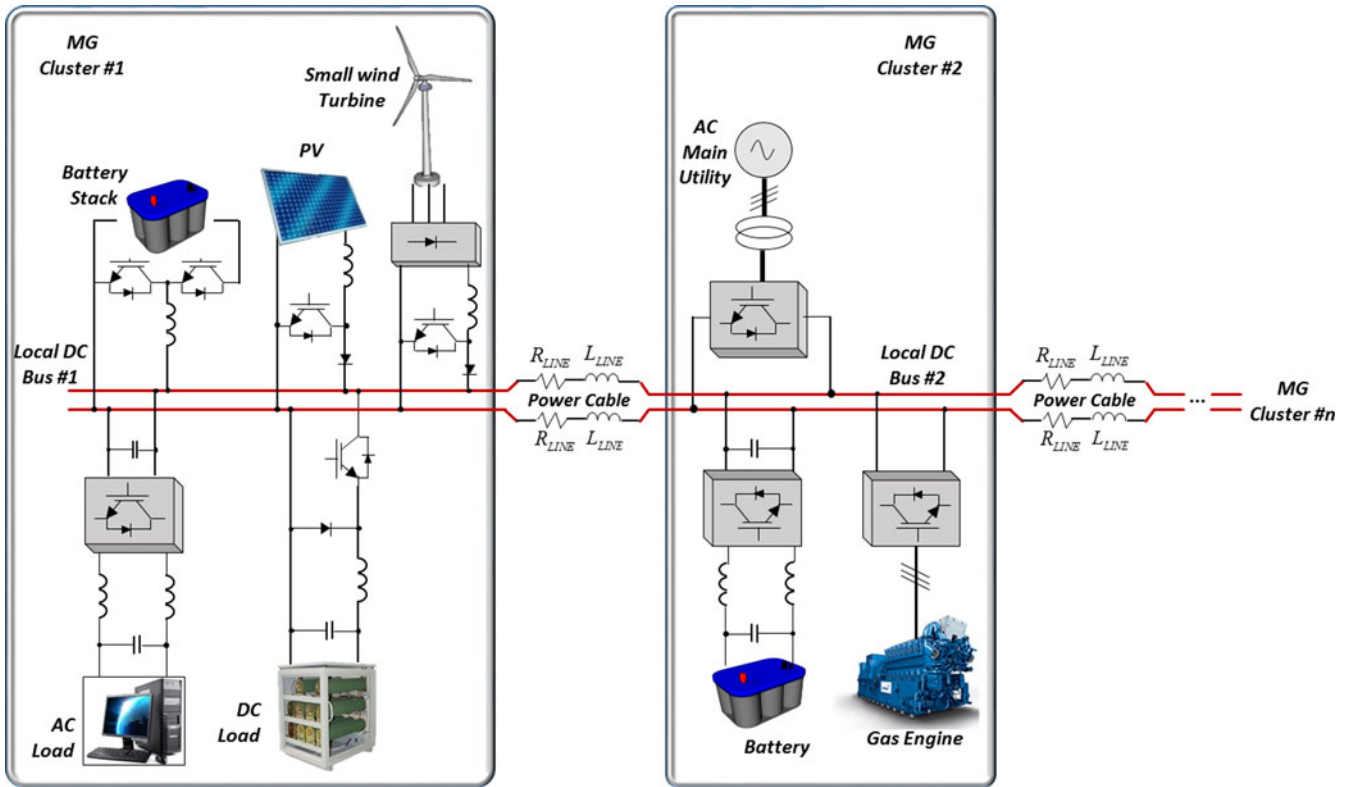


Fig. 5. Multiple bus dc MG with all components connected through dedicated converter interfaces.

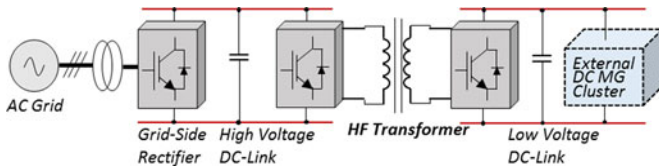


Fig. 6. SST-enabled dc MG.

#### D. Interconnection Between DC MG and AC Grids

In order to ensure the power availability and reliability, dc MGs can be interconnected with existing ac infrastructures through dc–ac converter interface. In that way, either an ac grid connection or a hybrid ac/dc MG can be established. In any case, dc link is typically used for efficient local coupling of dc-type components. The interface toward ac grid is reflected in most architectures presented in previous sections and can be achieved by unidirectional or bidirectional dc–ac converters. In case of a hybrid MG, dc MG can be regulated to absorb power from the ac MG when it has surplus of power. On the other hand, when load requirements exceed the available power, dc MG can deliver it back to the ac side (only if bidirectional interface is used).

Focusing on the power architectures of the interconnecting energy conversion systems between dc MGs and ac grids, some work has been done in the existing literature. In general, the interconnecting system can be classified into three categories, i.e., single, parallel, and aggregated converter system.

The single converter to link the dc MG and ac grid is usually used in small-scale systems. In [112], a PV-wind-battery hybrid MG is taken as an example to represent the ac and dc hybrid MG. The PV panel and battery form the dc bus, and the wind turbine and ac utility grid form the ac bus. Since the power capacity of the above system is typically not so large, a single bidirectional converter is used to link the dc subgrid and ac subgrid. In [113], an integrated configuration is employed with one ac–dc–ac converter as the energy hub to connect both ac and dc subgrids. In particular, the ac MG and ac utility grid are connected to the two ac sides of the ac–dc–ac converter, respectively. Meanwhile, the dc link of the converter is connected to the dc MG. Systematic power flow management among the ac utility grid, ac MG, and dc MG is designed to achieve the coordinated control.

In order to increase the exchanging power capability between dc MGs and ac grids, parallel interface converters can be used between the ac and dc bus. In [114], parallel converters between ac and dc bus are analyzed in detail. The model of the parallel converters is derived and the control system is designed to suppress the zero-sequence circulation current. In [115], parallel bidirectional interlinking inverters are used to connect the dc subgrids and ac subgrids. An improved droop control method based on per-unit frequency and amplitude is employed to enhance the power sharing among different converters. Meanwhile, it is pointed out that the proposed method also applies for different architectures with both ac and dc subgrids. In [116], different operation modes are studied and the one with ac to dc power flow is discussed in detail. A hierarchical control system

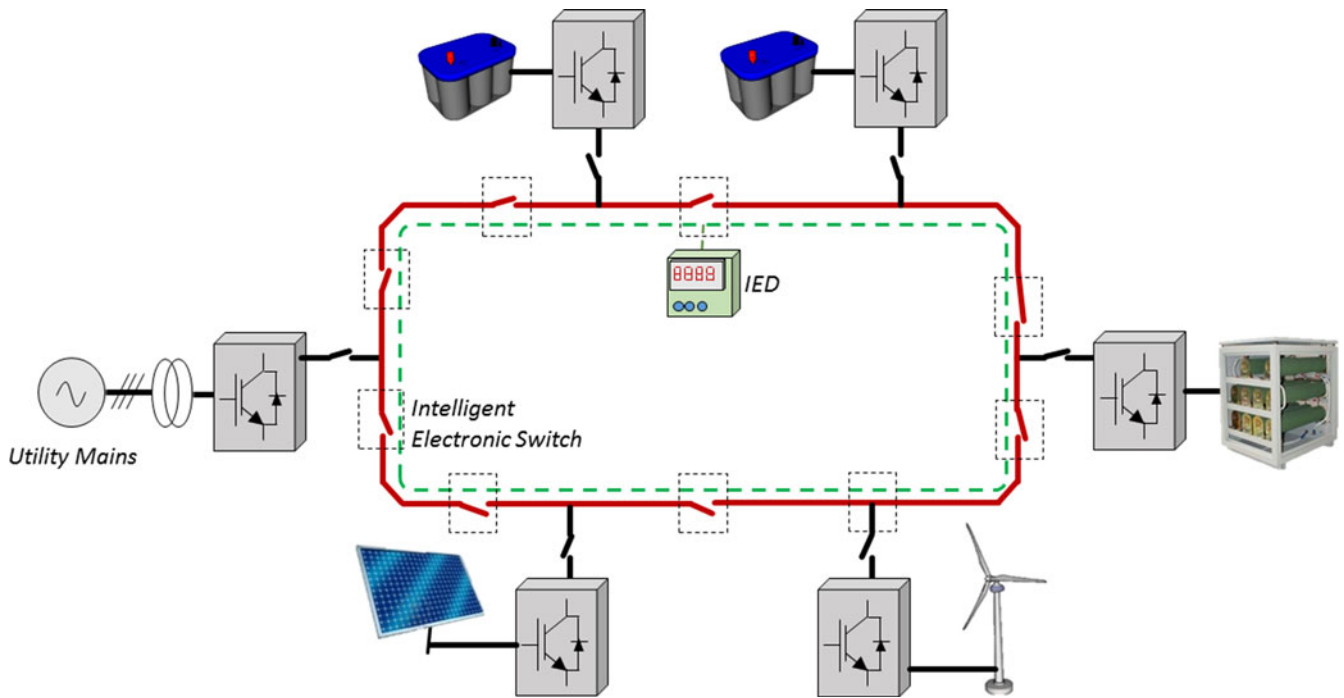


Fig. 7. DC MG based on ring bus.

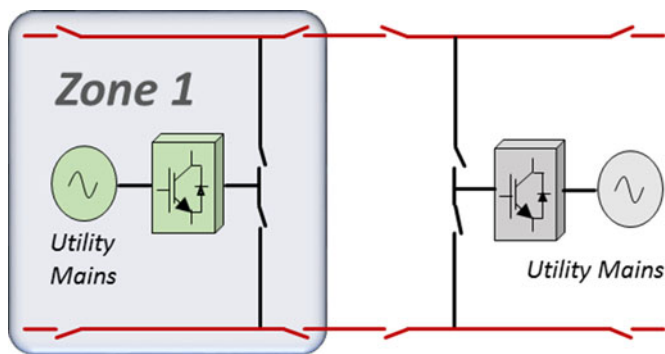


Fig. 8. Zonal dc MG.

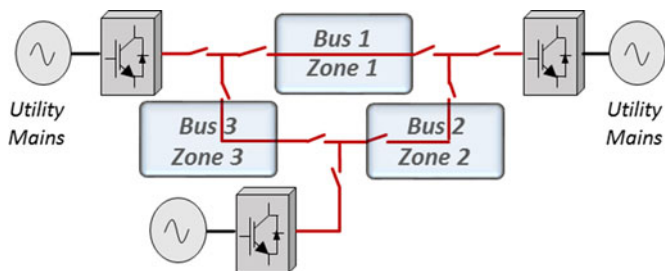


Fig. 9. MTDC MG.

is used to ensure the reliable operation of the whole ac and dc hybrid MG.

Apart from the configurations with single and parallel interface converters, the aggregated configuration is also employed

for ac and dc hybrid MGs. In [117], a hierarchical architecture is proposed to aggregate the dc MG with ac MG and ac utility grid. In Level 1, which is a microsource level, dc MGs are formed to integrate the DGs and energy storage units. In Level 2, combo-source level, each dc MG is connected to an ac link by using dc–ac bidirectional inverters. In Level 3, MG level, several ac-coupled combo sources are aggregated to form a MG and to achieve the functions of optimization, synchronization, dispatching, protection, etc. Finally, each MG in Level 3 is connected to the ac utility grid, respectively. In [118], the sources and loads in an ac and dc hybrid MGs are categorized into three groups, i.e., DG park, dc MG, isolated ac MG, and ac utility interface. An admittance-based model is derived and the overall system is thereby studied from the dynamic stability point of view. Considering the power architectures reviewed in Sections II-A–II-C, their capability of interconnecting with ac utility grid or ac MG depends primarily on the availability of high enough dc-link voltage level. For that matter, only in the single-bus dc MG with directly connected battery, there is no possibility of direct dc-link voltage control. However, it may still be connected to utility mains if an adequate number of battery cells are connected in series.

As a final remark, it should be noted that although the power architectures were presented in this section within the MG framework, they have a quite general structure. In that sense, the vast majority of them can be used in a very broad voltage and power range, given the proper selection of semiconductor devices and/or their arrangement within the multilevel converter structures. For that matter, a resemblance with typical architectures used in HVdc systems can be readily observed (see, i.e., [119]). In the respective reference, singular bus, bipolar bus,

TABLE I  
OVERVIEW OF HARDWARE TOPOLOGIES FOR DC MGs

DC Bus Configuration	Reported Voltage Levels (V)	Standardized Components	Direct ESS Connection	Inherent Stability	Expandability to Multiple Buses	Reliability
Direct battery connection	12, 24, 48	Yes	Yes	Yes	No	High
Single unipolar regulated bus	24, 48, 380	Yes	No	No	Yes	Medium
Bipolar regulated bus	$\pm 170$ , 340	No	No	No	Yes	Medium
Multiple regulated buses structure	48, 380	Yes	No	No	Yes	Medium
SST-enabled MG	380 and higher	No	Possible	If ESS directly connected	Yes	Medium
DC ring bus	Lab tests made at 24 V but suitable for higher	No	No	No	Yes	High
Zonal DC MG	380 or higher	Yes	Possible	If ESS directly connected	Possible	High
MTDC MG	380 or higher, and commonly used for HVdc systems	Yes	No	No	Yes	High

multiple bus, multiterminal, and ring bus structures have been shown to be used in HVdc systems up to levels of 350 kV. The exception is the topology presented in Fig. 1, which has a direct connection of the battery stack that is not typical for HVdc systems. Therefore, that topology is voltage limited by the battery, and the required voltage in that case is achieved by stacking up the number of low-voltage battery cells in series. However, it is not viable to increase the number of cells indefinitely due to problems associated with balancing of individual cells and reliability (if one cell fails, the whole stack will fail). To the best knowledge of the authors, the highest reported voltage of a battery stack by now is 6 kV [120].

The thorough discussion about the applications of each topology specifically for four emerging dc MG applications is provided in the following section. It presents the typical performance requirements and applicability of corresponding power architectures, together with the summary of suitable control strategies in that they can achieve the imposed objectives. Moreover, the typical voltage and power limitation limits for each application are given.

### III. APPLICATIONS OF DC MGs TO FUTURE SGs

Taking into account the advantages of dc MGs, their usage has been proposed for a number of different applications in future SG, with each one of them having its own specific requirements. In this section, a critical view on performance requirements of four modern dc MG applications is presented. They include high-efficiency households, renewable energy parks, hybrid ESSs, and EV fast charging stations, as shown in Fig. 10. While it should be acknowledged that the considered list is by no means complete, we have limited our discussion only to these novel applications of dc MGs. More details about some well-known and matured examples, such as DPSs [1]–[10], telecommunication [11]–[13], data centers [14], [15], vehicular electric power systems [16], [17], can be found in the existing literature.

Considering the specific characteristics of various power architectures that were reviewed in Section II of this paper together with control strategies that were reviewed in the first part of these two articles, a critical judgment in selecting the most suitable ones for every given application is provided below. Moreover, typical voltage and power limitations, as reported in the literature, are presented.

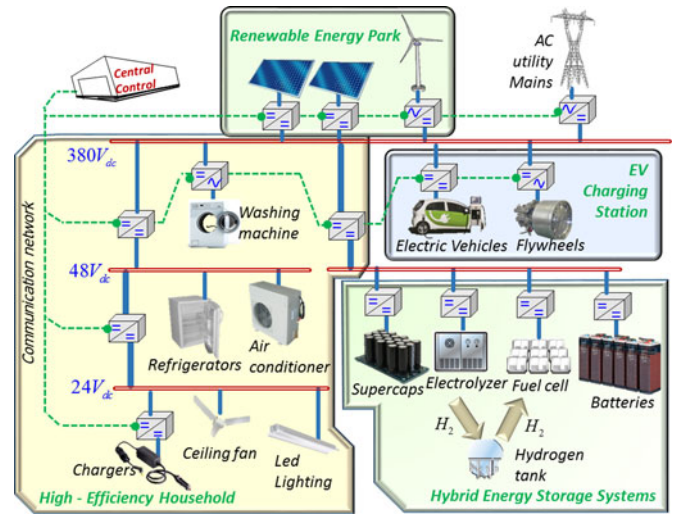


Fig. 10. Applications of dc MGs.

#### A. Performance Requirements for DC MG Applications

Desirable performance characteristics and functionalities of MGs in general were presented in [75], [121], and [122]. For completeness of this paper, they are summarized here by considering both the control and architectural perspectives. From control point of view, MG should be operated in a stable way and provide sufficient power quality at the point of common coupling (PCC). Moreover, it should provide functionalities, such as intelligent coordination of its internal units, as well as grid support capability when it is in grid-connected mode. These can be summarized as follows:

- 1) stability: sufficient stability margins during normal and transient operating modes;
- 2) power quality: satisfactorily voltage waveform in the common dc bus;
- 3) coordinated control: supervisory energy management system responsible for carrying out complex functions, such as economic dispatch, unit commitment, mode changing, efficiency optimization, and power flow control between internal clusters;
- 4) grid support capability: ability to provide ancillary services to the overhead ac power system.

TABLE II  
TYPICAL PERFORMANCE REQUIREMENTS FOR VARIOUS DC MG APPLICATIONS

DC MG Application		Hardware Architecture			Control Strategy	
		Redundant Power Supply	Flexibility	Scalability	Coordinated Control	Grid Support Capability
High-Efficiency Households	Importance Level	Low	Medium	High	High	Medium
	Description	Leads to higher installation cost.	Flexible clustering of households is a promising future concept.	Should continue working with new appliances integrated into household.	Efficiency and economic optimization in both single household and cluster should be achieved.	Households are conceived to support the grid indirectly—according to the price signal.
Renewable Energy Parks	Importance Level	Medium	Critical	Medium	High	Critical
	Description	Individual faulty unit can be bypassed so redundant power supply is not highly required.	Flexibility in terms of bypassing faulty elements should be guaranteed for fault ride through and occasional maintenance of RES generators.	Number of units within a branch can be defined considering the required voltage level but more branches can be added if the power rating of central inverter permits.	Required when there is a need to coordinately increase or reduce power production of multiple generators.	Required by grid codes for renewable energy systems.
Hybrid ESS	Importance Level	Medium	Low	High	High	High
	Description	Would not give too much benefit due to a fact that all units are typically connected to the same point.	Small and dense systems formed around a common dc bus. Probability of fault on dc bus is extremely low.	Plug and play of additional sources for expanding energy and/or power capacity is highly desirable.	Degradation of ESS and grid price signal can be integrated into cost function.	Important for grid support in some cases such as renewable energy parks or EV charging stations.
EV Fast Charging Stations	Importance Level	Medium	Medium	Medium	High	High
	Description	Too costly and not competitive with present commercial solutions.	In case of chargers aggregated in parking lots, reconfiguration and fault ride could increase reliability.	Plug and play capability of additional ESS will be a desirable feature when grid codes start to be imposed.	Optimization of charging strategies, charging scheduling, energy management between grid and ESS.	Grid codes are still not adopted, but will become important issue when they do.

On the other hand, from architecture point of view, functionalities that can be achieved through appropriate power architecture include redundancy, flexibility, and scalability. They can be summarized below:

- 1) redundancy: redundant power supply for the loads with minimized reliance on communication;
- 2) flexibility: possibility of reconfiguring the system online during faults and/or to form MG clusters;
- 3) scalability: possibility to seamlessly add or remove units within MG.

Among the items given above, stability and power quality in control strategy category can be interpreted as characteristics of the system. Although they have been treated in unitary fashion in [75], [121], and [122], here we draw the line between them and the other functionalities. The reason behind this is a fact that stability and sufficient power quality are mandatory objectives for every dc MG without exception. Also, the interventions to enhance stability margins and power quality mainly rely on local control diagram with the assistance of system level coordinated control to limited extent. The function of stability enhancement is programmed at the converter architecture and/or lowest control level. In particular, it can be improved by incorporating passive and active damping techniques, which are implemented

either by inserting passive elements within the converter filter or adding control loops within the local control, respectively, [123], [124]. On the other hand, power quality is related to the waveform of the voltage in common dc bus, and influence over it can also be achieved by proper combination of low-level architecture and control. One example of such a manipulation is interleaving number of dc–dc converters to reduce the voltage ripple and improve the transient response [125].

Here, we attempt to assess the extent to which the remaining five benchmark functionalities, i.e., redundancy, flexibility, scalability, coordinated control, and grid support, are relevant for viable performance of every addressed application. For that matter, we designate each one of them with one of the four levels of importance, i.e., low, medium, high, and critical.

Detailed elaboration about particular applications and importance of specific functions for their proper operation are given in following sections. The resulting evaluation is summarized in Table II. Moreover, through each of the sections, practical hints on how to meet specific objectives by choosing adequate combination of hardware architecture and control strategy are presented. Summary of that particular assessment is presented in Table III. In addition, Table III shows the typical voltage and power limitations reported for each particular application.

TABLE III  
SUITABLE CONTROL STRATEGIES AND ARCHITECTURE

Application	Control strategies	Architectures	Typical voltage limitation	Typical power limitation
Future households	Appliance scheduling—centralized, decentralized or distributed Adaptive droop control—for the cases when ESS are installed within the households	Single bus dc MG Bipolar single bus dc MG Redundant bus system—for mission critical applications Multiple dc MG clusters	For safety reasons, the maximum voltage for this application is commonly selected as 400 V.	Up to several tens of kilowatt for individual households.
Renewable energy parks	MPPT Grid support functionalities Coordinated control (when reduction of available production is required)—typically centralized	Single bus dc MG—for smaller scale parks Ring bus dc MG etc., with enhanced fault-tolerant capability Multiple dc MG clusters with fault isolation capability—virtually equivalent to series-parallel collector grids SST-enabled dc MG—for larger parks with connection to MV ac networks	Medium-voltage dc buses have been deployed to avoid multiple step-up transformers. A solution utilizing 7.5 kV has been reported in [155]. Highest analyzed voltages for offshore wind parks achieved by using series dc configuration were up to 300 kV [156].	Largest dc onshore and offshore renewable grids are in range of several hundred megawatts [156], [157].
Hybrid ESS	SoC estimation Charging algorithms Adaptive droop control—equalizing SoC of multiple ESS of same type DC bus signaling—coordination between ESS of different types	Single bus dc MG with direct connection of battery Single bus electronically regulated bus dc MG	The vast majority of hybrid ESSs reported in the literature is designed for 24, 48, and 380 V dc buses. However, higher common dc bus voltages such as 750 V have been used in some applications [158]. For a sole battery storage, 6-kV prototype has been developed [120].	Total power capacity depends on application but will in general never exceed the nominal power of a given application. Commonly, it is sized at approximately half of the nominal power (e.g., for smoothing power fluctuation of 2-MW wind turbine, optimum of 1-MW hybrid ESS is computed [159] while in a generic dc MG of 10 kW, 6 kW is used [160]).
EV charging stations	DC bus signaling—current sharing between grid-tied converter and ESS converter Adaptive droop control—sharing between multiple ESS SoC estimation Consensus algorithms—alternative for coordination between grid tied and ESS converter Charging algorithms—on site chargers typically have preinstalled a number of algorithms for different HEV batteries Grid support functionalities	Single electronically regulated dc bus for single charging station Multiple dc MG clusters or ring bus dc MG in case of multiple parking lots SST-enabled dc MG for larger stations with MV ac connection Ring bus dc MG etc., with enhanced fault-tolerant capability	5-kV MV dc substation for powering EVs was reported in [161]. However, much more common solution is to use step down transformer and establish lower voltage dc bus through rectifier interface [24], [162].	Maximum individual dc type chargers are rated at 240 kW [163]. However, more of these may be connected in parallel around the common dc bus.

### B. High-Efficiency Households

Since the majority of modern household energy sources and loads, e.g., PV panels, batteries, LEDs, and consumer electronics operate naturally at dc, it is more efficient to connect them around the dc bus to form a dc MG [126]. There are several demo projects of dc high-efficiency houses all over the world shown in the existing publications and project websites, such as USA [21], [22], Japan [20], Korea [127], and Denmark [128]. In contrast to the traditional ac system, dc system has higher efficiency. The energy saving opportunities by changing infrastructure from ac to dc can be up to 15% [129].

Except of benefits from the efficiency point of view, the control simplicity in case of dc makes it a much better option for integration of energy management systems. For this reason, dc MGs are excellent candidates for households that could greatly facilitate the achievement of objectives imposed by a number of present SG roadmaps, which state that increase in flexibility<sup>1</sup> of household consumers is a key for sustainable operation of the

future grids with high penetration of renewables [130], [131]. For that matter, real-time price signal is predicted to be the main driving factor that will motivate local households to rethink their distribution of energy.

In order to achieve flexibility, it is important to establish coordinated control of appliances within the households. It should be noted that integration of local ESSs with controllable appliances can also greatly aid to this functionality. Finally, all these units can be grouped into a number of clusters, where each one of them could absorb from or inject energy to other neighboring cluster(s), depending on the conditions of the system. In this way, even higher degree of flexibility and, consequently, larger potential to obtain concrete economic benefit can be obtained. While clustering of households is still at notional level, there are several published works that help in understanding the benefits of this concept [19], [74], [132].

The most common objectives in control of future households are reduction of the energy cost and maximization of customer comfort [132]–[137]. These objectives are most commonly achieved by scheduling the appliances within the households through various unit commitment strategies. In that sense, the respective appliances are typically coordinated on a higher

<sup>1</sup>Flexibility of consumers mentioned here is related to controllable power extraction from the grid and is different from the hardware flexibility addressed in Section III-A.

level in ON/OFF fashion and are not responsible for the voltage in the common dc bus. Therefore, the local control strategies do not apply since the voltage is regulated to a constant value by a grid-tied converter. Coordination based on the scheduling of appliances using decentralized [132], distributed [133], and centralized [134]–[137] control methods have been proposed.

Higher level of flexibility can be achieved if complimentary ESSs are used within the households. In that case, some of the control techniques presented in the first part of this paper can be deployed. For instance, in [138], the control method for a PV-based building with coordinated operation of batteries has been proposed to avoid the permanent active power injection to the utility grid. In [20] and [139], the bipolar configuration of dc MG has been proposed, which is useful to further enhance the dc power quality. Meanwhile, fuzzy droop control has been employed to reach the dynamic balancing of the SoC of each energy storage unit. In [140], batteries and heat pumps have been used as controllable loads in order to eliminate the power fluctuation by PV plant at the PCC. An optimized search algorithm was deployed to minimize the difference between highest and lowest power flow from the utility mains.

Regarding suitable hardware architectures, the single bus system shown in Fig. 2 is the most suitable configuration. In special cases, where redundant power supply is needed, configurations from Figs. 3 or 4 can be used. Finally, structure from Fig. 5 is appropriate for the application of multiple household cluster concepts.

### C. Renewable Energy Parks

A number of RESs can be clustered around the common dc bus to form an energy park. In that way, the system efficiency can be increased and issues such as reactive power circulation and harmonics avoided [126]. These kinds of dc systems are sometimes also referred to as the collector grids and are used for both photovoltaic and wind power applications [141], [142]. Compared to households, they present a different perspective, since they inject large amounts of power into the conventional ac power system and have more direct implications on its operation. Therefore, they need to be able to provide a number of ancillary services defined by well-established grid codes [143], [144]. As compliance with grid codes often implies reduction of active power injection to the grid, coordination between multiple renewable generators, which should achieve this task simultaneously, is mandatory. Several applications of dc collector grids are presented in the following paragraph.

A highly integrated dc module is developed in [39]. It provides the functionality to connect parallel interfaces for RES which share the same dc bus and have the same dc output value. In [40], a dynamic adaptor is proposed in a PV-based dc MG. The adaptor is connected at the output side of the solar field to cope with the randomness in the electrical production. Meanwhile, it can provide auxiliary function when the local dc MG is connected to the utility ac grid. In [41], in order to maximize the power utilization, the design and implementation of the multiple MPPTs is presented. The coordinated operation among different trackers is studied. In [42], a switched boost converter is

developed. This kind of converter can supply both ac and dc components, simultaneously, which makes it possible to behave as the interface to the local dc loads and external ac grid. In [43], a solar converter with high voltage gain is proposed to harvest maximum power from solar panels. From the system level point of view, the energy management system for renewable energy parks has also been studied. The cost and stability of a dc-coupled renewable energy park with hybrid sources is analyzed in [145], where the forecasting model for different sources and loads is also developed. In [146], the optimal allocation and economic analysis for the energy storage units for renewable energy parks is studied. In [147], the detailed power loss model is derived to optimize the system efficiency.

Regarding typical hardware configuration of large collector grids, a number of wind turbines or PV generators are connected in series to form a branch, which operates at a nominal dc voltage. Then, number of branches are connected in parallel to form a complete collector grid [148]. A critical function here is a fault ride through since stop of production presents a big economic loss. In particular, when one unit in a branch fails, it is necessary to bypass it in order to maintain the healthy units operational. On the other hand, branches are typically designed for a specific voltage, and if the power rating of the central inverter permits, additional branches can be added to expand the power rating of the whole system. In relation to architectures presented in Section II, single bus system of Fig. 2 can be used for smaller scale applications. On the other hand, for larger parks, connection to MV ac network can be achieved through SST, as shown in Fig. 6. Finally, as series–parallel collector grids resemble the structure of multiple clusters of Fig. 5, this configuration can be used to achieve desired dc-link voltage by connecting number of generators in series. It should be noted that for the sake of maintenance and fault ride through capability, it is mandatory to incorporate possibility of bypassing any given generator. In order to enhance the fault-tolerant capability, dc ring bus configuration in Fig. 7 and other configurations with higher reliability can be considered.

### D. Hybrid ESSs

ESS technologies are characterized by several important performance indicators, such as energy efficiency, energy and power density, cycle life, and rate of self-discharge [149]. In that sense, for situations when a single ESS is not able to meet the prescribed requirements of specific application, it is advisable to combine two or more technologies that complement each other. Since dc distribution offers a natural interface for practically all types of ESS, these kinds of hybrid ESS are frequently deployed around the common dc bus.

A large number of hybrid ESS applications have been reported in the literature and only several selected ones are shown here. From architectural point of view, the majority of applications are small and dense systems gathered around the common dc bus, which can be established either through direct connection of the battery stack or through regulated power electronic converters. Since the bus is small, the most common faults in these kinds of systems are directly on storage units and design

of fault ride through capability based on bypassing the faulted part of the common dc bus would unnecessarily complicate the system. On the other hand, plug and play capability is a highly desirable feature which can be exploited either for extending the power or energy capacity of the system [55]. Optimization of charge/discharge processes among different technologies is another important aspect. Objective function in this regard usually incorporates terms related to degradation of respective ESS technologies according to imposed mission profiles and can be executed online [150] or offline for capacity optimization [151]. In grid-connected applications, variable grid price signal can be incorporated as well within the objective function. In the remainder of this section, several hybrid ESS applications are reviewed.

The most common representative of hybrid ESS is secondary battery in combination with supercaps. In [33], a model predictive control has been employed to enforce super-caps to absorb only fast power changes, prolonging the lifetime of the battery in that way. Superconductive magnetic energy storage (SMES) was combined with battery in [34]. In this case, fuzzy control was used to make SMES act as a power buffer, while the battery is responsible for slower variations. A single synchronous buck converter was used as an interface between fuel cell and battery in [35]. Associated control was designed with the objective to share the pulsed current with respect to permitted voltage ranges of the two ESS technologies. Similar, but more reasonable approach which uses supercaps instead of batteries, and bidirectional isolated dc–dc converter, was presented in [36]. With the aim to operate compressed air energy storage at its highest efficiency during every operating condition, its supplementation with super-caps was investigated in [152]. Finally, as larger architectures in case of hybrid ESSs would bring unnecessary losses and complexity, the two most common power architectures are those shown in Figs. 1 and 2.

### E. EV Fast Charging Stations

EVs have gained increasing attention due to potential of reducing the greenhouse gas emissions and conventional fuel consumption. However, it has been shown that their large-scale integration could have a significant adverse effect on the electric grid [26] and dc MGs have been conceived as a promising solution to support the operation of the power system by aggregating multiple EVs [153]. Fleets of PHEV chargers represent a specific group of consumers with an outstanding potential from the increase of flexibility standpoint.

It is deemed that grid codes similar to those imposed for RES will be developed for fast dc charging stations, especially when the level of their penetration becomes more significant. In contrast to vehicle to grid where energy stored in vehicle's battery is used for grid support, charging stations with supplemental ESS have been proposed not to disrupt the recommended charging pattern of the vehicle's battery. In that sense, different types of energy storages can be employed in the dc EV charging stations, such as flywheel, battery, and hybrid ESSs [153]. Internal coordination between the ESS and grid is an important but challenging task, since number of objectives need to be simultaneously met, i.e., tracking of the variable grid price

signal, consideration about degradation of particular ESS technology, and grid friendly operation. Additionally, this type of multiobjective control can be achieved in myriad of ways. In the following paragraph, several approaches used in fast dc charging applications are provided.

In [27], the traditional power quality problems caused by the integration of multiple EVs have been analyzed. In [29], the power flow related to the dc charging station is optimized online to reduce the power line losses. In [154], the economic impact to the cost and benefits for installing the EV charging station is studied. Distributed bus signaling strategy was used in [23] to coordinate operation of grid and ESS converters. Flywheel based on induction machine was used in this particular application since it does not introduce significant performance degradation problems with cycling as batteries.

In order to reach an efficient power conversion in EV charging stations, different topologies of power electronic converters can be used. In [28], the topologies of nonisolated bidirectional dc–dc converters are reviewed to derive the suitable solution for the EV charging station with multiple parking decks. In [105], SST-based fast EV chargers are developed to reduce the size of the interfacing converters. It can be noted that configurations like single bus dc MG in Fig. 2, multiple dc MG clusters in Fig. 5, SST-enabled dc MG in Fig. 6, and fault-tolerant configuration such as ring bus dc MG in Fig. 7 can be used in case of parking lots.

Based on the above discussion for each specific application of dc MG, the suitable control strategies and architectures for each of them are summarized in Table III, and the typical voltage and power limitations for each application are also shown in this table.

## IV. GROUNDING AND PROTECTION ISSUES IN DC MGs

As opposed to many advantages that dc systems bring in relation to ac, design of their protection represents a considerable challenge. The most important aggravating circumstance in that view is problem with extinguishment of dc arc which inevitably appears upon the tripping of protective devices. On the other hand, as worldwide industry has just recently started to conceive dc MGs as serious actors in future SGs, there is still a general lack of understanding and experience with operational issues of these systems [75]. Therefore, widely accepted protection standards and guidelines are yet to be defined.

The aim of this section is to update the current status in dc MG protection area. For that matter, we address four main aspects: 1) types of faults; 2) grounding options; 3) protective devices; and 4) design of protection systems.

### A. Types of Faults

Fig. 11 shows two basic types of faults that can exist in dc power systems, i.e., line to ground and line to line faults [82]. It should be noted that although less serious, line to ground faults are the ones most frequently appearing in industrial distribution systems [164]. Line to ground fault can have either low or high fault impedance, while line to line fault typically has a low impedance [45]. Moreover, a fault can occur at various points in the system, and each one of them will have different

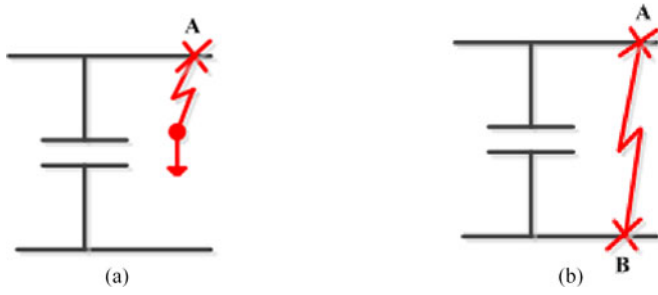


Fig. 11. Two fault types in dc systems. (a) Line to ground fault. (b) Line to line fault.

implications. For instance, unlike feeder faults that can be rapidly isolated from the main bus, faults at the main bus itself present a greater danger since all the feeders connected to it get affected [45].

When designing the protection system, it is important to identify all possible fault locations and to calculate the fault current for every given location [165]. All active sources within the system participate in the formation of the total fault current and their actual contributions depend on the effective impedances between the respective source and the fault locations. For instance, in case of bus fault, the contribution of a battery which is directly connected to the common dc bus via cable can be expressed as follows:

$$i_{\text{bat}}(t) = \frac{u_{\text{bat}}}{R_{\text{bat}} + R_{\text{cable}}} \left(1 - e^{-t/\tau_{\text{eff}}}\right) \quad (1)$$

where  $u_{\text{bat}}$  is the battery open-circuit voltage at the time of the fault,  $R_{\text{bat}}$  and  $R_{\text{cable}}$  are battery and connecting cable internal resistances, respectively, while  $\tau_{\text{eff}} = (L_{\text{bat}} + L_{\text{cable}})/(R_{\text{bat}} + R_{\text{cable}})$  is the time constant of the effective battery/cable system. On the other hand, if a battery (or any other type of source) is connected to the bus through power electronics converter interface, current resulting from quick discharge of its filter capacitor plays a pivotal role in the fault current

$$i_c(t) = \frac{u_{\text{dc}}}{R_c} e^{-t/\tau_c} \quad (2)$$

where  $u_{\text{dc}}$  is the common dc bus voltage at the time of the fault,  $R_c$  is the capacitor series resistance and  $\tau_c = C_{\text{dc}} R_c$  is the time constant of the effective converter/common dc bus  $RC$  system. If there exists a cable of considerable length between a converter and common dc bus, corresponding parameters of the cable should be included in calculation as well, resulting in a higher order dynamic system [164]. Similarly, faults that appear on feeders can be treated just as special cases of bus faults and their current contribution can be obtained by looking into effective impedances [45], [164], [166].

Total fault current can be calculated as a summation of current contributions from the battery, which is directly connected to the common bus and all other source converter currents [82]

$$i_{\text{fault}}(t) = i_{\text{bat}}(t) + \sum_{j=1}^N i_{c,j}(t) \quad (3)$$

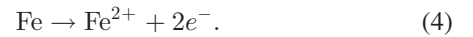
with  $N$  being the number of source converters connected to the common bus. It should be noted that  $i_{\text{bat}}(t)$  in equation above is

optional and exist only in systems which include direct interface of the battery to the common dc bus (e.g., system shown in Fig. 1). On the other hand, the impact of line to ground faults on system performance depends heavily on the grounding method. More details about the grounding methods are given in the next section.

## B. Grounding

The most important objectives of the system grounding are to facilitate fault detection, minimize stray currents (leakage current from conductor to the ground), and to ensure the personnel safety by minimizing the common-mode voltage (potential difference between device conductor and ground) [167].

Corrosion is the main problem associated with stray current. The phenomenon of stray-current-induced corrosion was first observed at the end of 19th century, in dc-powered electrified rail transit systems [168]. The problem has been initially attributed to the chemical structure of the soil surrounding the rails, but it was soon realized that the soil cannot be the sole contributor to it since corrosion has been observed in all metallic structures around the rail base. Therefore, it has been concluded that the leakage or stray current is the main culprit for significant amplification of this undesirable effect. Stray currents cause corrosion at the locations of current leakage from conductor into the soil. Since the current flow in conductor is primarily electronic, and ionic in the soil (i.e., ground), an electron to ion transfer is inevitable as current changes its transmitting medium. In particular, this electron producing or oxidation reaction can be represented by the following [169]:



The reaction shown above is known as corrosion if it takes place over a prolonged period of time. In a broad sense, corrosion of metallic objects will occur always at places where current transfers from them to the surrounding environment. By undertaking the measures to reduce the stray current, the corrosion rate will be reduced as well and severe structural damages to conductors avoided. Apart for traction applications, modeling, assessment, and stray-current control methods have been recently also proposed for large photovoltaic plants [170], [171].

Another important metric of grounding system performance is the level of common-mode voltage. It is referred to a phenomenon where the negative pole of the bus fluctuates as the neutral of the ac side that supplies dc bus through converter interfaces [172]. In traction applications, this voltage is commonly referred to as the touch potential [167]. Although the potential difference between the positive and negative pole will be controlled to a constant value, this kind of fluctuating phenomenon presents a challenge for the operation of the system. In particular, it gets mapped to the neutral points of all the loads and generators, contributing to the loop currents between the inter-connected converters. More importantly, common-mode voltage presents a personnel safety concern if it gets exceeds the value of 60 V, which is generally considered as a safety threshold [167].

It is important to note that the common-mode voltage and the level of stray current are related to each other by the grounding

resistance. More precisely, for a very high grounding resistance, there will essentially be no stray current, but the common-mode voltage will take its maximum value. On the contrary, if the system is solidly grounded, there will be virtually no common-mode voltage, but stray current will be the highest. Therefore, the objectives to minimize both of them at the same time are conflicting with each other. However, with a proper grounding strategy, this tradeoff can be explicitly taken into account.

Different grounding alternatives have been proposed for dc power systems. Some references suggest ungrounded operation, especially for low-voltage applications [75], [173], [174]. The reason for that is a fact that the common-mode voltage will typically not be on such a high level to present a danger for personnel safety, and will offer a possibility of continuing operation even in case of a single phase-to-ground fault. Still, it is instrumental then to discover and correct a possible second ground fault or it will create a line-to-line fault [175].

In grounded mode, either a solid, low- or high-resistance grounding can be applied [45]. Solid grounding is rarely used in modern systems because of the pronounced stray-current-induced corrosion [167]. There are a number of options regarding selection of the point in the system to be grounded; it can be either a positive pole, midpoint, or negative pole of the common dc link [164]. For that matter, Jacobson and Walker [176] suggest to use midpoint grounding in order to minimize the ground potential and enhance safety. Also, the circulating current can be greatly reduced in this way in relation to ac-side neutral grounded system. IEC 60364 standard [177] defines three grounding strategies which give specifications on how the source bus and conductive parts of the system are connected to earth. The respective methods are denoted by double letter abbreviations, i.e., TN, IT, and TT. Here, first letter reveals the type of connection of source bus to the ground, whereas the second one gives the conductive part type of ground connection. T, N, and I indicate direct connection to earth, direct connection to neutral, and isolation, respectively.

In the case of TN, power line and exposed conducting parts are commonly earthed through respective midpoints, while in IT, the power line is not earthed while conducting parts are commonly earthed either on positive or negative pole. On the other hand, there are multiple grounding points in TT systems. In general, it can be said that TN systems require low-grounding impedance and that it is easy to detect a line fault when this grounding strategy is applied. However, the touch voltage may get unacceptably high depending on the voltage level at the time when fault occurs. IT systems do not need low-grounding impedance, and have very low fault current if there is a single fault. However, another ground fault is likely to induce much higher current [82]. Moreover, it is complex to detect the line fault. It should be noted that positive pole is grounded by IT method in telecom systems in order to minimize the corrosion effect [45]. Installation of grounding system is straightforward in case of TT grounding, and the faults do not migrate [82]. Nevertheless, the high-voltage stress problem still exists and circulating current paths are likely to occur.

Unlike methods presented above, Paul [167] suggests several reconfigurable grounding options with a possibility to operate the system normally in ungrounded mode in order to minimize

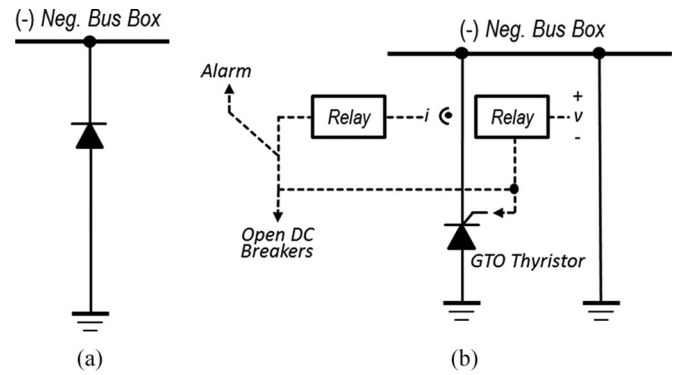


Fig. 12. Block diagrams of reconfigurable grounding systems. (a) Diode grounded system. (b) Thyristor grounded system.

TABLE IV  
SUMMARY OF COMMON-MODE VOLTAGE VERSUS STRAY CURRENT  
RELATIONSHIPS WITH DIFFERENT GROUNDING METHODS

System Grounding Method	Common-Mode Voltage	Stray Current Level
Ungrounded System	High	Low
Solidly Grounded	Low	High
Diode Grounded	Moderate/Low	Moderate/High
Thyristor Grounded	Moderate/High	Moderate/Low

stray-current-induced corrosion, but to automatically ground the system upon sensing dangerously high common-mode voltage. After the clearance of abnormal operating condition is achieved, the system is automatically switched back to ungrounded mode. Block diagrams of two examples of reconfigurable grounding systems, i.e., diode and thyristor grounding, are depicted in Fig. 12.

Diode grounding, as shown in Fig. 12(a), is comprised of a direct metallic connection of the negative portion of dc bus to the ground, but via a diode circuit. In that way, current is allowed to flow from the ground toward the negative bus when the voltage across the diode exceeds some threshold value. Once it happens, it gets dissipated. Since corrosion is not completely eliminated in these kinds of systems, they require a periodical maintenance [178].

Contrary to the diode scheme, bidirectional thyristor scheme offers active control over the grounding instances [see Fig. 12(b)]. This is achieved by triggering the thyristor gate only when the negative-to-ground voltage exceeds a preset value. Moreover, a current sensor is installed in series with the grounding conductor. If the decay of current is observed, it means that the system can be returned to ungrounded mode. On the contrary, if the current would maintain its amplitude, then a positive to ground fault is the most likely reason. In that case, dc feeder breakers are tripped in that case. The status of the system can be communicated with the environment through the alarm signal. The basic advantage of the thyristor grounding strategy is that the system maintains operation in ungrounded mode in every other instance, keeping the stray current minimized [167].

Table IV compares the characteristics of previously described diode and thyristor grounding schemes with respect to their ungrounded and solidly grounded counterparts [167].

### C. Protective Devices

Protection devices that are presently commercially available for dc systems include fuses and circuit breakers (CBs) [45]. However, they inherently introduce large time constants and time delays before activation, respectively. In addition, interruption of the current in both cases is accompanied by the appearance of the arc. While arc gets extinguished naturally in ac systems within the half cycle after tripping by first crossing of the current through zero, it presents a challenge in dc systems since the current has a steady value. Arc occurrence presents a dangerous condition not only from the safety point of view, but also causes contact erosion in CBs and consequently a short lifetime and high maintenance costs [75], [179].

Fuses operate on a principle of melting down the fuse link in a heat absorbing material. Their ratings are specified in terms of RMS values of voltages and currents and are, hence, equally applicable to ac and dc systems. However, it needs to be ensured that the time constant of current increase during the fault is below certain limits since slow increase of temperature may prevent the heat-absorbing material to extinguish the arc [180].

Standard molded-case circuit breakers (MCCB) that consist of a contactor, a quenching chamber, and a tripping device can be used as well. They can have either a thermal–magnetic or electronic tripping device. In either case, sufficient voltage blocking capability can be achieved by connecting contactors in series [45]. One potential problem with MCCBs in systems dominated by power electronic converters, as is the typical case in dc MGs, is that short-circuit currents are supplied mainly by filter capacitors. Their quick discharge following the fault causes currents of very high magnitude, but which last for a very short periods of time. Therefore, the force generated by these currents may be insufficient for opening the contacts of MCCB [75].

Due to inherent tripping time delays and other problems associated with fuses and CBs, protections utilizing only these devices have inherent performance limitations. In order to cope with that, usage of power electronics-based protection devices has been proposed. They are commonly referred to as the solid-state circuit breakers (SSCBs) [181], [182]. Several semiconductor devices, such as gate turn-off thyristors (GTOs), insulated-gate bipolar transistors (IGBTs), and insulated-gate commutated thyristors (IGCTs), have been used in this context, with each one of them having some specific advantages and drawbacks. For that matter, Meyer *et al.* [181] summarizes and compares a number of different switch topologies under technical and economic aspects. It is demonstrated that GTOs and IGCTs have much lower onlosses than IGBTs. Also, it is estimated that IGCT in rectifier configuration has by far the highest reliability.

In order to be able to interrupt the current, there needs to exist an element to which SSCB dissipates energy during interruption. Typical elements installed for that matter are metal–oxide varistors (MOVs), capacitors, and switched resistors or their combinations [182]–[184]. This brings a different perspective of current interruption when compared to conventional breakers where the electric power during tripping the device is manifested in an arc that needs to be extinguished using an arc chute [183], [185].

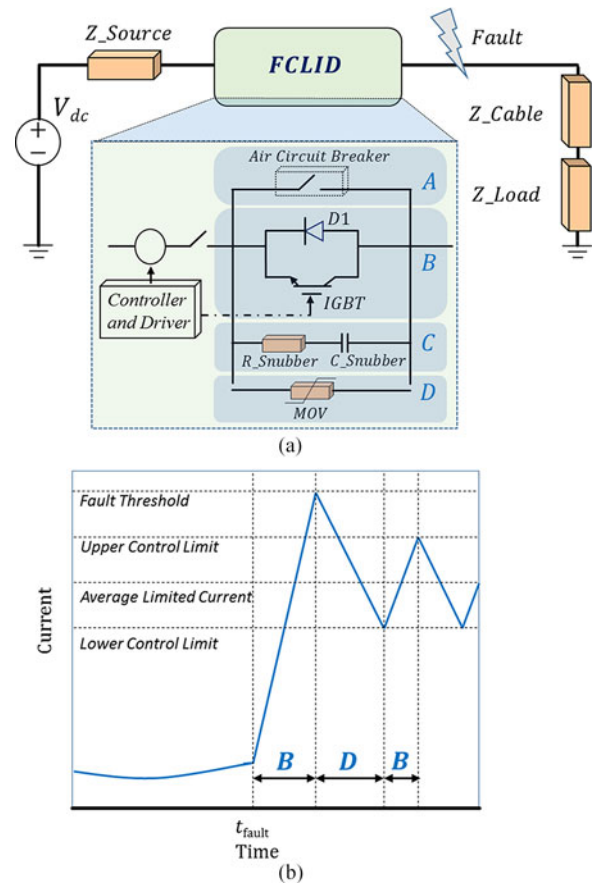


Fig. 13. Fault current limiting and interrupting device [182]. (a) Topology. (b) Fault current waveform.

Exploiting the capability of fast switching, this dedicated power dissipation element can be readily used for current limiting as well. In that sense, SSCBs can actively regulate the fault current to a selected level, rather than leaving it dependent on characteristics of supplying sources. Limited fault current provides less thermal and magnetic stress on the surrounding components resulting in a lower installation cost, as well as safer operation of the system in general [179], [186], [187]. Moreover, current limiting circuits installed at strategic places, e.g., radial line supplying a high power density load center, allow usage of commercially available protection devices with low power rating in the entire downstream network. It should be noted that current limitation functionality can also be achieved by superconducting elements [188]. However, they need to be actively cooled and the current limiting capacity with present technology is inferior to SSCB solutions [187].

A typical example of IGBT-based SSCB with current limiting capability is depicted in Fig. 13(a) [182]. When the semiconductor device (IGBT) switches OFF, the current flowing toward the fault location decays since the power gets dissipated in parallel combination of a snubber circuit and MOV. On the other hand, when the IGBT turns on, the current raises up again. The hysteresis-based controllers with preselected upper and lower thresholds for the fault current are the most commonly deployed to achieve current limiting. An idealized waveform of a current propagation using this kind of controller is shown in Fig. 13(b).

#### D. Design of Protection Systems

The objective of protection system is to detect, locate, and isolate any fault so that the dc MG can continue operating its uncorrupted part. In order to achieve this goal, knowledge about possible faults should be incorporated to select the most appropriate protection devices and grounding methods into a complete protection system. More specifically, desirable characteristics of such a system are reliability, selectivity, speed, performance, economics, and simplicity [75]. In the remainder of this section, several prominent approaches that try to meet these features are summarized.

An insightful example of selecting appropriate protection devices for different components of a dc MG was presented in [45]. According to specific fault withstanding capability of each associated unit, it was concluded that converter protection must be very fast in order to limit the fault current through IGBT diodes, while battery protection can allow slower reaction time as it can withstand considerably higher fault current without damage. For that matter, ultrafast hybrid CB was proposed for converter protection and regular CBs for battery protection. On the other hand, fuses and MCCBs were used for feeder protection. In particular, it was claimed that it is better to use MCCBs closer to the loads since they can simultaneously trip both poles, while fuses are more appropriate for installation closer to the bus due to their magnetic sensing that gives good selectivity. DC-link voltage information together with fault current was used for accurate fault detection.

A handshake method was proposed in [77] to split the dc system into three possible zones and isolate the possible fault on any of the buses. The drawback of this method is that it needs to completely shut down the system following the fault, which may be unnecessary in most conditions. Alternative method that avoids the shutdown was suggested in [82], where a loop-type dc bus was used together with protection that is able to detect the fault and separate only the faulted part, so that the rest of the system keeps operating. The method was realized by using communication system where one master unit receives current measurements from two slave units and gives commands to control the bus switches depending on the difference between the two measured currents. A protection scheme that relies on solid-state converter with fast-acting current-limiting capability was proposed in [189]. The respective converter uses two emitter turn-off devices in antiparallel configuration to realize both switching and protection. Furthermore, a relay-based coordination and backup protection schemes were addressed. Coordination was realized in a fully decentralized fashion, by using appropriate devices such as fuses and diodes.

In [78], consideration of the dynamic characteristics of fault impedances is addressed in detail, giving a motivation to employ a unit protection of dc MGs. For that matter, a flexible design framework for unit protection of dc MGs, which achieves high sensitivity while taking into account a total cost of the system by using commercial of the shelf technologies, is proposed. Moreover, practical limitations of communication technology have been discussed, and it has been shown that better discrimination of faults can be achieved by proposed method.

#### V. STANDARDS AND REGULATION ISSUES ASSOCIATED WITH DC MGs

Although dc MG has been intensively studied in recent years, it is still an emerging technology which needs to face number of challenges before widespread acceptance in industrial and commercial applications. Apart from protection issues discussed in the previous section, standardization is another barrier to proliferation of dc MGs. In order to promote dc MG, several organizations dedicated themselves to develop practical standards. Here, the existing standards and those under development are reviewed and discussed.

European standard ETSI EN 300 132-3-1 discusses the low-voltage dc systems [190]. It is mainly designed for data/telecom equipment with voltage levels of up to 400 V. In normal condition, considering that long power cables may be used, the reference voltage at the input terminal of the equipment is allowed to vary within specified bounds, i.e.,  $365 \pm 15$  V. In abnormal condition, voltage variations, voltage dips, short interruptions, and voltage surges are taken into account. For a certain type of fault, different time durations are specified for fault clearance. Meanwhile, for protection purpose, maximum steady-state current and inrush current are considered. In order to quantify the current limits, pulse time duration  $T_{50}$  is defined, which indicates the time duration when the pulse current exceeds 50% of the peak value. By considering different values of  $T_{50}$ , the limits of pulse current are identified and shown by the ratio between peak pulse current and nominal terminal current. Besides the aspects mentioned above, grounding and electromagnetic compatibility (EMC) requirements are also indicated in this standard.

EMerge Alliance is an open industry association founded recently to promote dc MGs in energy efficient buildings. The vision of EMerge Alliance is to deploy advanced architectures and control systems based on dc MGs in occupied spaces, data/telecom centers, building exteriors, and building services [191]. Except of promoting usage of dc MGs in industrial applications, EMerge Alliance is also working on dc MG standards. Although still under development, the preliminary standards for occupied spaces and data/telecom centers have been released [191]. For occupied spaces, the available power is harvested from conventional ac grid and optional onsite generation such as PV. The main power consumers in the occupied spaces are defined as air conditioners, lightning, wireless internet devices, etc. The dc buses are laid in the ceiling to form the power grid, and daylight sensors are optimally located to rationally adjust the lightning system for energy saving purpose. Apart from occupied spaces, EMerge Alliance also works on standardization of data/telecom centers [191]. Since data/telecom centers as information hubs play an important role in modern society, it is necessary to improve their performance and efficiency. DC MGs can significantly shorten the energy conversion chain from the input side of ac grid to dc end-user and thereby reduce the system power loss. Hence, they become the promising candidates for modern data/telecom centers. AC power is rectified to 380 V dc power to supply various components in data/telecom center, e.g., racks and servers,

while dc UPS is deployed to guarantee reliable power generation.

IEEE Standard 946 provides the recommended practice for dc auxiliary power systems [192]. Rather than being customized for dc MG, it was originally designed for battery-based dc auxiliary power systems for generation stations [193]. However, some of the content of this standard also applies to dc MGs nowadays. It normalizes the design and operation of lead acid batteries in dc power systems. Sizing of batteries, determination of battery duty cycles, maintenance, testing, and replacement are included. Meanwhile, technical specifications of battery chargers are also discussed. For example, in order to ensure safe operation of the battery, the output voltage ripple is limited to 2% of the nominal value without additional filtering. Typical system architecture of a dc distribution system is also shown in this standard, and the detailed voltage rating of each component is listed.

IEC SG4 has established a dedicated technical committee in charge of delivering standards for LVDC distribution systems of up to 1500 V [194]. The objective is to coordinate the standardization of different areas where LVDC distribution systems are used, e.g., data centers, commercial buildings, electricity storage for mobile products, EVs, etc. IEC SG4 also focuses on market needs to effectively promote LVDC systems into practical applications. By considering these requirements, energy efficiency, reduction of EMC issues, and reduced usage of natural resources are taken into account. Meanwhile, measuring methods, protection, and grounding issues are discussed. Finally, a dc-powered green home is proposed where PV or wind power are used as the energy input, and the heating ventilation air conditioning system is energized by dc power.

Recently formed IEEE DC@Home committee sponsored by the IEEE PES Intelligent Grid Coordinating Committee has also gathered some of the leading experts in the dc MG area to cooperate in developing the standards and defining the roadmap for research and commercialization of dc MGs [195]. The goal of DC@Home committee is to 1) create a business case for dc by determining the actual energy losses; 2) identify research required to advance the state of the art; 3) make preliminary recommendations on how dc would be delivered into the homes; and 4) determine the standards that should be followed.

REbus is an open standard that proposes the normalized operation of dc MG for both residential and commercial applications [85]. REbus dc MG operates alongside existing ac utility grid, and coordinates the operation of hybrid onsite renewable energy generation. Communication layers are included to enable system level energy management and monitor the status of each unit in dc MG. Based on REbus standard, the nominal dc bus voltage is 380 V. However, for decentralized coordination of purposes of primary source units, loads and ESSs, this voltage level can be changed within the specified range.

Apart from the aforementioned standards, some other works can be also used for standardizing the operation of dc MG. For example, National Electrical Code (NEC) includes articles and clauses introducing the utilization of dc technology [193]. For instance, military standard MIL-STD-1399 includes sections defining the requirements of dc equipment in shipboard power systems [193], [196]. Table V summarizes the general descrip-

tions and scopes of the standards mentioned in this section [193], [195], [197].

Finally, it should be noted that the IEEE Standard 1547 can also be used to normalize the operation of dc MGs. Although it is designed for ac distribution system with RESs, some of the concepts in it can be employed in dc systems and thereby used for expansion into standards for dc MGs. This standard covers different operation modes of MGs, i.e., grid-connected and islanding operation modes. Meanwhile, requirements for both normal and abnormal conditions are discussed. Section 4 in the given standard introduces the interconnection technical specifications and requirements, and is being regarded as the fundamental standardization guideline for MGs [197], [198]. It normalizes not only the operation in normal condition, e.g., voltage regulation and synchronization, but also the responses when distributed sources encounter faults. Detailed requirements of acceptable voltage and frequency ranges are given to quantify the response of protective devices. Power quality issues are also within the scope, including dc injection, harmonics, etc.

Among the standardization topics from different organizations, a common concern is the general consensus about the optimal voltage level for dc MG. At present, the most likely option seems to be 380 V as the rated voltage level, especially for household and data/telecom center applications. On the other hand, 48- or 24-V dc voltages can be commonly selected as auxiliary dc voltage levels for low-voltage rating appliances. For instance, in dc nanogrid proposed by the Virginia Polytechnic University [18], hybrid voltage levels of 380 and 48 V coexist to meet the requirements of different appliances in the house. However, different voltage levels have also been selected. For example, a 1500-V dc MG is built up in Finland [199], while a bipolar dc MG proposed in Japan, uses  $\pm 170$  V [20].

## VI. CONCLUSION AND FUTURE TRENDS

This paper has summarized the state of the art in hardware topologies and applications of dc MGs, together with practical concerns regarding their wide-scale deployment. Several types of prominent hardware topologies are presented before continuing with assessment of their suitability for practical applications, such as dc households, EV charging stations, hybrid ESSs, and RES parks. Regarding the hardware topologies, the common industrial approach based on direct connection of battery stack to the common dc bus can be replaced by more flexible structures which include regulated bus.

As an extension to singular bus structure, redundant bus topologies have been proposed as attractive solutions in case when highly reliable distribution is required. More recently, SST has been recognized as a key enabling technology not only for dc MGs, but also for future dc distribution systems in general. However, more development related to this technology needs to be done before it enters the SG.

Practical performance requirements of four typical dc MG applications, i.e., efficient households, renewable energy parks, hybrid ESSs, and fast dc EV charging stations, have been discussed. In that sense, a critical opinion about the usefulness of functionalities such as redundancy, flexibility, scalability,

TABLE V  
GENERAL DESCRIPTIONS AND SCOPES OF DC STANDARDS

Standard	Description	Scope
1 ETSI EN 300 132-3-1	Power supply interface at the input to data/telecom equipment Subpart 1 of Part 3: Direct current source up to 400 V	<ul style="list-style-type: none"> <li>• Designed for data/telecom equipment</li> <li>• For voltage levels of up to 400 V</li> <li>• Consider voltage level in normal operation and requirements for different types of abnormal operation</li> <li>• Limit of fault current</li> <li>• Grounding and EMC</li> </ul>
2 EMerge Alliance dc MG	Standards for occupied spaces and data center	<ul style="list-style-type: none"> <li>• Recommended architecture and control system in dc MGs</li> </ul>
3 IEEE 946	Recommended practice for the design of dc auxiliary power systems for generating stations	<ul style="list-style-type: none"> <li>• Recommended practice of lead-acid-battery-based dc auxiliary power systems for generation stations</li> <li>• Normalize the operation regarding batteries, e.g., sizing, determination of duty cycle, maintenance, etc.</li> <li>• Technical specifications of battery chargers</li> <li>• Typical architecture of dc systems</li> <li>• Voltage ratings of dc equipment</li> </ul>
4 IEC SG4	LVDC distribution system up to 1500V	<ul style="list-style-type: none"> <li>• Coordinate the standardization of different areas, e.g., data centers, commercial buildings, etc.</li> <li>• Energy efficiency, EMC, reduction of natural resources</li> <li>• 100% dc or hybrid ac and dc architecture</li> <li>• Life cycle of equipment, protection and grounding</li> </ul>
5 IEEE DC@Home	DC-powered house	<ul style="list-style-type: none"> <li>• Standard and roadmap of applying LVDC MG in residential houses</li> </ul>
6 REbus	Open standard for clean power distribution relying on dc	<ul style="list-style-type: none"> <li>• Evaluation of losses using dc power at home</li> <li>• Operate alongside with existing ac system</li> <li>• Hybrid coordination of on-site renewable energy generation</li> <li>• 380 V dc common bus with acceptable variation based on the status of source, load and energy storage</li> </ul>
7 NEC	Legal codes including introduction of dc technology	<ul style="list-style-type: none"> <li>• Article 393, 625, 690, 692 introduce dc technology</li> </ul>
8 MIL-STD-1399	Electrical interface characteristics for shipboard equipment	<ul style="list-style-type: none"> <li>• Includes sections defining the requirements of dc equipment in shipboard power systems</li> </ul>
9 IEEE 1547	Requirements for interconnecting distributed resources with electric power systems	<ul style="list-style-type: none"> <li>• Designed for ac system and some of its contents can be used as a reference when designing the standards for dc systems</li> <li>• Islanding and grid-connected operation modes</li> <li>• Normal and abnormal operation</li> <li>• Requirements and practice for distributed sources</li> </ul>

coordinated control, and grid support capability has been given. Moreover, elaboration about possibility to achieve the proposed performance specification within the framework of available architectures and control strategies has been presented together with several examples for each application.

Finally, the state of the art in protection and standardization for dc MGs has been presented. Regarding protection, basic protection principles and grounding methods have been explained within the framework of available devices. This has been followed by a critical evaluation of several newly proposed protection systems. Regarding regulation issues, it has been pointed out that the key prerequisite for true large-scale integration of low-voltage dc distribution networks into the SG is adoption of corresponding standards. In that sense, IEEE 1547 is the suitable standard for ac MGs, but only parts of it can be used for dc MGs. Therefore, dedicated standards for normalizing the operation of dc MGs are highly required. For that matter, standards such as EMerge Alliance, IEC SG4, and IEEE DC@Home and REbus are under development. This paper has addressed the current status and future development directions for each one of them. On the other hand, ETSI EN 300 132-3-1, IEEE 946, NEC, and MIL-STD-1399 are already established and the number of concepts used in them can be already directly used.

Given the increasing penetration of RESs and electronically regulated loads in modern distribution networks, it is foreseeable that dc MGs will continue to be rapidly developed in the following years. Referencing to the topics covered in this manuscript, several key lines of future research can be pointed out. First of all, a lot of attention will need to be devoted to still unresolved practical issues associated with dc systems, i.e., protection and grounding schemes. For that matter, design of fast acting dc breakers and grounding devices based on solid-state technology with current limiting capability will continue to be a very important research topic. As is customary, objective will be to achieve extended functionalities with more compact design and higher reliability. Moreover, coordinated integration of these kinds of devices into overall protection systems is envisioned to be a fertile ground for researchers in the nearest future. On the other hand, new and improved converter topologies will be developed for interfacing different types of sources or ac grids with dc MGs. Finally, the applications of dc MGs will be extended to a large extent. In particular, penetration of medium voltage dc distribution systems into practical industrial applications is expected to experience rapid growth in near future, both for autonomous and grid-connected applications. This expansion will naturally lead to proposals of new hardware architectures

and alternative arrangements of the common dc bus for higher reliability and redundancy.

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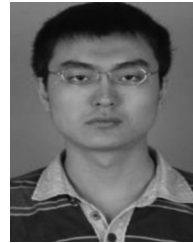


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