

A Multiinput Bridgeless Resonant AC–DC Converter for Electromagnetic Energy Harvesting

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Abstract—Flapping electromagnetic-reed generators are investigated to harvest wind energy, even at low cutoff wind speeds. Power electronic interfaces are intended to address ac–dc conversion and power conditioning for single- or multiple-channel systems. However, the generated voltage of each generator reed at low wind speed is usually below the threshold voltage of power electronic semiconductor devices, increasing the difficulty and inefficiency of rectification, particularly at relatively low output powers. This paper proposes a multiinput bridgeless resonant ac–dc converter to achieve ac–dc conversion, step-up voltage and match optimal impedance for a multichannel electromagnetic energy harvesting system. Alternating voltage of each generator is stepped up through the switching LC network and then rectified by a free-wheeling diode. Its resonant operation enhances efficiency and enables miniaturization through high frequency switching. The optimal electrical impedance can be adjusted through resonance impedance matching and pulse-frequency-modulation control. A 5 cm × 3 cm, six-input standalone prototype is fabricated to address power conditioning for a six-channel wind panel.

Index Terms—Ac–dc conversion, electromagnetic energy harvesting, multiinput converter, resonant converter, wind energy.

I. INTRODUCTION

WIND energy is considered as one of the most promising alternative sources. Flapping electromagnetic-reed (EMR) generators, suitable to energize standalone small-size and low-power electrical loads, such as light poles and wireless sensors, can harvest energy from wind-induced vibration [1]. The EMR generators, such as the wind energy harvesting cell proposed in [2], are suitable for virtually any wind conditions including turbulent wind found in many real-world settings [3]–[5]. Power electronic interfaces (PEI) are required to condition the output power of energy harvesters and effectively deliver power to the loads [6]–[14], as shown in Fig. 1 (a). Power density and efficiency at low power conversion are important performance metrics of power converters for low-speed wind energy harvesting. One of the main challenges toward fabricating efficient converters for EMR generators is the efficient rectification of low-amplitude generator voltages [15], [16]. Due to large voltage drop of diodes, the conventional diode-bridge topologies are inefficient and in some cases impractical for low-voltage and low-power energy harvesting applications.

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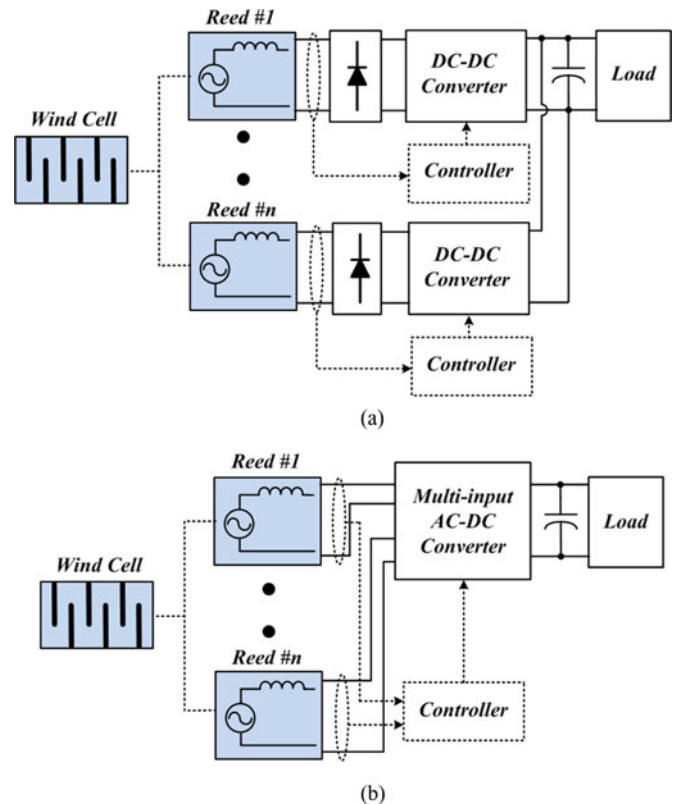


Fig. 1. Multichannel EMR generators and PEI system: (a) conventional PEI and (b) proposed multiinput PEI.

The majority of efforts toward efficient rectification of low-voltage irregular outputs of EMR generators has focused on replacing the conventional p-n junction diodes with modified MOSFET connection, including diode-connected MOSFETs [17], gate cross-coupled MOSFETs [18], and active diodes [19]. These technologies reduce the forward voltage-drop of diodes through different connections of MOSFET terminals. However, such rectifiers suffer from losses caused either by gate threshold voltage or turn-on resistance of MOSFET. Moreover, these passive rectifiers require a second stage to regulate electrical impedance, inherently reducing the efficiency.

Researchers have studied single-stage bridgeless ac–dc switching converters, including H-bridge converter [20], ac–dc boost converter, ac–dc buck-boost converter [21] and ac–dc switch-inductor boost converter [22]. These topologies provide ac–dc conversion and impedance regulation through either using bidirectional transistors and split capacitors, or paralleling dc–dc converters. However, they suffer from drawbacks in terms of inefficient hard switching, asymmetrical operation and complicated high-side gate drive. Soft-switching converters are

considered for efficient rectification at high switching frequencies [23], [24]. Majority of investigations on soft-switching converters focus on modification of hard-switching converters, referred as quasi-resonant converters. They utilize auxiliary LC resonant circuits to primarily shape the current and voltage of main transistors during the turn-on and turn-off of the switching components [25], [26]. However, the auxiliary resonant circuits result in additional footprint, switching losses and conduction losses. Resonant converters, such as synchronized switch harvesting on inductor converters [27]–[30], utilize passive resonant networks as low impedance branches across transistors. They eliminate the switching losses without additional bulky components. However, a postponed bridge rectification stage, such as a dual-stage resonant boost converter, is required for ac–dc conversion [31].

In this paper, a new multiinput bridgeless resonant ac–dc converter, illustrated in Fig. 1 (b), is proposed to efficiently convert low-amplitude alternative voltages of multiple EMR generators into a regulated dc output voltage. The topology is capable of interfacing multiple, independent, alternating, input sources without using a diode bridge. Very-low-amplitude voltages can be stepped up to a relatively high voltage. Larger number of input sources results even in higher output voltage. The resonance nature of operation eliminates the switching losses, and allows high frequency switching. It also enables miniaturization through using substantially smaller capacitive and magnetic components. The multiinput converter uses the self-inductance of input sources, and only utilizes one magnetic component and one diode. Furthermore, this topology reduces the voltage stress on power transistors. In summary, the proposed topology has superiorities in terms of: 1) capability of interfacing and conditioning the output power of multiple EMR generators; 2) no need for inefficient diode bridge; 3) small number and value of passive components; 4) high voltage gain at 0.5 switching duty cycle; 5) resonant lossless switching; 6) relatively low stress on transistors; and 7) simple gate drive.

This paper is organized as follows. Section II presents the operation principle of an EMR generator and the modeling of a breeze energy system. Soft-switching operation modes, steady-state analyses and optimal impedance matching are investigated in Section III. The design specification for a six-input converter and experimental results are presented in Section IV. Finally, Section V concludes the paper.

II. MULTIINPUT RESONANT AC–DC CONVERTER

A. EMR Generator Model

According to the general model of an electromagnetic system, one EMR generator can be modeled as a bipolar time-varying electromotive force (EMF or open-circuit voltage, v_{emf}) connected in series with an inner resistance (coil resistance, r_{EMR}) and a self-inductance (coil inductance, L_{EMR}) [32]. Both the equivalent mechanical and electrical models are presented in Fig. 2. The PEI needs to be designed to achieve: 1) rectification, 2) voltage step-up, 3) impedance matching, and 4) high efficiency operation across wide output voltages.

Conventionally, the EMR generators are interfaced with diode bridges for rectification [6]. Due to the rectification, the equivalent open-circuit voltage ($|v_{emf}|$) is equal to the absolute value

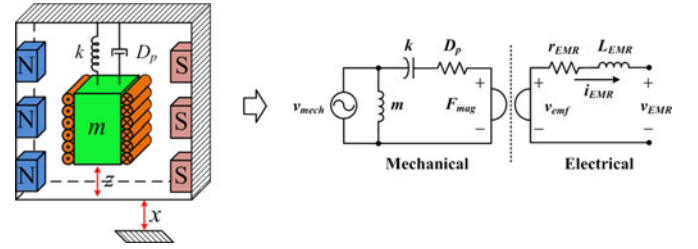


Fig. 2. Equivalent circuit of one EMR generator (v_{mech} : mechanical velocity; m : proof mass; k : spring stiffness; D_p : mechanical damping; F_{mag} : magnetic force; v_{emf} : EMF; r_{EMR} : coil resistance; L_{EMR} : self-inductance; i_{EMR} : reed terminal current; v_{EMR} : reed terminal voltage).

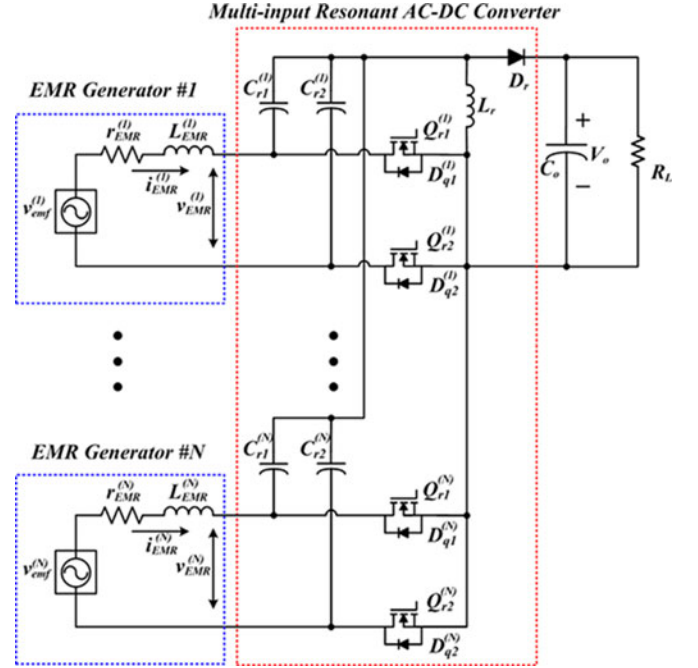


Fig. 3. Illustrative scheme of the proposed multiinput converter ($v_{emf}^{(i)}$: EMF of # i reed; $r_{EMR}^{(i)}$: coil resistance; $L_{EMR}^{(i)}$: self-inductance; $i_{EMR}^{(i)}$: reed terminal current; $v_{EMR}^{(i)}$: reed terminal voltage; $C_{r1}^{(i)} = C_{r2}^{(i)}$: resonant capacitors; L_r : resonant inductor; $Q_{r1}^{(i)}, Q_{r2}^{(i)}$: MOSFETs; D_r : output diode; C_o : output capacitor).

of EMF. First, since the load is connected in series with the reed, the load root-mean-square (RMS) voltage is lower than RMS value of generator EMF. However, in most of cases, a load voltage higher than EMF is required (i.e., for battery charging). As a result, a switching power converter capable of stepping up the EMF is necessary. Second, in order to extract the maximum power from a source, the equivalent input impedance (Z_{in}) of the circuit should be set equal to the optimal impedance (Z_{opt}) for impedance matching [33]. However, with a wide range of load (R_L), it is difficult and impractical to adjust the input impedance to the optimal value by using a diode bridge. A switching power converter should be used to adjust the impedance (Z_r) and regulate the input impedance (Z_{in}) for optimal impedance matching.

B. Circuit Description

Fig. 3 illustrates the proposed multiinput single-stage bridgeless resonant ac–dc converter. The topology is capable of

operating with multiple inductive sources. Each inductive source, in this case EMR generator, is numbered with $i = 1, 2, \dots, N$. The multiinput circuit is formed by one resonant inductor, one diode and multiple MOSFET-capacitor bridges. Each input source is connected to two MOSFET-capacitor bridges ($Q_{r1}^{(i)}, C_{r1}^{(i)}$ and $Q_{r2}^{(i)}, C_{r2}^{(i)}$), which share a resonant inductor and a diode.

The resonant inductor (L_r) is tuned with resonant capacitors ($C_{r1}^{(i)}$ and $C_{r2}^{(i)}$) to resonate at the resonant frequency (f_r) near the switching frequency (f_s). Ideally, the resonant components ($L_r, C_{r1}^{(i)}$ and $C_{r2}^{(i)}$) present a lossless low drain-to-source impedance branch across MOSFETs near f_s . The LC network amplifies the EMF. $C_{r1}^{(i)}$ and $C_{r2}^{(i)}$ are in parallel with MOSFETs and the diode to ensure ZVS at turning-on and turning-off. Switching losses are eliminated through the oscillating voltage and current, while the input energy is stored and released to the load through the active LC network. Furthermore, the tuned networks eliminate overvoltage spikes as well as diode reverse recovery issues while simultaneously maintaining low peak voltage stresses on MOSFETs.

MOSFETs ($Q_{r1}^{(i)}$ and $Q_{r2}^{(i)}$) are actively turned on and off with duty cycle close to 0.5 in order to generate drain-to-source pulse voltage ($v_{ds1}^{(i)}$ and $v_{ds2}^{(i)}$) at the input of LC network. The amplitude of drain-to-source pulse voltage is higher than v_{emf} due to the energy stored in $L_{EMR}^{(i)}$. The fundamental component of $v_{ds1}^{(i)}$ and $v_{ds2}^{(i)}$ pass through the LC network and generate an amplified oscillating voltage near f_s across the rectifier diode D_r . ZVS and ZCS operation of $Q_{r1}^{(i)}$ and $Q_{r2}^{(i)}$ provide energy recovery to drain-to-source MOSFET parasitic capacitor (C_{ds}), which in turn increases the efficiency. The drain-to-source overvoltage spike is eliminated; thereby no snubber circuit is required. The output diode (D_r) rectifies the amplified oscillating voltage after the tuned network into a dc output voltage. It generates freewheeling path for the resonant inductor current as well as charging path for the resonant capacitors.

C. Operation Mode Analyses

In a switching cycle, one EMR generator can be assumed as a current source if the generator has a large self-inductance. In this case, for simplicity of analysis, each EMR generator is assumed as a current source ($I_{in}^{(i)}$) during a switching period. In a mechanical vibration cycle, $I_{in}^{(i)}$ is positive at positive half cycle and is negative at negative half cycle. There are three switching submodes during one switching cycle at either positive half cycle or negative half cycle. For simplicity, the switching submodes of a dual-input topology with a positive input and a negative input, as illustrated in Fig. 4 (a), are presented in steady state. The analyses can be analogously extended to the multiinput topology. For a positive input ($I_{in}^{(1)}$), $Q_{r1}^{(1)}$ is conducting in all submodes while $Q_{r2}^{(1)}$ is switching; for a negative input ($I_{in}^{(2)}$), $Q_{r1}^{(2)}$ is conducting in all submodes while $Q_{r2}^{(2)}$ is switching. Therefore, the rectification of each bipolar source is achieved by conducting $Q_{r2}^{(i)}$ while switching $Q_{r1}^{(i)}$ at positive input cycle; or conducting $Q_{r1}^{(i)}$ while switching $Q_{r2}^{(i)}$ at negative input cycle.

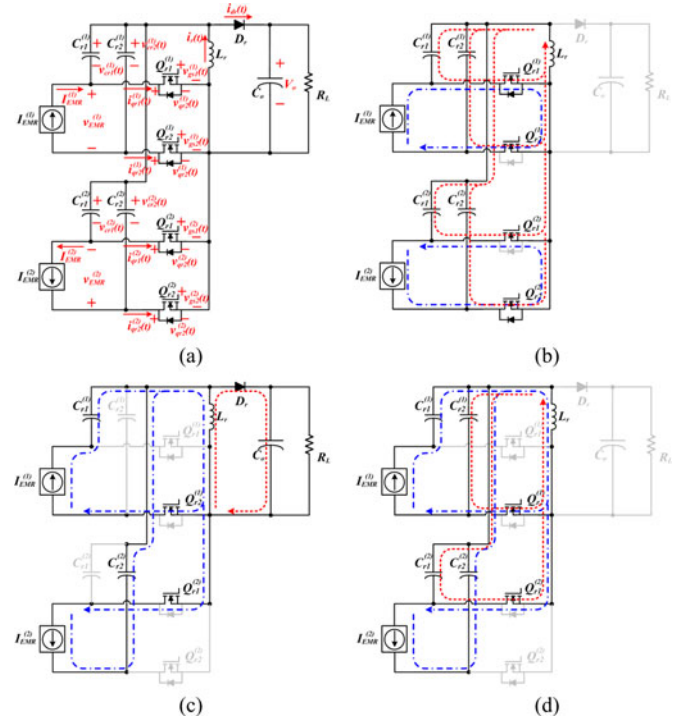


Fig. 4. (a) Simplified topology of dual-input resonant converter with a positive input and a negative input. (b)–(d) Switching submodes during a switching cycle.

The simulation waveforms of reed #1 during one switching cycle are demonstrated in Fig. 5. The switching frequency is close to the resonant frequency (f_r) of $L_r C_{r1} C_{r2}$ network, and the switching duty cycle is close to 0.5. Relative to the resonant capacitors, C_o has very small impedance at f_s , thereby it acts as a voltage source and can be considered as a constant value, V_o . The capacitances $C_{r1}^{(i)}$ and $C_{r2}^{(i)}$ ($i = 1, 2$) are set equal to C_r for the impedance matching.

At t_0 : (circuit initial state) It is assumed that $I_{EMR}^{(1)}$ and $I_{EMR}^{(2)}$ have initial values. $v_{cr1}^{(1)}, v_{cr2}^{(1)}, v_{cr1}^{(2)}$ and $v_{cr2}^{(2)}$ have the same negative initial values, $v_{cr1}(t_0)$. $Q_{r1}^{(1)}$ and $Q_{r2}^{(2)}$ are both off before t_0 .

Mode I ($t_0 \sim t_1$): At t_0 , as shown in Fig. 4 (b), body diodes of $Q_{r1}^{(1)}$ and $Q_{r2}^{(2)}$ are turned on at zero voltage [$v_{qr1}^{(1)}(t_0)$ and $v_{qr2}^{(2)}(t_0)$] (equal to $v_{cr2}^{(1)} - v_{cr1}^{(1)}$). As soon as the currents through body diodes reduce to zero, $Q_{r1}^{(1)}$ and $Q_{r2}^{(2)}$ are turned on at zero voltage [$v_{qr1}^{(1)}(t_0)$ and $v_{qr2}^{(2)}(t_0)$] (equal to $v_{cr2}^{(1)} - v_{cr1}^{(1)}$) and zero current [$i_{qr1}^{(1)}(t_0)$ and $i_{qr2}^{(2)}(t_0)$]. The self-inductances of reeds start to get charged by the input EMFs, thereby $I_{EMR}^{(1)}$ and $I_{EMR}^{(2)}$ increase linearly. $C_{r1}^{(1)}, C_{r2}^{(1)}, C_{r1}^{(2)}$ and $C_{r2}^{(2)}$ begin to resonate with L_r at f_r . $v_{cr1}^{(1)}, v_{cr2}^{(1)}, v_{cr1}^{(2)}$ and $v_{cr2}^{(2)}$ are equal and increase sinusoidally until they are equal to V_o at t_1 . The governing equations in terms of $v_{cr1}^{(i)}, v_{cr2}^{(i)}$ and i_r are

$$\begin{cases} \frac{\partial i_r(t)}{\partial t} = -\frac{v_{cr1}^{(i)}(t)}{L_r} = -\frac{v_{cr2}^{(i)}(t)}{L_r} \\ \frac{\partial v_{cr1}^{(i)}(t)}{\partial t} = \frac{\partial v_{cr2}^{(i)}(t)}{\partial t} = \frac{i_r(t)}{2NC_r} \end{cases} \quad (1)$$

$$(2)$$

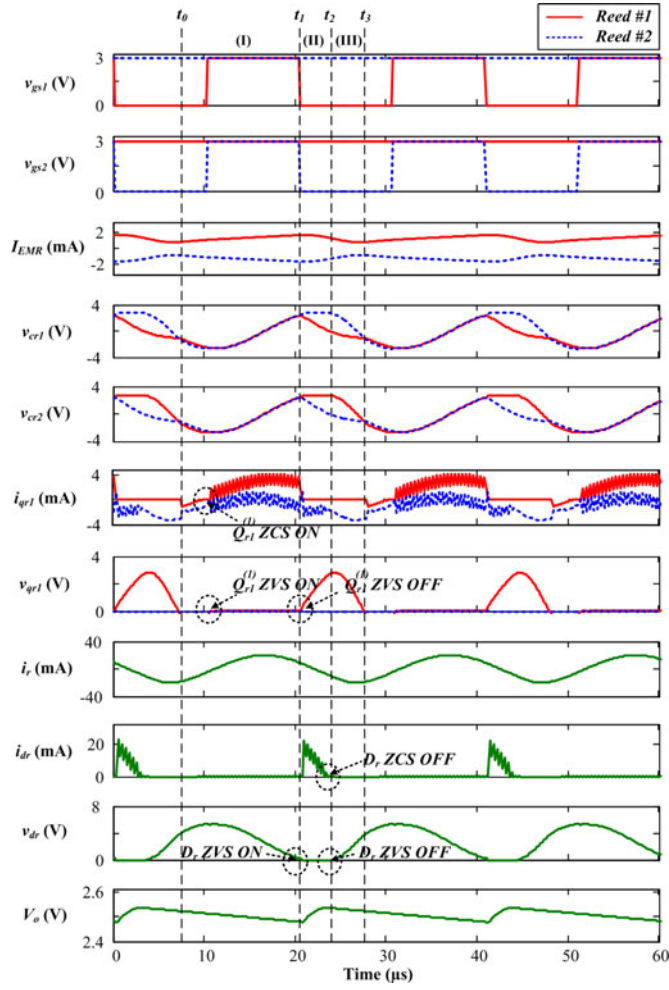


Fig. 5. Simulation waveforms of reed #1 during a switching cycle.

where $N = 2$ is the number of input sources. Hence, the transient state of i_r and $v_{cr1}^{(i)}$ can be expressed as

$$\begin{cases} i_r(t) = i_r(t_0) \cos \omega_r(t - t_0) - \frac{v_{cr1}^{(i)}(t_0)}{Z_r} \sin \omega_r(t - t_0) & (3) \\ v_{cr1}^{(i)}(t) = v_{cr2}^{(i)}(t) = v_{cr1}^{(i)}(t_0) \cos \omega_r(t - t_0) \\ + i_r(t_0) Z_r \sin \omega_r(t - t_0) & (4) \end{cases}$$

where $\omega_r = (2NL_rC_r)^{-1/2}$ is the angular resonance frequency of LC network; and $Z_r = (L_r/2NC_r)^{1/2}$ is the characteristic impedance of LC network. Assuming $i_r(t_0)$ is very small, the first component of $i_r(t)$ and the second component of $v_{cr1}^{(i)}(t)$ can be neglected.

Mode II ($t_1 \sim t_2$): At t_1 , $Q_{r1}^{(1)}$ and $Q_{r2}^{(2)}$ are turned off at zero voltage [$v_{qr1}^{(1)}(t_1)$ and $v_{qr2}^{(2)}(t_1)$], as depicted in Fig. 4 (c). The energy stored in the reed self-inductance begins to be transferred to L_r . D_r turns on at zero voltage [$v_{dr}(t_1)$], allowing the current (equal to $I_{EMR}^{(1)} + I_{EMR}^{(2)} + i_r$) to freewheel through D_r . i_r decreases linearly due to V_o across L_r . In this mode, $v_{cr1}^{(1)}$ and $v_{cr2}^{(2)}$ decrease; however, $v_{cr2}^{(1)}$ and $v_{cr1}^{(2)}$

remain equal to V_o . The governing equations are

$$\frac{\partial i_r(t)}{\partial t} = -\frac{V_o}{L_r} \quad (5)$$

$$\frac{\partial v_{cr1}^{(1)}(t)}{\partial t} = -\frac{I_{EMR}^{(1)}}{C_r} \quad (6)$$

$$\frac{\partial v_{cr2}^{(2)}(t)}{\partial t} = -\frac{I_{EMR}^{(2)}}{C_r}. \quad (7)$$

$v_{cr1}^{(1)}$ and $v_{cr2}^{(2)}$ decrease linearly from their boundary condition $v_{cr1}^{(1)}(t_1) = v_{cr2}^{(2)}(t_1) = V_o$, yielding

$$i_r(t) = -\frac{V_o}{L_r}(t - t_1) + i_r(t_1) \quad (8)$$

$$v_{cr1}^{(1)}(t) = -\frac{I_{EMR}^{(1)}}{C_r}(t - t_1) + V_o \quad (9)$$

$$v_{cr2}^{(2)}(t) = -\frac{I_{EMR}^{(2)}}{C_r}(t - t_1) + V_o \quad (10)$$

$$v_{cr2}^{(1)}(t) = v_{cr1}^{(2)}(t) = V_o. \quad (11)$$

Mode III ($t_2 \sim t_3$): At t_2 , i_{dr} drops to zero, leaving no current to freewheel through D_r . D_r turns off at both zero voltage [$v_{dr}(t_2)$] and zero current [$i_{dr}(t_2)$] at t_2 . $C_{r2}^{(1)}$, $C_{r1}^{(2)}$, and L_r begin to resonate, until $v_{cr2}^{(1)}$ and $v_{cr1}^{(2)}$ are respectively equal to $v_{cr1}^{(1)}$ and $v_{cr2}^{(2)}$ at t_3 . The governing equations of Mode III can be represented as

$$\frac{\partial i_r(t)}{\partial t} = -\frac{v_{cr2}^{(1)}(t)}{L_r} = -\frac{v_{cr1}^{(2)}(t)}{L_r} \quad (12)$$

$$\frac{\partial v_{cr2}^{(1)}(t)}{\partial t} = \frac{\partial v_{cr1}^{(2)}(t)}{\partial t} = \frac{\sum_{i=1}^N I_{EMR}^{(i)} + i_r(t)}{NC_r}. \quad (13)$$

The governing equations of $v_{cr1}^{(1)}$ and $v_{cr2}^{(2)}$ are similar to those in Mode II. By applying boundary conditions $v_{cr2}^{(1)}(t_2) = v_{cr1}^{(2)}(t_2) = V_o$ and $i_r(t_2) = -I_{EMR}^{(1)} - I_{EMR}^{(2)}$, i_r , $v_{cr2}^{(1)}$ and $v_{cr1}^{(2)}$ can be presented as

$$i_r(t) = -\sum_{i=1}^N I_{EMR}^{(i)} - \frac{V_o}{\sqrt{2}Z_r} \sin \sqrt{2}\omega_r(t - t_2) \quad (14)$$

$$v_{cr2}^{(1)}(t) = v_{cr1}^{(2)}(t) = V_o \cos \sqrt{2}\omega_r(t - t_2). \quad (15)$$

At t_3 : the body diodes of $Q_{r1}^{(1)}$ and $Q_{r2}^{(2)}$ turn on, both at zero voltage, and $v_{cr1}^{(1)}$ and $v_{cr2}^{(2)}$ become equal. Consequently, the circuit returns to the original state.

Since the integral of the current through C_o over one switching period at steady state equals to zero; $\int i_{dr}(t) dt = V_o/R_L f_s$. Using the boundary condition $i_r(t_1) = i_{dr,peak}/2$, the following equation can be obtained as:

$$i_r(t_1) = V_o \sqrt{\frac{1}{2L_r R_L f_s}}. \quad (16)$$

On the other hand, the integral of the current through $C_{r1}^{(1)}$ over one switching period at steady state equals to zero; $v_{cr1}^{(1)}(t_0) = v_{cr1}^{(1)}(t_3)$. Using the boundary condition $v_{cr1}^{(1)}(t_1) = V_o$, the following equation can be obtained from

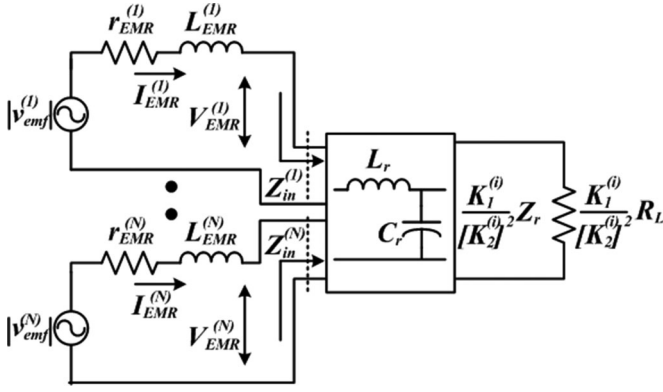


Fig. 6. Impedance network of an N -channel system.

(3), (4), (9), and (16).

$$V_o \left[1 - \sqrt{\frac{\omega_r Z_r}{2R_L f_s}} \sin \omega_r (t_1 - t_0) - \cos \omega_r (t_1 - t_0) \right] \\ = \frac{t_3 - t_1}{NC_r} \sum_{i=1}^N I_{EMR}^{(i)}. \quad (17)$$

By setting the transistor turn-on time $t_{on} = t_1 - t_0 = 3T_r/4 = 3\pi/2\omega_r$ and $R_L \gg Z_r$, V_o can be represented in terms of the sum of $I_{EMR}^{(i)}$ as

$$V_o = \frac{t_{off}}{NC_r} \sum_{i=1}^N I_{EMR}^{(i)} \quad (18)$$

where $t_{off} = t_3 - t_1 = T_s - t_{on}$ is the turn-off time interval. By introducing the ratio of angular resonance frequency to switching frequency ($\gamma = f_r/f_s$) and the coefficient $\alpha = 4\gamma - 3$, (18) can be written as

$$V_o = \alpha \pi Z_r \sum_{i=1}^N I_{EMR}^{(i)}. \quad (19)$$

The switching frequency (f_s) is slightly higher than the resonance frequency (f_r). When $R_L \gg Z_r$, the converter can be considered as a current-controlled voltage-source. The impedance network of an N -channel system during one switching period is shown in Fig. 6. V_o is determined by the sum of the reed currents ($I_{EMR}^{(i)}$), through setting the network impedance (Z_r) to a desired value. At the same Z_r and load condition, connecting more EMR generators results in a higher output voltage. Furthermore, the output voltage regulation can be achieved through real-time pulse frequency modulation (PFM).

III. OPTIMAL IMPEDANCE MATCHING

To extract the maximum power from an N -channel system, it is necessary to acquire the equivalent input impedance of an N -input converter depicted in Fig. 6. In the case of a resistive load (R_L) comparable to Z_r , in one switching period, including R_L in V_o yields

$$\frac{V_o}{\sum_{i=1}^N I_{EMR}^{(i)}} = \frac{\alpha \pi Z_r}{1 + \sqrt{\pi Q_r \gamma}} \quad (20)$$

where $Q_r = Z_r/R_L$ denotes the quality factor. According to (20) and $V_o = I_o R_L$, the output-to-input current ratio can be represented as

$$\frac{I_o}{\sum_{i=1}^N I_{EMR}^{(i)}} = \frac{\alpha \pi Q_r}{1 + \sqrt{\pi Q_r \gamma}}. \quad (21)$$

In a vibration cycle, considering the conversion efficiency (η_{ff}), the generated power ($P_{EMR}^{(i)}$) from each reed and the output power (P_o) of the converter should satisfy

$$P_{EMR}^{(i)} = V_{EMR}^{(i)} I_{EMR}^{(i)} = K_1^{(i)} \sum_{i=1}^N P_{EMR}^{(i)} \quad (22)$$

$$I_{EMR}^{(i)} = K_2^{(i)} \sum_{i=1}^N I_{EMR}^{(i)} \quad (23)$$

$$P_o = V_o I_o = \eta_{ff} \sum_{i=1}^N P_{EMR}^{(i)} \quad (24)$$

$$Z_{in}^{(i)} = \frac{V_{EMR}^{(i)}}{I_{EMR}^{(i)}} = \frac{P_{EMR}^{(i)}}{[I_{EMR}^{(i)}]^2} \quad (25)$$

where $V_{EMR}^{(i)}$ is the average terminal voltage of the i th reed during one switching period, $K_1^{(i)}$ is the percentage value of i th reed's generated power over total power, $K_2^{(i)}$ is the percentage value of i th reed's terminal current over total current, $Z_{in}^{(i)}$ is the equivalent input impedance of the i th input connected to the i th reed. In a switching period, $K_1^{(i)}$ and $K_2^{(i)}$ can be assumed constant due to the assumption that the reed current ($I_{EMR}^{(i)}$) and the reed power ($P_{EMR}^{(i)}$) are constant. However, $K_1^{(i)}$ and $K_2^{(i)}$ are variable in a vibration cycle, since the reed current and the reed power oscillate at vibration frequency.

Based on (25), the equivalent input impedance connected to the i th reed can be expressed as

$$Z_{in}^{(i)} = \frac{K_1^{(i)}}{[K_2^{(i)}]^2} \frac{(\alpha \pi Q_r)^2 R_L}{\eta_{ff} (1 + \sqrt{\pi Q_r \gamma})^2} = \frac{K_1^{(i)}}{[K_2^{(i)}]^2} Z_{in} \quad (26)$$

where Z_{in} is the equivalent input impedance of the i th input connected to the i th reed while other reeds are disconnected. $Z_{in}^{(i)}$ is constant during a switching period; however, it changes in a vibration cycle due to the variable coefficients $K_1^{(i)}$ and $K_2^{(i)}$. The impedance network of an N -channel system during one vibration cycle is illustrated in Fig. 6. The characteristic impedance of the converter and load impedance are different for each reed, and they change during a vibration cycle. The total input power of the circuit is given by

$$\sum_{i=1}^N P_{EMR}^{(i)} = \sum_{i=1}^N [I_{EMR}^{(i)}]^2 Z_{in}^{(i)} = Z_{in} \sum_{j=1}^N K_1^{(j)} \left[\sum_{i=1}^N I_{EMR}^{(i)} \right]^2 \\ = Z_{in} \left[\sum_{i=1}^N I_{EMR}^{(i)} \right]^2. \quad (27)$$

According to the general model of EMR generators, the maximum power of the i th reed connected to the i th input can be extracted when the input impedance of the i th input equals to the optimal input impedance ($Z_{in}^{(i)} = Z_{opt}$). However, for an

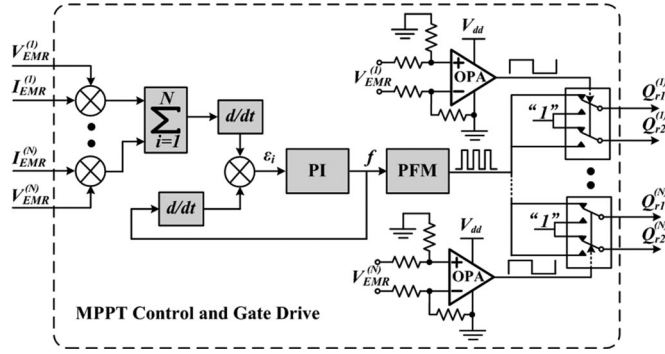


Fig. 7. Illustrative scheme of the MPPT control strategy and gate drive.

N -channel system connected to an N -input converter, the maximum power of the N -channel system is extracted as

$$P_{EMR,max} = Z_{in} \left[\sum_{i=1}^N I_{EMR}^{(i)} \right]^2 \Bigg|_{Z_{in} = Z_{opt}^*} \quad (28)$$

where $Z_{opt}^* \leq Z_{opt}$. (28) reveals the fact that the total maximum power of an N -channel system is extracted through achieving optimal impedance matching of the N -input converter ($Z_{in} = Z_{opt}^*$) rather than optimal impedance matching of each input ($Z_{in}^{(i)} = Z_{opt}$). The optimal impedance can be acquired by setting characteristic impedance (Z_r) of LC network.

In addition to the impedance matching through LC network design, the input impedance can be further regulated in real time by PFM. Increasing the switching frequency (f_s) reduces the equivalent input impedance (Z_{in}). A closed-loop PI control is used to acquire the optimal switching frequency (f_{opt}) corresponding to Z_{opt} . f_s is increased when both derivations of the total input power and f_s have the same polarity; on the other hand, it is decreased when the derivation polarities of the total input power and f_s are different. The maximum power point tracking (MPPT) control and gate drive system is demonstrated in Fig. 7. Zero-crossing comparators are used to generate pulse signals synchronous with the reed vibration. Demultiplexer switches are controlled by pulse signals to produce MOSFET gating signals.

IV. EXPERIMENTAL RESULTS

A $5 \text{ cm} \times 3 \text{ cm}$, six-input prototype of the proposed resonant converter, illustrated in Fig. 8, is fabricated for power conditioning of six EMR generators. Components and design parameters of the prototype PEI are listed in Table I.

A. PEI Design Specification

In order to achieve optimal impedance matching for a six-channel system, the characteristic impedance (Z_r) is set equal to 133Ω . For a more powerful source, Z_r and Z_{in} can be reduced through either reducing the resonant inductance or increasing resonant capacitance. Moreover, the natural frequency (f_r) is set to 45 kHz with consideration of resonant losses and size miniaturization. Higher resonant frequency leads to smaller size of passive components; however, it results in higher resonant losses. The switching frequency (f_s) is chosen to be 48 kHz , slightly higher than f_r . The resonant inductor and resonant ca-

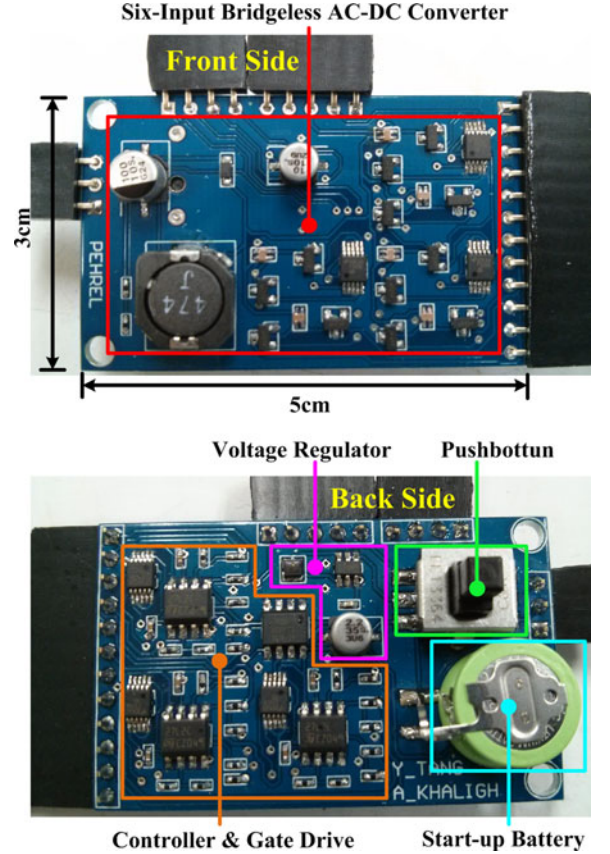


Fig. 8. Six-input standalone prototype of the proposed resonant ac-dc converter for a six-channel EMR-generator system.

TABLE I
COMPONENT PARAMETERS OF EMR GENERATOR AND PEI

Parameter	Description	Quantity	Part Number
Power Converter			
Resonant capacitor ($C_{r1}^{(1)\sim(6)}, C_{r2}^{(1)\sim(6)}$)	2.2 nF, 50 V, Array	six	CKCM25×8R1H222M060AK
Resonant inductor (L_r)	$470 \mu\text{H}$, 0.62 A , 0.89Ω	one	MSS1048-474KL
Output dc capacitor (C_o)	$100 \mu\text{F}$, 10 V	one	EEE-1AA101WR
Transistor ($Q_{r1}^{(1)\sim(6)}, Q_{r2}^{(1)\sim(6)}$)	20 V , 1.9 A , $63 \text{ m}\Omega$	12	IRLML2030TRPBF
Diode (D_r)	20 V , 0.5 A , 0.3 V	one	DB2J20900L
Control Board			
Microcontroller	3V , $144 \mu\text{A}$, 2 PWM , 3 ADC	one	ATTINY13V-10SSU
OpAmp	$0.04 \text{ V}/\mu\text{s}$, 3 V , $14 \mu\text{A}$	three	TS27L2CDT
Resistor	$100 \text{ k}\Omega$	12	MCR01MRTJ104
Capacitor	1 nF , 10 V	six	C1005JB1H102K050BA

pacitors are determined as

$$\begin{cases} L_r = \frac{Z_r}{2\pi f_r} & (29) \end{cases}$$

$$\begin{cases} C_r = \frac{1}{4\pi N f_r Z_r} & (30) \end{cases}$$

In order to reduce the resonant losses at low power, a $470\text{-}\mu\text{H}$ inductor with low dc resistance is selected as the resonant inductor (L_r , much smaller than the self-inductance of the EMR generator). $2.2\text{-nF}/50\text{-V}$ ceramic capacitor arrays are used as resonant capacitors. The MOSFETs with high compactness, fast

TABLE II
ELECTRICAL PERFORMANCES OF TESTBED

Parameter	Nominal Value
Power Amplifier	
Input frequency (f_{in})	20~50 Hz
Open-circuit voltage (v_{emf})	0.5~3 V (rms)
Terminal voltage (V_{EMR})	2.5~7 V (peak)
Output power (P_{EMR})	1.8~34 mW
EMR Generator	
Input frequency (f_{in})	15 Hz
Open-circuit voltage (v_{emf})	0.5~2.5 V (rms)
Reed terminal voltage (V_{EMR})	2.5~5 V (peak)
Output power (P_{EMR})	1~4.5 mW
Power Converter	
Switching frequency (f_s)	48 kHz
Resonant frequency	45 kHz
Characteristic impedance	133 Ω
Output voltage	1.2~5.5 V
Output power	1.5~30 mW
Efficiency	85~91%

transient response and low on-resistance are selected to reduce conduction losses. These MOSFETs have low gate charge and low gate voltage (as low as 2 V). Hence, the circuit can operate with low supply voltage, and will have low gating power dissipation as well as fast transient response. The rectifier diode (D_r) is selected based on the low forward voltage and low on-resistance. Low-power components are selected in the control board to enhance the standalone capability. ATTINY13V is used as the controller due to its low profile package, low power consumption and minimum required functions. Opamps and demultiplexer switches are adopted to detect the input polarity and provide gating signals.

B. Energy Harvesting Testbed

The prototype is tested with different sources to evaluate the operational performance at low power. The summary of circuit performance is listed in Table II. A 1-V dc source corresponding to 1-V EMF of the EMR generator is first applied to demonstrate the circuit's switching operation. A 1.5-mH inductor corresponding to the self-inductance of the EMR generator is placed in conjunction with each input. The experimental waveforms at 48-kHz switching frequency are presented in Fig. 9(a) and (b). High switching frequency can be achieved by tuning the network, illustrated in Fig. 9(c). According to the switching waveforms, the MOSFETs and the diode have ZVS turn-on and turn-off characteristics at the switching frequency close to the resonant frequency. A 2.4-V maximum output voltage is acquired through a 1-k Ω dc resistive load, with 94.4% dc-dc conversion efficiency.

A power amplifier circuit in conjunction with 1.5-mH inductors at each input is utilized to emulate the inductive ac sources. In this case, the overall input power is evenly split between six channels. The rms voltage of the power amplifier is set at 0.5~3 V corresponding to the EMF voltage, v_{emf} , of EMR generators. 20~50-Hz ac frequency corresponding to low mechanical vibration frequency of EMR generator is selected to investigate the case of ambient energy harvesting. The measured output power varies from 1.5 to 30 mW at 20-Hz ac input power, as presented in Fig. 10(a) and (b), respectively. The converter shows good performance on rectification of a wide range

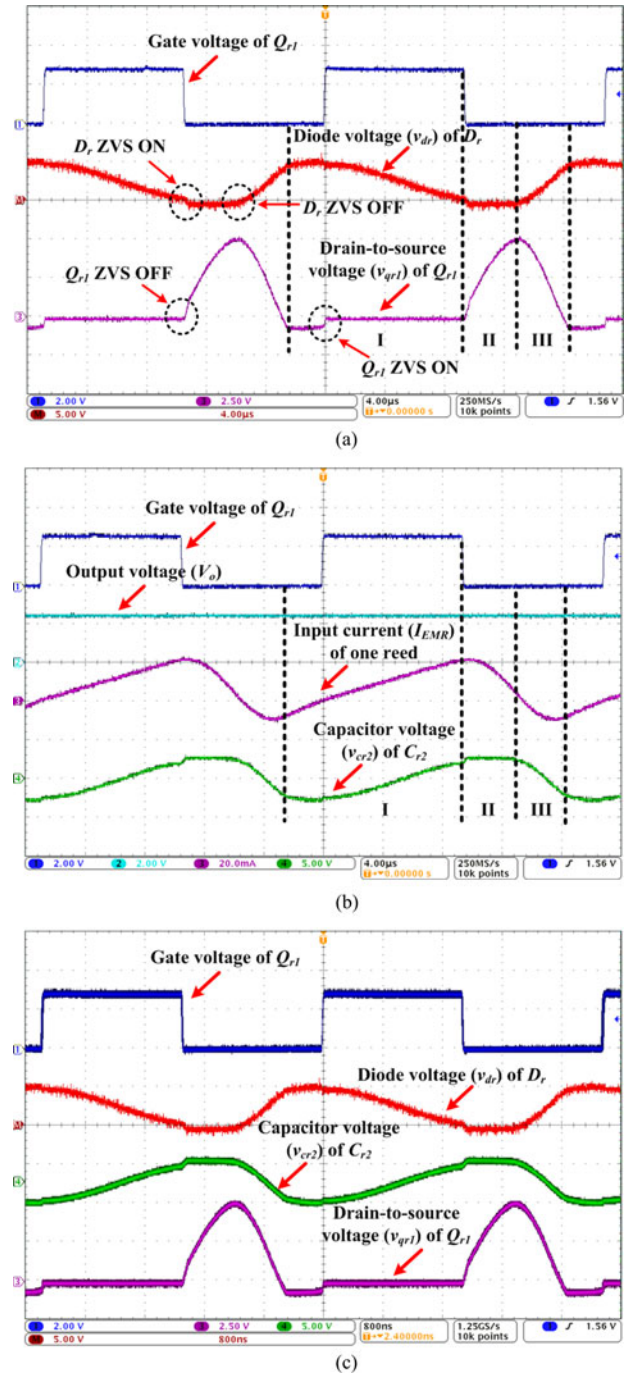
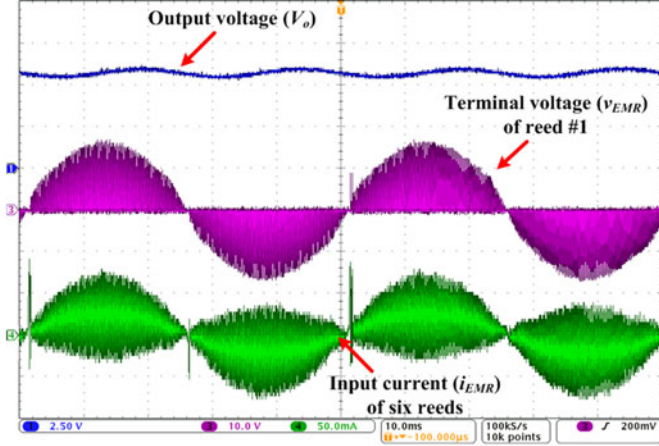


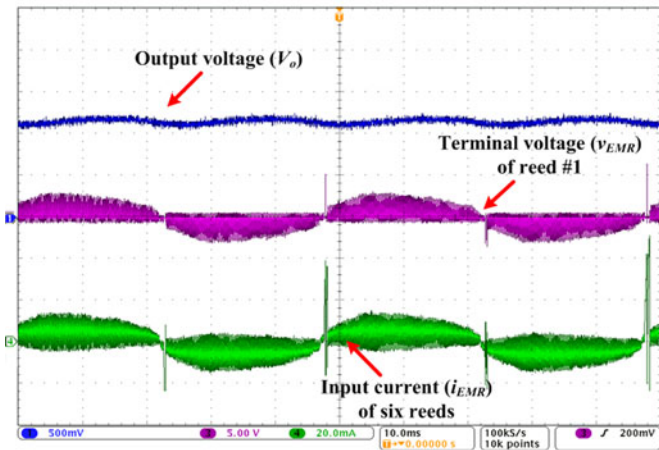
Fig. 9. Experimental waveforms of switching operation: $v_{emf} = 1V$, $f_s = 48$ kHz; x-axis: $4 \mu s/div$; y-axis: (a) Ch1 = gate voltage of Q_{r1} , 2 V/div; Ch3 = drain-to-source voltage (v_{qr1}) of Q_{r1} , 2.5 V/div; ChM = diode voltage (v_{dr}) of D_r , 5 V/div; and (b) Ch1 = gate voltage of Q_{r1} , 2 V/div; Ch2 = output voltage (V_o), 2 V/div; Ch3 = input current (I_{EMR}) of one input, 20 mA/div; Ch4 = inductor voltage (v_r), 5 V/div; and (c) $f_s = 200$ kHz, Ch1 = gate voltage of Q_{r1} , 2 V/div; Ch3 = drain-to-source voltage (v_{qr1}) of Q_{r1} , 2.5 V/div; Ch4 = inductor voltage (v_r), 5 V/div; ChM = diode voltage (v_{dr}) of D_r , 5 V/div.

of EMF voltages (from 0.5 to 3 V). Furthermore, experimental waveforms with step change of input ac power and input ac frequency are illustrated in Fig. 11(a) and (b), respectively. The ac-dc conversion efficiency is above 80% even at very-low input voltage and power.

There are two types of losses: 1) power stage losses and 2) controller losses. Since the switching losses are eliminated



(a)



(b)

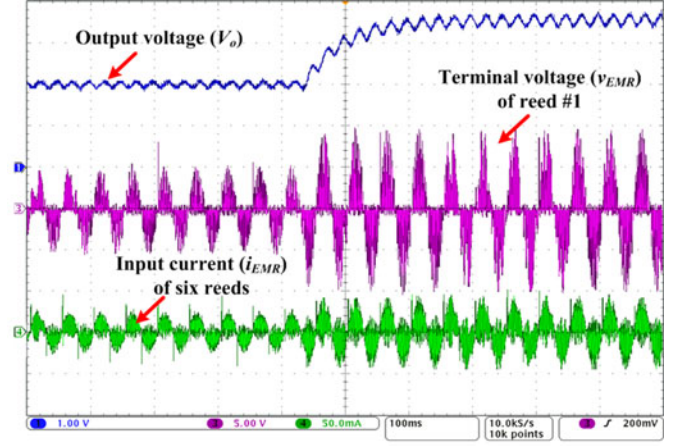
Fig. 10. Experimental waveforms of power amplifiers: $f_{in} = 20$ Hz; x-axis: 10 ms/div; y-axis: (a) $v_{emf} = 3 V_{rms}$; Ch1 = output voltage (V_o), 2.5 V/div; Ch2 = terminal voltage (v_{EMR}) of reed #1, 10 V/div; Ch3 = input current (i_{EMR}) of six reeds, 50 mA/div; and (b) $v_{emf} = 0.5 V_{rms}$; Ch1 = output voltage (V_o), 0.5 V/div; Ch2 = terminal voltage (v_{EMR}) of reed #1, 5 V/div; Ch3 = sum of the input currents (i_{EMR}) of six reeds, 10 mA/div.

through the resonant operation, the power stage losses can be categorized as the transistor conduction losses and the inductor losses. The controller stage losses include losses associate with the microcontroller, operational amplifiers and demultiplexer switches.

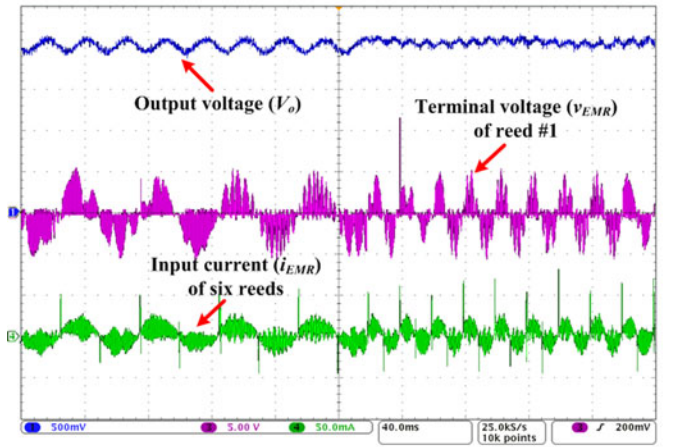
The conduction losses (P_{con}), including transistor turn-on conduction losses (P_Q), inductor copper losses (P_{Lr}) and diode forward power losses (P_{dr}), are represented as

$$P_{con} = P_Q + P_{Lr} + P_{dr} = \sum_{i=1}^{12} R_{Q,on}^{(i)} \cdot [I_{Q,rms}^{(i)}]^2 + R_{Lr} \cdot I_{Lr,rms}^2 + V_{dr,fw} \cdot I_{dr,avg} \quad (31)$$

where $R_{Q,on}^{(i)}$ is the drain-to-source on-state resistance of the transistors, $I_{Q,rms}^{(i)}$ is RMS value of the transistor current, R_{Lr} is the coil resistance of the resonant inductor, $I_{Lr,rms}$ is RMS value of the resonant inductor current, $V_{dr,fw}$ is the forward voltage drop of the output diode, and $I_{dr,avg}$ is the average value of the diode current. In addition, the inductor ferrite losses, com-



(a)



(b)

Fig. 11. Experimental waveforms of power amplifiers with step change: x-axis: 40 ms/div; y-axis: (a) $v_{emf} =$ from 1 V_{rms} to 2 V_{rms} ; Ch1 = output voltage (V_o), 1 V/div; Ch2 = terminal voltage (v_{EMR}) of reed #1, 5 V/div; Ch3 = input current (i_{EMR}) of six reeds, 50 mA/div; and (b) $f_{in} =$ from 20 to 50 Hz; Ch1 = output voltage (V_o), 0.5 V/div; Ch2 = terminal voltage (v_{EMR}) of reed #1, 5 V/div; Ch3 = input current (i_{EMR}) of six reeds, 50 mA/div.

TABLE III
POWER LOSS ESTIMATION OF PEI

Component	Power Loss	Loss Breakdown
<i>Power Stage</i>	2.21 mW	55.8%
Transistor ($P_Q^{(1) \sim (12)}$)	0.25 mW	6.3%
Diode (P_{dr})	0.96 mW	24.2%
Inductor coil (P_{Lr})	0.7 mW	17.7%
Inductor core (P_{fer})	0.3 mW	7.6%
<i>Controller</i>	1.75 mW	44.2%
Microcontroller	1.2 mW	30.3%
Opamp	0.126 mW	3.2%
Demultiplexer	0.18 mW	4.6%
Gating	0.2 mW	5.1%
Sensing resistor	0.04 mW	1.0%

posed of hysteresis loss (P_{hys}) and eddy current loss (P_{eddy}), can be expressed as

$$P_{fer} = P_{hys} + P_{eddy} = K_h \cdot f_s \cdot B_{max}^n + K_e \cdot f_s^2 \cdot B_{max}^2 \quad (32)$$

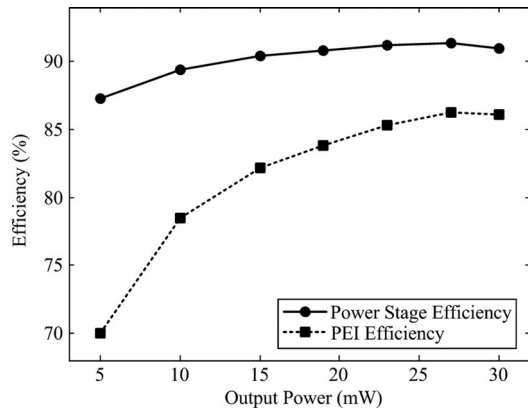


Fig. 12. Power conversion efficiency of the power stage and the entire PEI (including controller losses) at different output power.

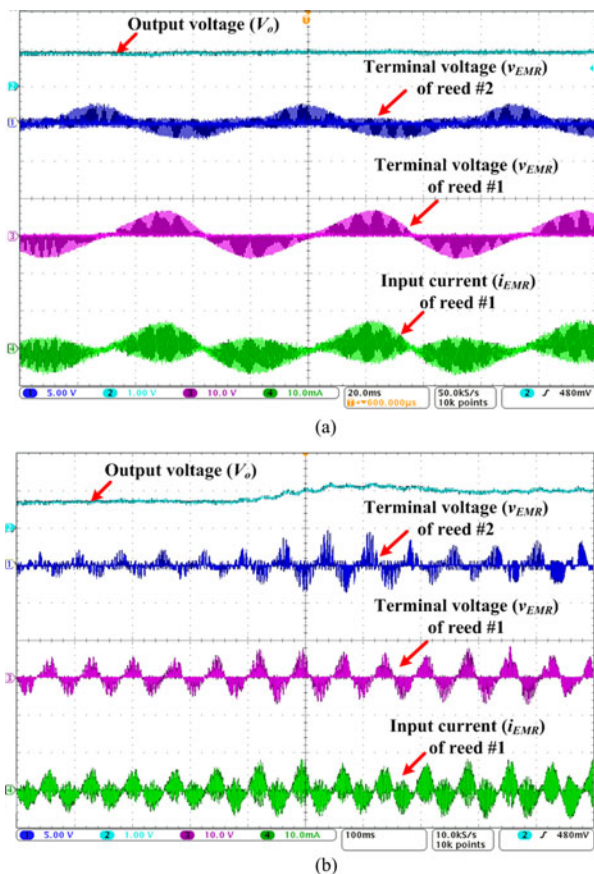


Fig. 13. Experimental waveforms of EMR generators: x-axis: (a) 20 ms/div; (b) 100 ms/div; y-axis: (a) constant wind speed; (b) wind speed step change; Ch1 = terminal voltage (v_{EMR}) of reed #2, 5 V/div; Ch2 = output voltage (V_o), 1 V/div; Ch3 = terminal voltage (v_{EMR}) of reed #1, 10 V/div; Ch4 = input current (i_{EMR}) of reed #1, 10 mA/div.

where K_h and K_e are constants depending on magnetic material, f_s is the switching frequency, B_{max} is the maximum flux density, and n is a constant between 1.6 and 2.0.

The total loss of the converter at 23-mW output power is 3.96 mW. Table III summarizes the loss breakdown associated with each component. According to this table, power stage loss is 2.21 mW (55.8% of total loss) and controller loss is 1.75

mW (44.2% of total loss). The power conversion efficiency at different output power levels is presented in Fig. 12.

To verify the circuit performance with EMR generators, the prototype is connected to the six-channel wind panel. The reeds are driven by a low-speed fan and vibrate around 15 Hz. In this case, each reed generates a unique EMF and power. The terminal voltages (v_{EMR}) of reed #1 and reed #2 and the input current (i_{EMR}) of reed #1 are measured, as depicted in Fig. 13. Very low power (as low as 1 mW) can be extracted at low cutoff wind speed (as low as 3 m/s). The converter, connected to a 1-k Ω resistor, is capable of converting low EMFs (as low as $0.5 V_{rms}$) of six individual generators to a common dc output voltage (as high as 1.2 V), which make it suitable to charge a 1.2-V rechargeable battery.

V. CONCLUSION

This paper introduces a multiinput bridgeless resonant ac–dc converter suitable for efficient, low-voltage, low-power, ac–dc power conversion of multiple electromagnetic generators. The multiinput single-stage topology is capable of directly converting independent, low-amplitude, alternative voltages of EMR inductive generators to a stepped-up dc output voltage with relatively high efficiency. Low-frequency alternating voltages of EMR generators are first converted into a high-frequency alternating voltage through an LC network and then rectified into a dc output voltage through a soft-switched diode. Optimal electrical impedance matching is achieved through proper LC network design and PFM control to scavenge maximum power of EMR generators. In addition, high-frequency soft-switching increases the potential of size miniaturization without suffering from switching losses. The converter performance is verified through a 5 cm \times 3 cm standalone prototype, which converts ac voltages of six-channel generators into a dc output voltage. A maximum PEI conversion efficiency of 86.3% is measured at 27-mW ac–dc power conversion. The topological concept, presented in this paper, can be adapted for rectification of any inductive voltage sources or electromagnetic energy-harvesting device.

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