

Automatic Resonant Frequency Tracking in Parallel LLC Boost DC–DC Converter

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Abstract—This paper proposes an automatic resonant frequency tracking scheme for resonant converters. A plant modeling approach is presented based on realistic ramp variation of resonant frequency due to slowly varying environmental conditions. The drift in resonant frequency is detected by observing the phase relationship of an electrical variable pair. A rigorous analysis is carried out to select the most suitable variable pair for phase comparison. The effects of circuit nonidealities on the proposed phase detection technique have been detailed. The proposed control technique has been implemented using low cost analog circuitry and is applied to a parallel LLC tank-based resonant boost dc–dc converter. Experimental results are presented on a 160-W prototype to validate the analytical predictions.

Index Terms—DC–DC converter, parallel LLC tank, phase comparator, resonant frequency, self-tuning.

I. INTRODUCTION

DEMAND for dc–dc converters for high voltage gain (A_v) is increasingly felt in many recent applications like high intensity discharge (HID) lamps in automobiles (12–120 V) [1], telecom supplies (48–380 V) [2], dc bus supply for grid-side converters in photovoltaic/fuel-cell/battery interfaces [3]–[7] and drive converters in electric/hybrid electric vehicles [8]–[12]. Additional demands on power density require that the source current drawn by these converters be continuous. Classical boost converters are inadequate for these applications since their A_v versus duty-ratio characteristics become concave in continuous conduction mode, given the inevitable presence of parasitic resistances. Cascaded boost converters [13], coupled inductor boost converters [14], and boost converters comprising voltage multiplier cells [15] provide high gain, but have high part count. The Z-source inverter [16] has restricted gain and efficiency after incorporating parasitic resistances. Moreover, discontinuity in its A_v characteristic around maximum gain region complicates the control scheme. Some of the aforementioned applications demand galvanic isolation, which allow the designer to achieve any high voltage gain by selecting transformer turns ratio. But transformers having large turns ratio suffer from high leakage inductances, which inevitably increase the voltage stress on switches [17]. A parallel LLC

resonant boost converter is reported in [18], which successfully provides high A_v along with galvanic isolation, which not only ensures reduction in voltage stress but also achieves soft switching.

In the conventional resonant converters, the switching frequency is used as a control variable to modulate the tank gain [19]–[24]. But if the tank is designed to provide very high gain, the gain characteristic necessarily has an extremely high curvature around the resonant frequency, which makes minute modulation of the switching frequency to achieve precise and steady voltage control practically unrealizable. Hence, to extract maximum gain, the converter is operated [18] at the tank resonant frequency, which, however, drifts due to tank parameter variation leading to reduction in A_v . This reduction could be prevented with an online strategy to tune the converter switching to match the current resonant frequency, which summarizes the motivation and scope of automatic resonant frequency tracking (ARFT) methods.

In the reported literature, frequency tracking has been achieved for parallel resonant converter [25], [26] and LCC [27] by sensing the tank input current followed by a phase-locked loop. But this is not applicable for isolated topologies since the tank input current no longer remains in phase with the switching signals, due to the transformer magnetizing current [28]. For isolated converters, a recent ARFT strategy [29] for a series LLC tank topology stipulates a two-step approach. First, the switching signals of the secondary side synchronous rectifier (SR) MOSFETs are adjusted to eliminate body diode conduction. Then, a pulsewidth-locked loop (PWLL) is used to equalize the pulsewidth of the primary and SR switches, which in turn ensures the frequency tuning. But this method, which exploits the switching behavior of an antiparallel diode–MOSFET pair, is not applicable for topologies having a diode bridge rectifier in secondary.

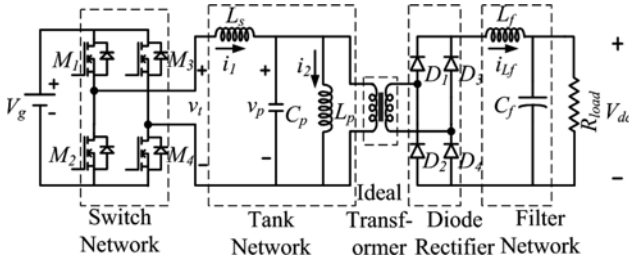
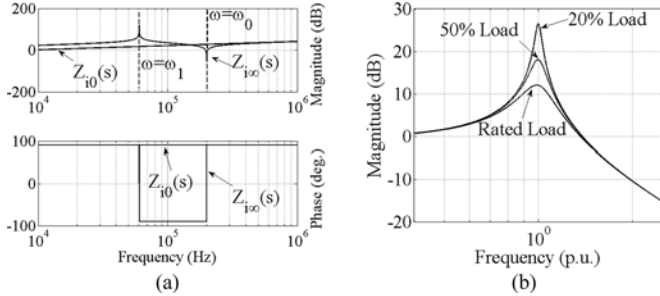
This paper presents a different approach for self-tuning, which is based on phase relationship between a pair of unique electrical variables in the resonant tank. Detailed analysis to decide the appropriate variable pair, based on justifiable criteria, is provided. The rest of this paper is organized as follows. Section II reviews the parallel LLC tank-based boost topology proposed in [18] and describes the output filter inductor design. Section III details the design of the ARFT controller, while Section IV presents an analytical approach to choose an appropriate variable pair to detect the frequency drift. Section IV analyses the effect of tank nonidealities on the phase relationship of the chosen variable pair. Analog implementation of the proposed control technique is described in Section V. Numerical simulation results are presented

Manuscript received February 10, 2014; revised May 17, 2014; accepted July 21, 2014. Date of publication July 29, 2014; date of current version February 13, 2015. Recommended for publication by Associate Editor C. A. Canesin.

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Digital Object Identifier 10.1109/TPEL.2014.2344021

Fig. 1. Parallel *LLC* resonant boost dc-dc converter topology.Fig. 2. (a) $Z_{i0}(s)$ and $Z_{i\infty}(s)$. (b) Voltage gain versus p.u. switching frequency.

in Section VI and augmented with experimental results on a 160-W prototype to validate the analysis.

II. PARALLEL *LLC* RESONANT BOOST DC-DC CONVERTER TOPOLOGY

The dc-dc converter topology proposed in [18], depicted in Fig. 1, is basically a high-frequency *LLC* resonant converter. The resonating elements comprise the series inductor L_s transformer magnetizing inductance L_p and the parallel capacitor C_p . The tank is excited through a full-bridge circuit and its output feeds the transformer primary. Rectification of the secondary voltage is done by a diode bridge rectifier followed by a second-order filter for double-frequency attenuation.

A. Properties of the Tank

Fig. 2(a) shows the transfer characteristic of the short-circuit input impedance $Z_{i0}(s)$ and the open-circuit input impedance $Z_{i\infty}(s)$ of the *LLC* tank network given by the following equations [18]:

$$\begin{aligned} Z_{i0}(s) &= sL_s \\ Z_{i\infty}(s) &= sL_s + \left(\frac{1}{sC_p} \parallel sL_p \right) \\ &= s(L_s + L_p) \cdot \frac{1 + s^2/\omega_0^2}{1 + s^2/\omega_1^2} \end{aligned} \quad (1)$$

where

$$\omega_1 = 1/\sqrt{L_p C_p}, \quad \omega_0 = 1/\sqrt{(L_s \parallel L_p) C_p}. \quad (2)$$

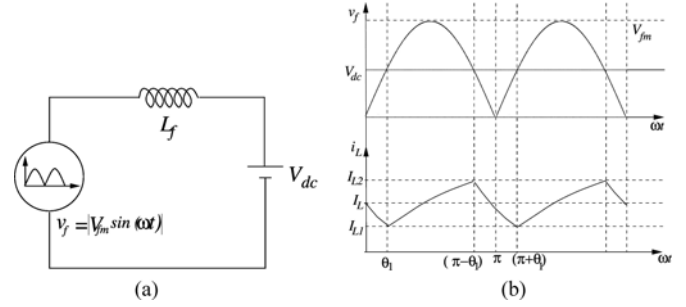


Fig. 3. (a) Diode rectifier circuit. (b) Inductor current waveform.

The tank has two corner frequencies: ω_1 , due to L_p and C_p , and ω_0 , due to $(L_s \parallel L_p)$ and C_p . From Fig. 2(a), it is clear that for $\omega > \omega_0$, both $Z_{i0}(s)$ and $Z_{i\infty}(s)$ are inductive, which implies inductive loading of the switch network, thus ensuring load independent ZVS [30]. This is an advantage of the voltage stiff *LLC* tank topology compared to the conventional current stiff *LLC* topology [31]–[36]. The maximum voltage gain of the tank at $\omega = \omega_0$ is derived [18] as

$$\left| \frac{V_p(j\omega)}{V_i(j\omega)} \right|_{\omega=\omega_0} = \left[\frac{L_p}{L_s + L_p} \right] R \sqrt{\frac{C_p}{L_s \parallel L_p}}. \quad (3)$$

where R is the equivalent load resistance seen from the transformer primary given by

$$R = \pi^2 R_{load} / 8N^2 \quad (4)$$

where N is the transformer turns ratio. Variations of voltage gain with switching frequency at different load conditions are shown in Fig. 2(b). To achieve peak gain, the converter should always operate at a switching frequency

$$f_s = f_0 = \omega_0 / 2\pi. \quad (5)$$

B. Filter Inductor Design

While deriving (4), small ripple approximation [30] was applied to the filter inductor current and its validity must be ensured over the entire load range, especially at low-load conditions. Fig. 3(a) shows the diode rectifier output v_f , the filter inductor L_f , and the dc bus voltage V_{dc} . V_{fm} denotes the peak value of v_f . V_{dc} is assumed to be constant and ripple free. The typical waveform of inductor current, i_L , is shown in Fig. 3(b). I_{L2} and I_{L1} are the maximum and minimum values of i_L , respectively, whereas I_L denotes the average value.

From Fig. 3(b), it is obvious that

$$V_{dc} = 2V_{fm} / \pi = V_{fm} \sin \theta_1 \Rightarrow \sin \theta_1 = 2/\pi. \quad (6)$$

During the interval, $\theta_1 \leq \omega t \leq \pi$

$$\omega L_f \frac{di_L}{d(\omega t)} = V_{fm} \sin(\omega t) - V_{dc}. \quad (7)$$

Solving (7) and noting that $i_L = I_{L1}$ at $\omega t = \theta_1$ and $i_L = I_{L2}$ at $\omega t = (\pi - \theta_1)$, the per unit (p.u.) peak to peak inductor current ripple is derived for minimum load condition as

$$(I_{L2} - I_{L1}) / I_L = KV_{dc}^2 / \omega L_f P_{0min} \quad (8)$$

TABLE I
COMPARISON OF INDUCTOR POWER DENSITY WITH OTHER TOPOLOGIES

Topology	L_f (μH)	P_0 (kW)	I_L (A)	$P_0/(L_f I_L^2)$ (W/HA ²)
Parallel LLC Boost	10,000	0.160	0.357	125,540
ZVS-ZCS				
Full Bridge [37]	12	2	41.67	95,984
Natural ZVS				
Bidirectional [38]	5	1.6	133.3	18,000
Isolated Boost				
Full Bridge [39]	2.5	3	350	9,796

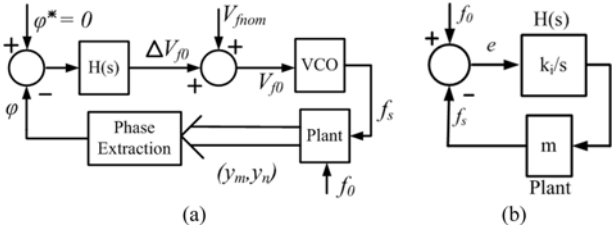


Fig. 4. (a) Proposed Frequency Tracking loop. (b) Simplified block diagram of frequency tracking loop.

where

$$K = \left(\pi \sqrt{1 - (4/\pi^2)} + 2 \sin^{-1}(2/\pi) - \pi \right). \quad (9)$$

and $P_{0\min}$ is the minimum value of output power (P_0). To restrain this ripple to less than 40% of the average output current at 20% load, the value of L_f has been evaluated from (8) and (9) and is listed in Table I. A direct indication of the inductor power is obtained by comparing $P_0/(L_f I_L^2)$ [30] for some sample topologies [37]–[39], which proves that the filter inductor of the parallel LLC boost converter offers the optimal design.

III. PROPOSED ARFT SCHEME

Since the drift in the tank resonant frequency is mainly due to change in ambient conditions, the ARFT scheme is proposed as a slow outer-loop nesting an inner fast voltage control loop. Thus, the bandwidth of this loop is designed at least an order lower than that of the inner loop. With this consideration, the plant, comprising the switch network and tank, is adequately modeled as

$$[i_1 \ v_p \ i_2]^T = \mathbf{G}_p v_t, \quad \mathbf{G}_p = [A_1 e^{j\phi_1} \ A_2 e^{j\phi_2} \ A_3 e^{j\phi_3}]^T \quad (10)$$

where A_k and ϕ_k , $k = 1, 2, 3$ are not functions of time but exclusively depend on the tank excitation frequency, f_s . The plant parameters, A_k and ϕ_k , are expressed in terms of LLC circuit parameters and have been presented in Appendix B. Assuming realization of a reasonably accurate phase detection network, which, when operating on any two plant variables y_m and y_n , extracts

$$\Delta\phi = \arg(y_m \cdot y_n^*) \quad (11)$$

a frequency tracking loop is proposed as shown in Fig. 4(a). The proposed control loop is further simplified and depicted in

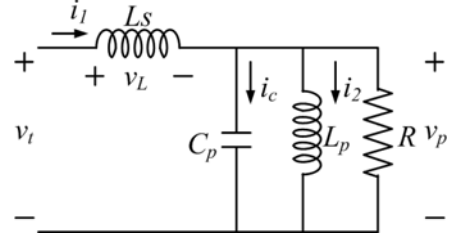


Fig. 5. Parallel LLC tank topology.

Fig. 4(b). Since the ARFT loop is designed for operation close to the exact resonant frequency, the plant is modeled using a first-order approximation as follows:

$$\Delta\phi = \left(\frac{\partial\phi}{\partial\omega} \bigg|_{\omega_0} \right) \Delta\omega = m\Delta\omega. \quad (12)$$

Controller design is based on ensuring arbitrary steady-state tracking error for a realistic ramp variation in the resonant frequency f_0 , given by

$$f_0(t) = F_{0i}(1 + \alpha t) \quad (13)$$

where F_{0i} is the initial value of $f_0(t)$ and α the ramp slope. An integral controller $H(s)$ is selected and the integral constant k_i decides the steady-state error e_{ss} as follows:

$$E(s) = F_0(s) - F_s(s) = F_{0i}(s + \alpha)/s(s + mk_i). \quad (14)$$

Using the final value theorem, the steady-state error is

$$e_{ss} = \lim_{s \rightarrow 0} sE(s) = (F_{0i}\alpha)/(mk_i). \quad (15)$$

Arbitrary steady-state error e_{ss} is ensured by the integral constant k_i given by

$$k_i = \alpha/m e_{sspu}, \quad e_{sspu} = e_{ss}/F_{0i}. \quad (16)$$

IV. CHOICE OF A VARIABLE PAIR TO DETECT DETUNING

Fig. 5 shows the circuit diagram of the resonant tank along with the relevant state variables. Denoting the state vector $\mathbf{X} = [i_1 \ v_p \ i_2]^T$ and its time derivative as $\dot{\mathbf{X}} = [v_L \ i_c \ v_p]^T$, selection of the suitable variable pair begins with the definition of the set \mathbf{A} as well as its Cartesian square $\mathbf{\Pi}$ as follows:

$$\mathbf{A} = \{\mathbf{X}^T, \dot{\mathbf{X}}^T, v_t\} \quad \text{and} \quad \mathbf{\Pi} = \mathbf{A} \times \mathbf{A}. \quad (17)$$

Assuming that the frequency response of the tank network has negligible sidebands, a binary relation Φ is defined on \mathbf{A} as follows:

$$\Phi \subset \mathbf{\Pi} \quad \text{and} \quad (18)$$

$$\Phi \{ \langle x_m, x_n \rangle : \arg[x_m(j\omega)/x_n(j\omega)] \in (\mathbf{B1} \cup \mathbf{B2}) \}. \quad (19)$$

The membership of sets $\mathbf{B1}$ and $\mathbf{B2}$ and its implications are explained as follows.

- $\mathbf{B1}$ is the set with no element a constant. (*Nontriviality*) Trivial 2-tuples are of the form $\langle x, x \rangle$, $\langle x, \dot{x} \rangle$ or $\langle \dot{x}, x \rangle$.

TABLE II
TRANSFER FUNCTIONS OF RELEVANT VARIABLE PAIRS AND THEIR PHASE RELATIONSHIPS

$G_k(s)$	C_{mn}	$N(s)$	$D(s)$	$\tan(\phi)$	$\frac{d\phi}{d\omega_{pu}} _{\omega_{pu}=1}$
$V_p(s)/V_t(s)$	$a/(1+a)$	1	$F_1(s)$	$-\omega_\gamma/\omega_\alpha$	$-2Q_0 = -m_2$
$V_L(s)/V_t(s)$	$1/(1+a)$	$F_2(s)$	$F_1(s)$	$a\omega_\gamma/(\omega_\alpha\omega_\beta + \omega_\gamma^2(1+a))$	$-a\rho(Q_0/\delta) = -m_1$
$V_L(s)/V_p(s)$	$1/a$	$F_2(s)$	1	$\omega_\gamma(1+a)/\omega_\beta$	$(1+a)(2+a)/(Q_0\delta) = m_3$

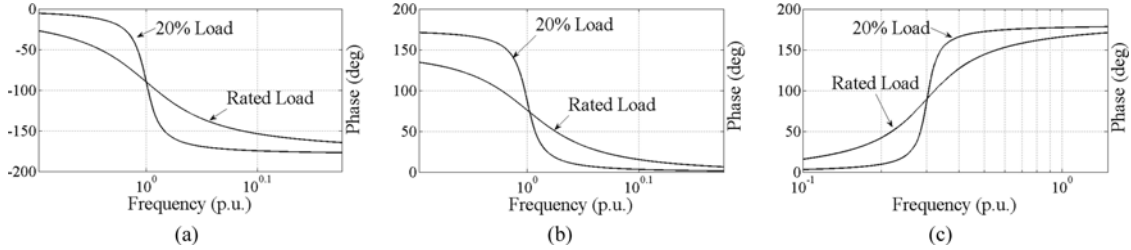


Fig. 6. Phase plots of all feasible variable pairs.

2) **B2** is the set with no two elements, ϕ_m, ϕ_n , satisfying $\phi_m = \pm\phi_n \pm C$ (any constant). (*Uniqueness*)

Nonunique pairs of 2-tuples are of these forms: $\langle x_m, x_n \rangle$ and $\langle x_n, x_m \rangle$; $\langle x_m, x_n \rangle$ and $\langle \dot{x}_m, \dot{x}_n \rangle$; $\langle x_m, x_n \rangle$ and $\langle x_m, \dot{x}_n \rangle$; and $\langle x_m, x_n \rangle$ and $\langle x_m, \ddot{x}_n \rangle$.

Using these rules, the final solution set Φ is seen to comprise only three pairs, as indicated in Table II. In case of nonunique pair of 2-tuples, $\langle x_{m1}, x_{n1} \rangle$ and $\langle x_{m2}, x_{n2} \rangle$, which are both non-trivial, preference is given to voltage variables as current measurement would introduce additional impedances in the power circuit.

For each 2-tuple, $\langle x_m, x_n \rangle$ in Φ the corresponding transfer function

$$\frac{x_n(s)}{x_m(s)} \triangleq G_k = C_{mn} \frac{N(s_0)}{D(s_0)}, \quad s_0 = s/\omega_0 \quad (20)$$

is also listed in Table II, where C_{mn} is a constant, and $N(s_0)$ and $D(s_0)$ are the numerator and denominator polynomials, respectively, in the Laplace operator s normalized by the resonant frequency ω_0 . Furthermore, the following definitions were used for concise expression of the quantities listed in Table II:

$$Q_0 = R\sqrt{(1+a)(C_p/L_p)}, \quad a = L_p/L_s \quad (21)$$

$$F_1(s) = s_0^2 + (s_0/Q_0) + 1 \quad (22)$$

$$F_2(s) = s_0^2(1+a) + (s_0/Q_0)(1+a) + 1. \quad (23)$$

It is observed that the modulus of these transfer functions are typically either symmetric or load-dependent around resonant frequency. Hence, the sense of frequency error is not suitably reflected in the gain characteristics. It is for this reason, attention is given to the phase, ϕ ($=\arg[G_k(j\omega)]$), as plotted in Fig. 6 at the two extremes of the load range considered. The expressions of $\tan(\phi)$ are listed in the fifth column of Table II, where

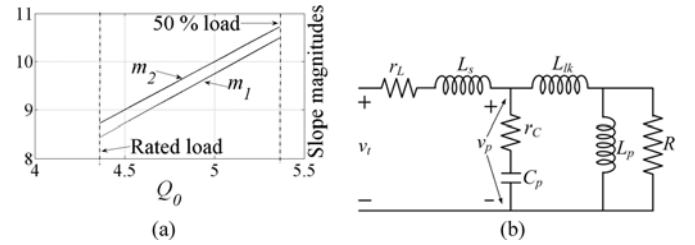


Fig. 7. (a) Variation of phase curve slope magnitudes with Q_0 . (b) Nonideal parallel LLC tank circuit.

$$\omega_{pu} = \omega/\omega_0, \quad \omega_\alpha = 1 - \omega_{pu}^2 \quad (24)$$

$$\omega_\beta = 1 - (1+a)\omega_{pu}^2, \quad \omega_\gamma = \omega_{pu}/Q_0. \quad (25)$$

In the second step, a localized search for the best candidate transfer function is carried out from the set II. The chief criterion used is the sensitivity of the phase ϕ to the excitation frequency of the tank input, in the vicinity of the resonance frequency ω_0 and is expressed by modifying (12) as

$$m = \left. \frac{d\phi}{d\omega_{pu}} \right|_{\omega_{pu}=1}. \quad (26)$$

These are listed in Table II using the following definitions:

$$\delta = a^2 + ((1+a)/Q_0)^2, \quad \rho = ((1+a)/Q_0^2) + 2a. \quad (27)$$

Visual inspection of Fig. 6(a)–(c) clearly shows that m_3 is much lower than m_1 or m_2 , hence, it is ignored in subsequent investigation. Also evident is that the sensitivity has an inverse relationship with load, hence, its variation is probed in the range 50%–100% of rated load and shown in Fig. 7(a). Clearly, the pair $\langle v_t, v_p \rangle$ has the greater sensitivity and is, therefore, chosen for ARFT. Substituting $m = m_2 = 2Q_0$ in (16), k_i is selected to limit the steady-state error (e_{sspu}) to 0.0004 p.u., for a ramp rate of 0.05 p.u. frequency drift in 20 s, at rated load and the result is tabulated in Table IV.

TABLE IV
SYSTEM SPECIFICATIONS

L_s	L_p	C_p	f_s	N	C_f	V_g	V_{dc}	k_i
$20 \mu\text{H}$	$200 \mu\text{H}$	33 nF	200 kHz	4	$16.5 \mu\text{F}$	35 V	400 V	0.6944

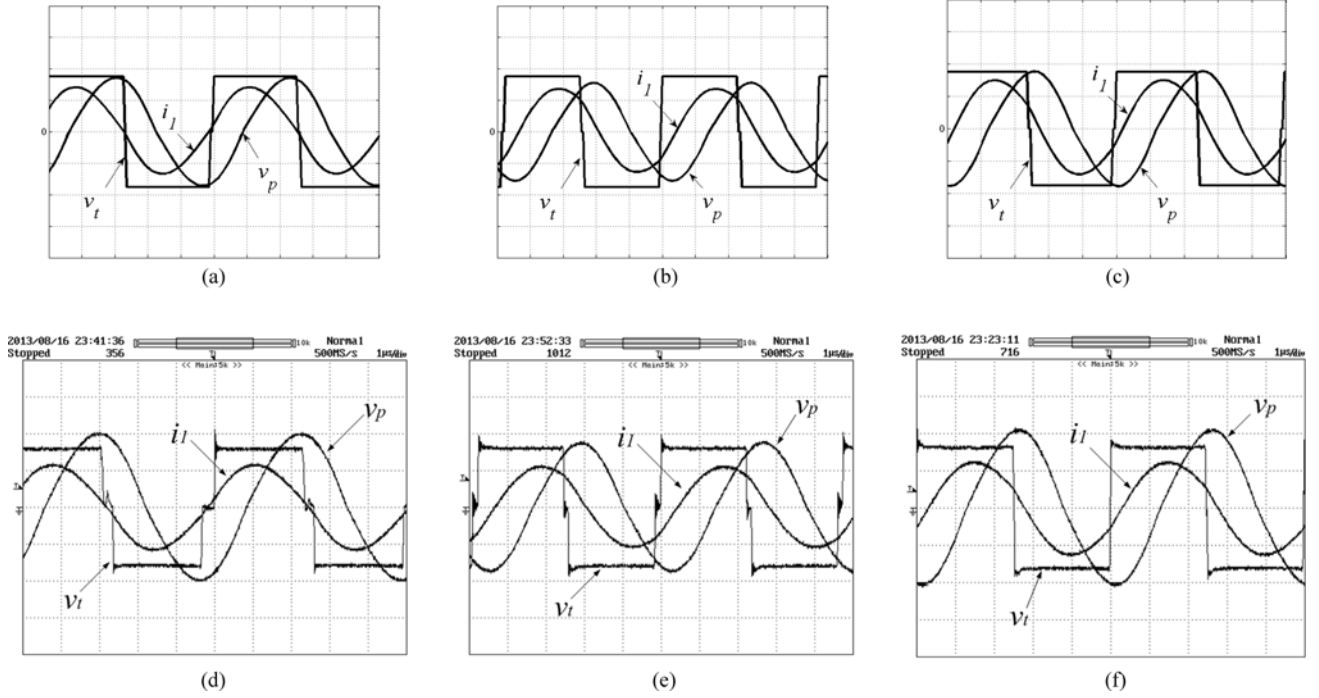


Fig. 10. Performance of frequency tracking controller. Simulation results: (a) open loop ($f_s = 190 \text{ kHz}$ and $f_0 = 200 \text{ kHz}$), (b) open loop ($f_s = 210 \text{ kHz}$ and $f_0 = 200 \text{ kHz}$), (c) closed loop ($f_s = f_0 = 200 \text{ kHz}$). Experimental results: (d) open loop ($f_s = 190 \text{ kHz}$ and $f_0 = 200 \text{ kHz}$), (e) open loop ($f_s = 210 \text{ kHz}$ and $f_0 = 200 \text{ kHz}$), (f) closed loop ($f_s = f_0 = 200 \text{ kHz}$). Scale: v_t (20 V/div), i_1 (5 A/div), v_p (100 V/div); X-axis: (Time – 1 μs /div).

$V_{f_{nom}}$. This configuration ensures $V_\phi = V_\phi^* = 0$ in closed loop, and thus, $f_s = f_0$ is ensured. Practical realization of the proposed technique requires a small voltage V_d to be added with $(V_m - V_c)/2$, which is

$$V_d = t_{cor} \times V_m (T_s/2) \quad (30)$$

where

$$t_{cor} = t_{GD} + t_p + t_{d(off)} + t_f \quad (31)$$

$$-(t_{comp} + t_{RED} + t_{width}). \quad (32)$$

Gate driver delay and propagation delay of dead-time generation circuit are denoted by t_{GD} and t_p , respectively. Delays associated with MOSFETs are turn-off delay $t_{d(off)}$ and fall time t_f . t_{comp} and t_{RED} are propagation delays of comparator and rising edge detector used for phase comparison, respectively. Pulse width of “Read” signal is denoted by t_{width} .

VI. RESULTS AND DISCUSSIONS

A model of parallel *LLC* resonant converter and the proposed control scheme is developed in MATLAB Simulink. All system data are listed in Table IV. The experimental prototype to implement the complete analog controller and the power circuit is

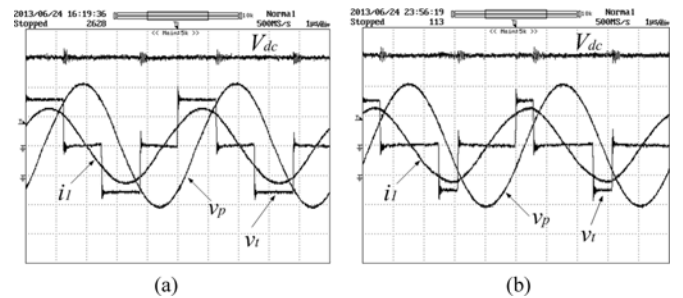


Fig. 11. Closed-loop performance of the frequency tracking controller under different load conditions. Experimental results: (a) 50% load, (b) 20% load. Scale: v_t (20 V/div), i_1 (5 A/div), v_p (100 V/div), V_{dc} (100 V/div); X-axis: (Time – 1 μs /div).

fabricated on an impedance-controlled circuit board (PCB) using planar magnetics. The performance of the ARFT controller is shown in Figs. 10–13. The resonant frequency f_0 is at 200 kHz and the converter is operating near rated load with zero phase overlap (see Fig. 10). When switching frequency, $f_s = 190 \text{ kHz}$, i_1 leads v_t and v_p lags v_t by an angle less than 90° . Similarly, when $f_s = 210 \text{ kHz}$, i_1 lags v_t and v_p lags v_t by an angle greater than 90° . Whereas i_1 lags v_t and v_p lags v_t exactly

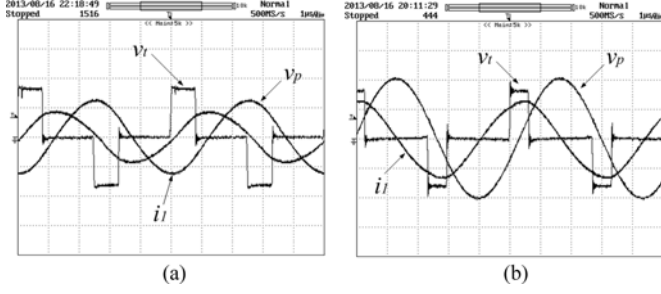


Fig. 12. Performance of the frequency tracking controller. Experimental results: (a) open loop ($f_s = 200$ kHz and $f_0 = 185$ kHz), (b) closed loop ($f_s = f_0 = 185$ kHz). Scale: v_t (20 V/div), i_1 (5 A/div), v_p (100 V/div); X-axis: (Time – 1 μ s/div).

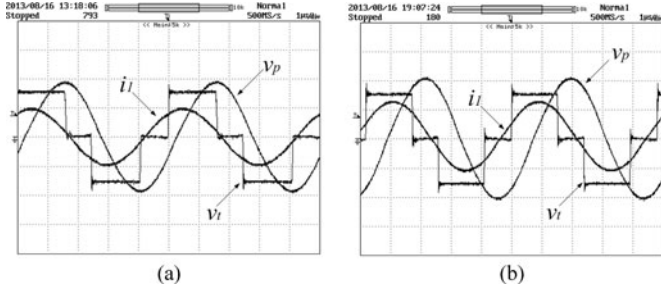


Fig. 13. Performance of the frequency tracking controller. Experimental results: (a) open loop ($f_s = 200$ kHz and $f_0 = 208$ kHz), (b) closed loop ($f_s = f_0 = 208$ kHz). Scale: v_t (20 V/div), i_1 (5 A/div), v_p (100 V/div); X-axis: (Time – 1 μ s/div).

by 90° during closed-loop operation, which supports the analysis of the previous section. Closed-loop performance of the ARFT controller under different load conditions is depicted in Fig. 11, which also shows the output dc bus voltage V_{dc} . Now, the resonant frequency is changed to 185 kHz by increasing the value of the capacitor C_p and the converter is operated at 30-W load with an overlap angle of 150° . Open-loop and closed-loop results are shown in Fig. 12. Similarly, open-loop and closed-loop results for resonant frequency 208 kHz is shown in Fig. 13 when the converter operates at 80-W load with an overlap angle of 75° . To verify the tracking performance of the ARFT controller, a variable inductor is emulated. A component of the series inductor L_s is varied almost linearly so that the net series inductance is expressed as

$$L_s(t) = L_{si}(1 + \beta t) \quad (33)$$

where L_{si} is the initial value of L_s and β the ramp slope. The resonant frequency variation under this condition is derived from (2), (5), and (33) assuming $(L_{si} + L_p) \gg L_{si}\beta t$ and expressed as

$$f_0(t) = F_{0i}(1 - \beta t/2) \quad (34)$$

where

$$F_{0i} = 1/2\pi\sqrt{L_{si}L_pC_p/(L_{si} + L_p)}. \quad (35)$$

Defining

$$\Delta f_0(t) = f_{nom} - f_0(t), \quad \Delta F_{0i} = f_{nom} - F_{0i} \quad (36)$$

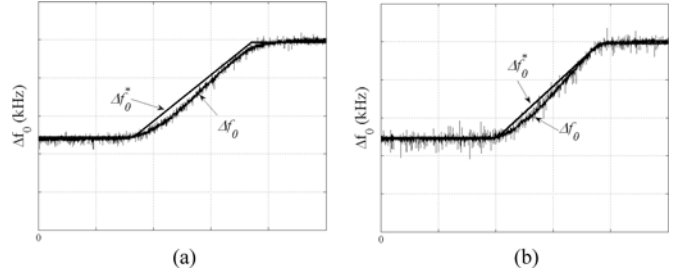


Fig. 14. Performance of the frequency tracking controller under ramp variation in resonant frequency. Experimental results: (a) rated load, (b) 20% load. Scale: Δf_0 (3.58 kHz/div); X-axis: (Time – 10 s/div).

TABLE V
P.U. STEADY-STATE FREQUENCY ERROR

Load condition	L_{sf} (μ H)	L_{si} (μ H)	t_r (s)	e_{sspu} (analytical)	e_{sspu} (experimental)
Rated	25	22.7	20.5	4.1×10^{-4}	5×10^{-4}
20% load	25	22.7	17.8	8.8×10^{-5}	9.3×10^{-5}

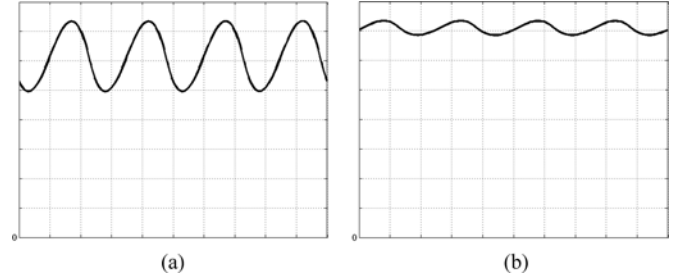


Fig. 15. Filter inductor current (a) at 20% load (0.01 A/div), (b) at rated load (0.05 A/div); X-axis: (Time – 1 μ s/div).

where f_{nom} is the predefined base value of the resonant frequency. Equation (34) is rearranged as

$$\Delta f_0(t) = \Delta F_{0i} + \beta F_{0i}t/2. \quad (37)$$

Following the same steps as in (14) and (15), the p.u. steady-state frequency error, e_{sspu} , is derived as

$$e_{sspu} = \beta/(2m_2k_i). \quad (38)$$

The tracking performance of the ARFT controller is shown in Fig. 14 for two extreme load conditions, where Δf_0^* denotes the reference. Analytical and experimental values of e_{sspu} is given in Table V, where L_{sf} denotes the final value of L_s and t_r is the duration of ramp. Close agreement of these results validates the controller design.

The filter inductor currents at rated load (160 W) and at 20% load conditions are shown in Fig. 15. At low load, peak-to-peak filter inductor current ripple is 35% of the average current, which is sufficiently low to validate the approximation of ac resistance across the tank capacitor. Fig. 16 depicts the tank input current with and without the dc elimination loop. It is clear that this loop ensures zero-mean tank input current and removes any chance of the transformer saturation.

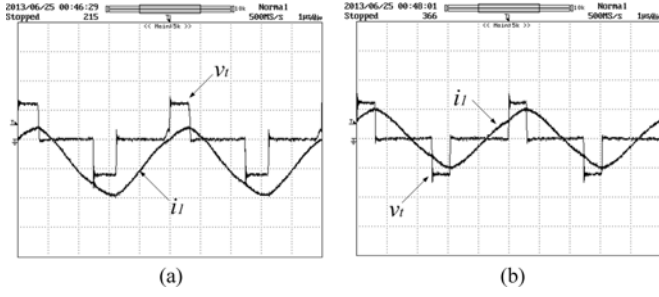


Fig. 16. Performance of dc current elimination loop. Experimental results: (a) open loop, (b) closed loop. Scale: v_t (20 V/div), i_1 (2 A/div); X-axis: (Time – 1 μ s/div).

VII. CONCLUSION

This paper presents the detailed analysis, design, and realization of a self-tuning method for resonant converters, with specific consideration of a three-element, parallel *LLC* circuit. The governing equations of the converter are presented along with its voltage gain and associated resonance/antiresonance modes. A closed-loop ARFT algorithm is proposed to ensure self-tuning under parameter variations due to environmental conditions. The relevant plant model is derived and necessity of an accurate phase detector is noted. Selection of the most suitable input variable pair for the phase detector is carried out through rigorous analysis and the achievable resolution of the phase detector is derived for a realistic range of circuit non-idealities. It is emphasized that this analysis is presented in a general form and applicable for any other resonant converter topology. Analog circuit realization of the phase detector is presented in appropriate detail. Analytical predictions regarding the performance of the ARFT scheme are experimentally validated by drifting the tank resonant frequency from its initial designed value. This paper, thus, establishes a tractable approach toward solving the perennial problem of resonance drift in resonant converters.

APPENDIX A

COEFFICIENTS OF $V_p(s)/V_t(s)$

$$\begin{aligned}
 a_3 &= a\omega_0 r_{C_{pu}} L_{lk_{pu}} \\
 a_2 &= a(\omega_0 L_{lk_{pu}} + \eta r_{C_{pu}} Q_0 \omega_\zeta) \\
 a_1 &= \eta Q_0 \omega_\zeta \\
 b_4 &= \omega_0^3 L_p C_p L_{lk_{pu}} \\
 b_3 &= \omega_0(\omega_0^2 L_p C_p L_{lk_{pu}} r_{L_{pu}} \\
 &\quad + a L_{lk_{pu}} r_{C_{pu}} + r_{C_{pu}} + \omega_0 \eta Q_0 / \omega_\sigma) \\
 b_2 &= \omega_0(1 + r_{C_{pu}} r_{L_{pu}} + a L_{lk_{pu}}) + \omega_0^2 \eta Q_0 r_{L_{pu}} / \omega_\sigma \\
 &\quad + Q_0 r_{C_{pu}} \omega_\zeta (1 + a\eta) \\
 b_1 &= \omega_0 r_{L_{pu}} + Q_0 \omega_\zeta (1 + r_{C_{pu}} r_{L_{pu}} + a\eta) \\
 b_0 &= r_{L_{pu}} Q_0 \omega_\zeta.
 \end{aligned}$$

where

$$\begin{aligned}
 L_{lk_{pu}} &= L_{lk} / L_p, \quad r_{L_{pu}} = r_L / (\omega_0 L_s), \quad r_{C_{pu}} = \omega_0 C_p r_C \\
 \eta &= 1 + L_{lk_{pu}}, \quad \omega_\sigma = \sqrt{(1+a) / L_p C_p} \\
 \omega_\zeta &= 1 / \sqrt{(1+a) L_p C_p}.
 \end{aligned}$$

APPENDIX B PLANT PARAMETERS

TABLE VI

k	A_k	$\tan(\phi_k)$
1	$\sqrt{a L_s C_p (\psi_2^2 + \psi_3^2)} / (\omega_{pu} \psi_1 \sqrt{1+a})$	$-\psi_3 / \psi_2$
2	$a / (\sqrt{\psi_1} (a+1))$	$-\omega_\gamma / \omega_\alpha$
3	$\sqrt{a C_p} / (\omega_{pu} (1+a) \sqrt{L_s} (1+a) \psi_1)$	$\omega_\alpha / \omega_\gamma$

where

$$\begin{aligned}
 \psi_1 &= \omega_\gamma^2 + \omega_\alpha^2 \\
 \psi_2 &= (\omega_{pu} \omega_\alpha (1+a) / Q_0) - \omega_\beta \omega_\gamma \quad (99)
 \end{aligned}$$

$$\psi_3 = \omega_\alpha \omega_\beta + (\omega_{pu} \omega_\gamma (1+a) / Q_0). \quad (100)$$

ACKNOWLEDGMENT

The authors would like to acknowledge Mr. A. K. Basu and Mr. Nandkishore for their support during development of the experimental hardware.

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